

SEMiX® 5

Trench IGBT Modules

SEMiX405GARL07E3

Features

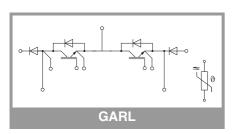
- Solderless assembling solution with PressFIT signal pins and screw power terminals
- IGBT Trench Gate Technology
- V_{CE(sat)} with positive temperature coefficient
- · Low inductance case
- Reliable mechanical design with injection moulded terminals and reliable internal connections
- UL recognized file no. E63532
- NTC temperature sensor inside

Typical Applications*

- UPS
- 3 Level Inverters

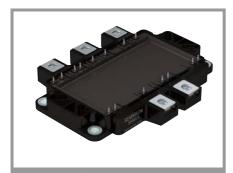
Remarks

- Case temperature limited to $T_C=125^\circ$ max.
- Product reliability results are valid for T_{jop} =150°C
- · Dynamic data are estimated
- For storage and case temperature with TIM see document "TP(HALA P8) SEMiX 5p"



Absolute Maximum Ratings							
Symbol	Conditions		Values	Unit			
IGBT				1			
V _{CES}	T _j = 25 °C		650	V			
Ic	T _i = 175 °C	T _c = 25 °C	457	Α			
	11 - 173 0	T _c = 80 °C	343	Α			
I _{Cnom}			400	Α			
I _{CRM}			1200	Α			
V_{GES}			-20 20	V			
t _{psc}	$V_{CC} = 360 \text{ V}$ $V_{GE} \le 15 \text{ V}$ $V_{CES} \le 650 \text{ V}$	T _j = 150 °C	6	μs			
Tj			-40 175	°C			
Inverse di	ode						
V_{RRM}	T _j = 25 °C		650	V			
I _F	T _i = 175 °C	T _c = 25 °C	86	Α			
	1, = 173 0	T _c = 80 °C	64	Α			
I _{Fnom}			50	Α			
I _{FRM}			100	Α			
I _{FSM}	$t_p = 10 \text{ ms, sin } 180^{\circ}, T_j = 25 {}^{\circ}\text{C}$		550	Α			
Tj			-40 175	°C			
Freewhee	ling diode						
V_{RRM}	T _j = 25 °C		650	V			
I _F	T _i = 175 °C	T _c = 25 °C	484	Α			
	1,-175 0	T _c = 80 °C	353	Α			
I _{Fnom}			400	Α			
I _{FRM}			800	Α			
I _{FSM}	$t_p = 10 \text{ ms, sin } 180^{\circ}, T_j = 25 ^{\circ}\text{C}$		2646	Α			
T _j			-40 175	°C			
Module							
I _{t(RMS)}			450	Α			
T _{stg}	module without TIN	Λ	-40 125	°C			
V _{isol}	AC sinus 50Hz, t =	1 min	4000	V			

Characte	eristics					
Symbol	Conditions		min.	typ.	max.	Unit
IGBT	•		•			
V _{CE(sat)}	$\begin{array}{c} I_{C} = 400 \text{ A} \\ V_{GE} = 15 \text{ V} \\ \text{chiplevel} \end{array}$	T _j = 25 °C		1.45	1.90	V
		T _j = 150 °C		1.70	2.10	V
V_{CE0}	chiplevel	T _j = 25 °C		0.90	1.00	V
		T _j = 150 °C		0.82	0.90	V
r _{CE}	V _{GE} = 15 V chiplevel	T _j = 25 °C		1.38	2.3	mΩ
		T _j = 150 °C		2.2	3.0	mΩ
V _{GE(th)}	$V_{GE}=V_{CE}$, $I_{C}=6.4$ mA		5.1	5.8	6.4	V
I _{CES}	V _{GE} = 0 V V _{CE} = 650 V	T _j = 25 °C		0.12	0.3	mA
		T _j = 150 °C		-		mA
C _{ies}	V _{CE} = 25 V V _{GE} = 0 V	f = 1 MHz		24.7		nF
Coes		f = 1 MHz		1.54		nF
C _{res}		f = 1 MHz		0.73		nF
Q _G	V _{GE} = - 15 V+ 15 V			5139		nC
R _{Gint}	T _j = 25 °C			1.0		Ω



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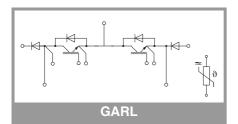
Typical Applications*

- UPS
- 3 Level Inverters

Remarks

- Case temperature limited to T_C=125° max.
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Characte	eristics					
Symbol	Conditions		min.	typ.	max.	Unit
IGBT						
t _{d(on)}	V _{CC} = 300 V	T _j = 150 °C		220		ns
t _r	I _C = 400 A	T _j = 150 °C		220		
Eon	$V_{GE} = +15/-15 \text{ V}$ $R_{G \text{ on}} = 10 \Omega$	T _j = 150 °C		27.91		
t _{d(off)}	$R_{G \text{ off}} = 10 \Omega$	T _j = 150 °C		1120		
t _f	$di/dt_{on} = 2038 \text{ A/}\mu\text{s}$ $di/dt_{off} = 3960 \text{ A/}\mu\text{s}$	T _j = 150 °C		103		ns
E _{off}		T _j = 150 °C		27.89		mJ
R _{th(j-c)}	per IGBT				0.14	K/W
R _{th(c-s)}	per IGBT (λgrease=0.81 W/mK, thickness 50-100μm)			0.06		K/W
Inverse d	 iode					
$V_F = V_{EC}$	I _F = 50 A	T _i = 25 °C		1.37	1.73	V
	V _{GE} = 0 V chiplevel	T _i = 150 °C		1.35	1.72	V
V _{F0}	chiplevel	T _i = 25 °C		1.04	1.24	V
-10		T _i = 150 °C		0.85	0.99	V
r _F	chiplevel	T _i = 25 °C		6.7	9.8	mΩ
		T _i = 150 °C		10	15	mΩ
I _{RRM}	I _F = 50 A	T _i = 150 °C		-		Α
Q _{rr}		T _i = 150 °C		-		μC
E _{rr}	V _{CC} = 300 V	T _i = 150 °C		-		mJ
R _{th(j-c)}	per diode				0.81	K/W
R _{th(c-s)}	per diode per diode (λgrease=0.81 W/mK, thickness 50-100μm)			0.082		K/W
Eroourboo	ling diede					
$V_F = V_{EC}$	eling diode I _F = 400 A	T _i = 25 °C		1.39	1.75	V
• F - • FC	$V_{GE} = 0 \text{ V}$	•				
	chiplevel	T _j = 150 °C		1.38	1.76	V
V _{F0}	chiplevel	T _j = 25 °C		1.04	1.236	V
	F	T _j = 150 °C		0.85	0.99	V
r _F	chiplevel	T _j = 25 °C		0.88	1.30	mΩ
		T _j = 150 °C		1.32	1.93	mΩ
I _{RRM}	l _F = 400 A di/dt _{off} = 2038 A/μs	T _j = 150 °C		188.2		A
Q _{rr}	α, αι _{οπ} – 2000 Α/μδ	1] = 100 0		37		μC
Err	V _{CC} = 300 V	T _j = 150 °C		6.27		mJ
R _{th(j-c)}	per diode				0.17	K/W
$R_{th(c-s)}$	per diode (λgrease=0.81 W/mK, thickness 50-100μm)			0.069		K/W





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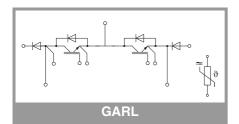
Typical Applications*

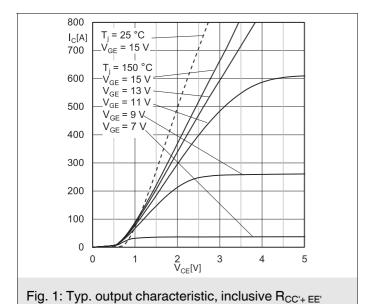
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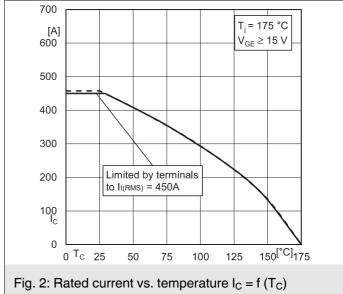
Remarks

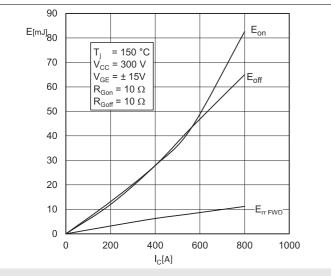
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Characteristics							
Symbol	Conditions		min.	typ.	max.	Unit	
Module							
L _{CE}				30		nΗ	
R _{CC'+EE'}	measured per	T _C = 25 °C		0.8		mΩ	
	switch	T _C = 125 °C		1.1		mΩ	
Rth _{(c-s)1}	calculated without t	hermal coupling		0.017		K/W	
Ms	to heat sink (M5)		3		6	Nm	
M _t		to terminals (M6)	3		6	Nm	
						Nm	
W				398		g	
Temperatu	ure Sensor					\Box	
R ₁₀₀	T _c =100°C (R ₂₅ =5 kΩ)		493 ± 5%			Ω	
B _{100/125}	$R_{(T)}=R_{100}exp[B_{100/125}(1/T-1/T_{100})];T[K];$			3550 ±2%		К	

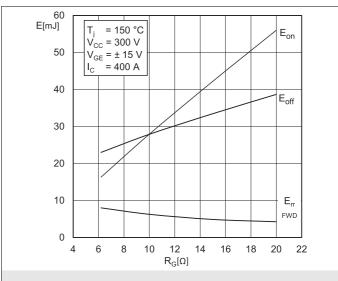


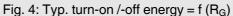












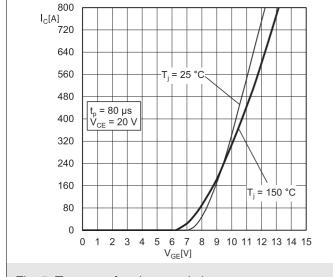


Fig. 5: Typ. transfer characteristic

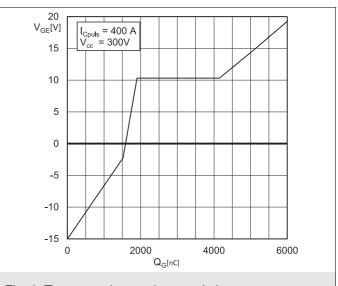


Fig. 6: Typ. gate charge characteristic

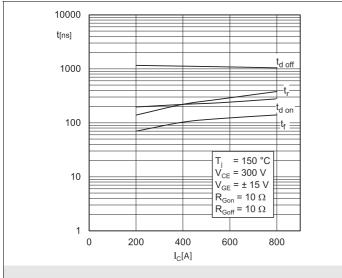


Fig. 7: Typ. switching times vs. I_C

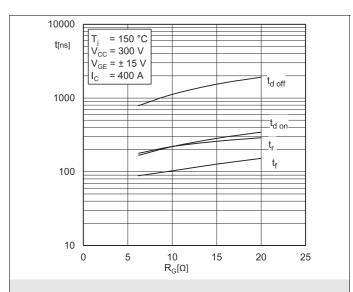


Fig. 8: Typ. switching times vs. gate resistor R_G

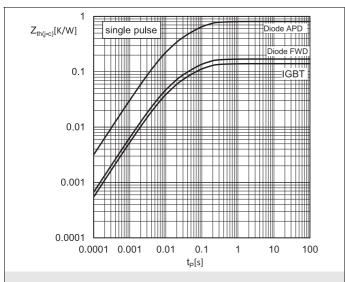


Fig. 9: Transient thermal impedance

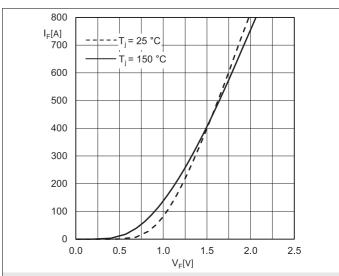


Fig. 10: Typ. FWD diode forward characteristic, incl. R_{CC}

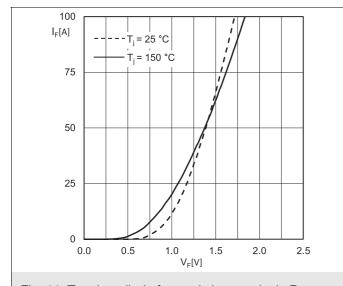


Fig. 11: Typ. inv. diode forward charact., incl. R_{CC'+ EE'}

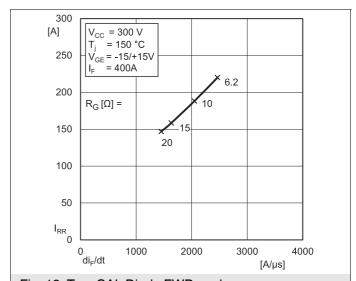
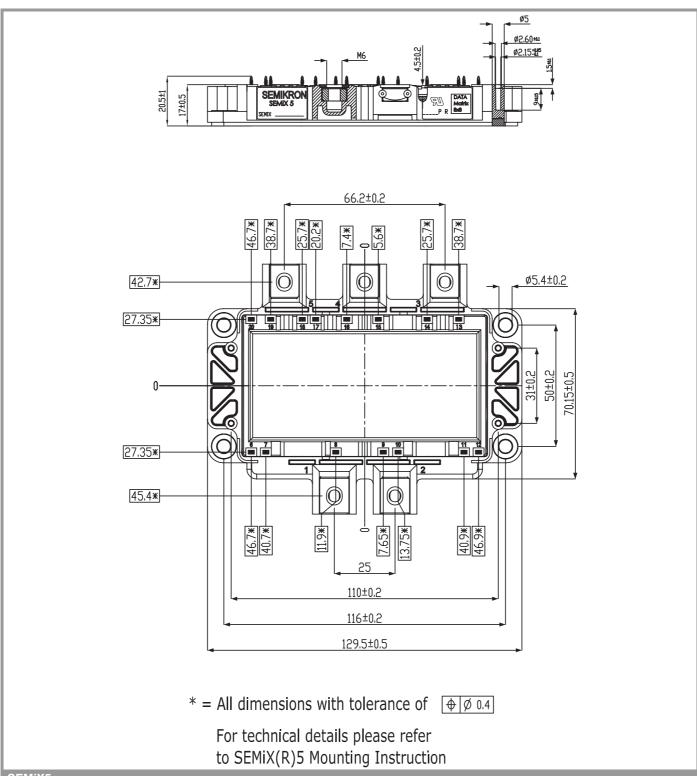
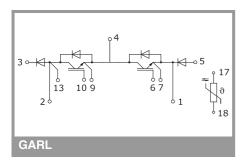


Fig. 12: Typ. CAL Diode FWD peak reverse recovery current



SEMiX5p



This is an electrostatic discharge sensitive device (ESDS) according to international standard IEC 61340.

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