

# SEMiX® 3p shunt

### Trench IGBT Modules

## SEMiX603GB12E4I25p

#### Features\*

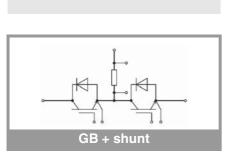
- · Homogeneous Si
- Trench = Trenchgate technology
- V<sub>CE(sat)</sub> with positive temperature coefficient
- High short circuit capability
- Press-fit pins as auxiliary contacts
- Thermally optimized ceramic
- Current sensing shunt resistor
- UL recognized, file no. E63532

### **Typical Applications**

- AC inverter drives
- UPS
- Renewable energy systems

### **Remarks**

- Product reliability results are valid for T<sub>i</sub>=150°C
- V<sub>isol</sub> between temperature sensor and power section is only 2500V
- For storage and case temperature with TIM see document "TP(\*) SEMiX 3p"



Absolute	Maximum Ratin	ıgs		
Symbol	Conditions		Values	Unit
IGBT	•		•	
V <sub>CES</sub>	T <sub>j</sub> = 25 °C		1200	V
Ic	T <sub>j</sub> = 175 °C	T <sub>c</sub> = 25 °C	1110	Α
		T <sub>c</sub> = 80 °C	853	Α
I <sub>Cnom</sub>			600	Α
I <sub>CRM</sub>	$I_{CRM} = 3 \times I_{Cnom}$		1800	Α
$V_{GES}$			-20 20	V
t <sub>psc</sub>	$V_{CC} = 800 \text{ V}$ $V_{GE} \le 15 \text{ V}$ $V_{CES} \le 1200 \text{ V}$	T <sub>j</sub> = 150 °C	10	μs
Tj			-40 175	°C
Inverse d	iode			
$V_{RRM}$	T <sub>j</sub> = 25 °C		1200	V
I <sub>F</sub>	T <sub>i</sub> = 175 °C	T <sub>c</sub> = 25 °C	856	Α
	11, = 173 0	T <sub>c</sub> = 80 °C	640	Α
I <sub>Fnom</sub>			600	Α
I <sub>FRM</sub>	I <sub>FRM</sub> = 2xI <sub>Fnom</sub>		1200	Α
I <sub>FSM</sub>	t <sub>p</sub> = 10 ms, sin 180°, T <sub>j</sub> = 25 °C		3456	Α
Tj			-40 175	°C
Module				
I <sub>t(RMS)</sub>	$T_c = 80^{\circ}C$		600	Α
T <sub>stg</sub>	module without 7	ГІМ	-40 125	°C
V <sub>isol</sub>	AC sinus 50Hz, t	= 1 min	4000	V

Characteristics							
Symbol	Conditions		min.	typ.	max.	Unit	
IGBT							
V <sub>CE(sat)</sub>	$I_{\rm C} = 600  {\rm A}$	T <sub>j</sub> = 25 °C		1.80	2.05	V	
V <sub>GE</sub> = 15 V chiplevel	T <sub>j</sub> = 150 °C		2.03	2.30	V		
V <sub>CE0</sub> chiplevel	chinlevel	T <sub>j</sub> = 25 °C		0.87	1.01	V	
	Chipievei	T <sub>j</sub> = 150 °C		0.77	0.90	V	
r <sub>CE</sub>	V <sub>GE</sub> = 15 V	T <sub>j</sub> = 25 °C		1.55	1.73	mΩ	
	chiplevel	T <sub>j</sub> = 150 °C		2.1	2.3	mΩ	
$V_{GE(th)}$	$V_{GE} = V_{CE}, I_C = 22.2$	2 mA	5.3	5.8	6.3	V	
I <sub>CES</sub>	$V_{GE} = 0 \text{ V}, V_{CE} = 12$	00 V, T <sub>j</sub> = 25 °C			5	mA	
C <sub>ies</sub>	V 05.V	f = 1 MHz		37.5		nF	
Coes	$V_{CE} = 25 \text{ V}$ $V_{GF} = 0 \text{ V}$	f = 1 MHz		2.31		nF	
C <sub>res</sub>	VGE - UV	f = 1 MHz		2.04		nF	
$Q_G$	V <sub>GE</sub> = - 8 V+ 15 V			3450		nC	
R <sub>Gint</sub>	T <sub>j</sub> = 25 °C			1.2		Ω	
t <sub>d(on)</sub>		T <sub>j</sub> = 150 °C		260		ns	
t <sub>r</sub>	$I_C = 600 \text{ A}$ $V_{GF} = +15/-15 \text{ V}$	T <sub>j</sub> = 150 °C		85		ns	
E <sub>on</sub>	$R_{Gon} = 1.5 \Omega$	T <sub>j</sub> = 150 °C		63		mJ	
t <sub>d(off)</sub>		T <sub>j</sub> = 150 °C		560		ns	
t <sub>f</sub>	$di/dt_{on} = 6800 \text{ A/}\mu\text{s}$	T <sub>j</sub> = 150 °C		145		ns	
E <sub>off</sub>	$\begin{array}{l} \text{di/dt}_{\text{off}} = 3700 \text{ A/µs} \\ \text{dv/dt} = 3400 \text{ V/µs} \\ \text{L}_{\text{s}} = 21 \text{ nH} \end{array}$	T <sub>j</sub> = 150 °C		80		mJ	
R <sub>th(j-c)</sub>	per IGBT				0.037	K/W	
R <sub>th(c-s)</sub>	per IGBT (λ <sub>grease</sub> =0.81 W/(m*K))			0.035		K/W	
R <sub>th(c-s)</sub>	per IGBT, pre-applied phase change material			0.025		K/W	



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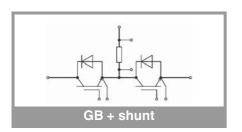
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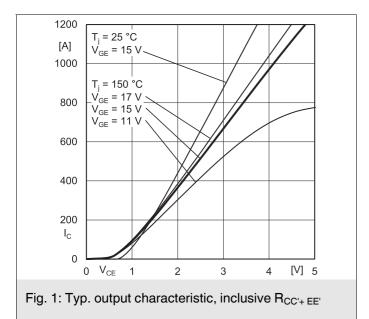
### **Remarks**

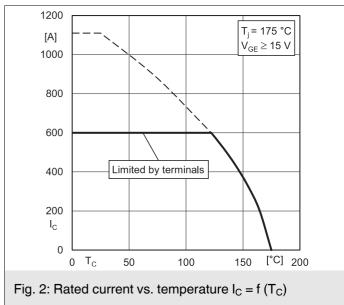
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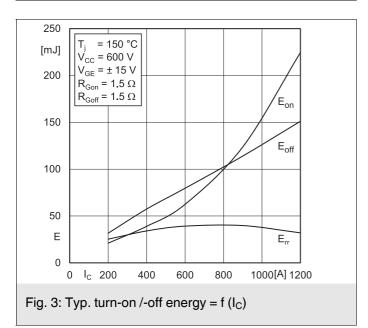
Characte	eristics					
Symbol	Conditions		min.	typ.	max.	Unit
Inverse d	iode					•
$V_F = V_{EC}$	I <sub>F</sub> = 600 A	T <sub>j</sub> = 25 °C		2.08	2.44	V
	V <sub>GE</sub> = 0 V chiplevel	T <sub>j</sub> = 150 °C		2.08	2.34	V
$V_{F0}$	chiplevel	T <sub>j</sub> = 25 °C		1.39	1.59	V
		T <sub>j</sub> = 150 °C		1.08	1.18	V
r <sub>F</sub>	chiplevel	T <sub>j</sub> = 25 °C		1.16	1.42	mΩ
	Chipievei	T <sub>j</sub> = 150 °C		1.67	1.93	mΩ
I <sub>RRM</sub>	$I_F = 600 \text{ A}$ $di/dt_{off} = 6500 \text{ A/µs}$ $V_{GE} = -15 \text{ V}$	T <sub>j</sub> = 150 °C		465		Α
Q <sub>rr</sub>		T <sub>j</sub> = 150 °C		108		μC
E <sub>rr</sub>	$V_{CC} = 600 \text{ V}$	T <sub>j</sub> = 150 °C		40		mJ
R <sub>th(j-c)</sub>	per diode				0.065	K/W
R <sub>th(c-s)</sub>	per diode (λ <sub>grease</sub> =0	.81 W/(m*K))		0.039		K/W
R <sub>th(c-s)</sub>	per diode, pre-applied phase change material			0.031		K/W
Module	•					
L <sub>CE</sub>				20		nΗ
R <sub>CC'+EE'</sub>	measured per	T <sub>C</sub> = 25 °C		0.95		mΩ
	switch, shunt excluded	T <sub>C</sub> = 125 °C		1.25		mΩ
R <sub>th(c-s)1</sub>	calculated without thermal coupling			0.009		K/W
$R_{\text{th(c-s)2}}$	including thermal coupling, Ts underneath module ( $\lambda_{grease}$ =0.81 W/(m*K))			0.015		K/W
R <sub>th(c-s)2</sub>	including thermal coupling, Ts underneath module, pre-applied phase change material			0.011		K/W
Ms	to heat sink (M5)		3		6	Nm
M <sub>t</sub>		to terminals (M6)	3		6	Nm
						Nm
W					350	g
Temperat	ture Sensor					_
R <sub>100</sub>	T <sub>c</sub> =100°C (R <sub>25</sub> =5 k	Ω)		493 ± 5%		Ω
B <sub>100/125</sub>	$R_{(T)}=R_{100}exp[B_{100/1}]$	<sub>25</sub> (1/T-1/T <sub>100</sub> )]; T[K];		3550 ±2%		К

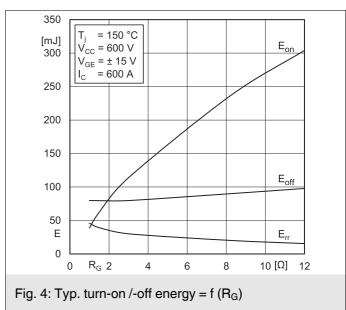
Characteristics						
Symbol	Conditions	min.	typ.	max.	Unit	
Shunt					•	
R <sub>Shunt</sub>	Tolerance = $\pm 1$ %, $T_c = 20$ °C		0.26		mΩ	
α				50	ppm/K	
T <sub>Shunt</sub>				170	°C	
R <sub>th(r-c)</sub>				2	K/W	
P <sub>Shunt</sub>	T <sub>c</sub> = 80 °C			45	W	

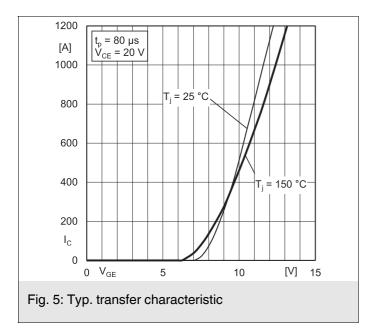


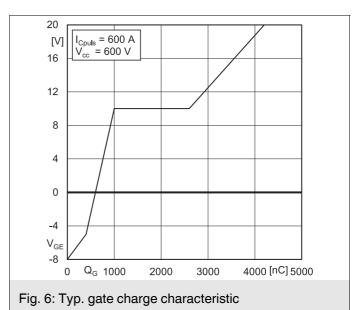


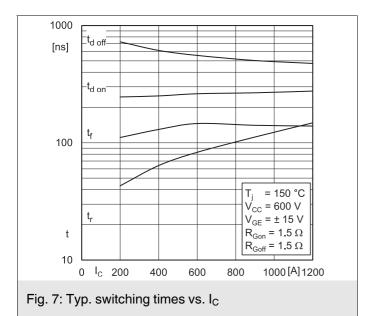


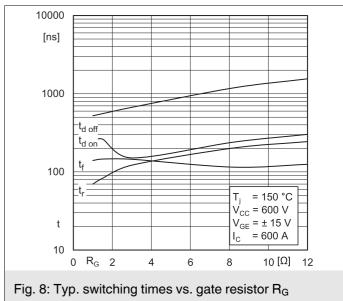


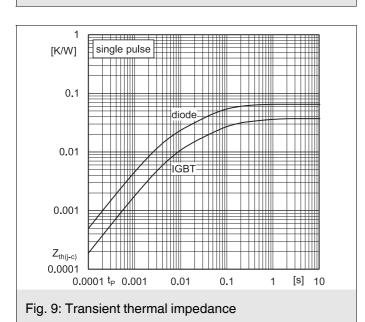


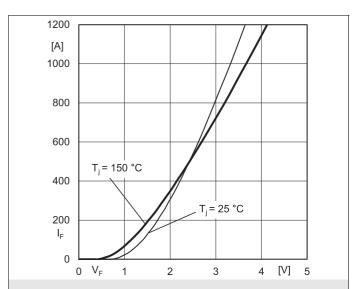












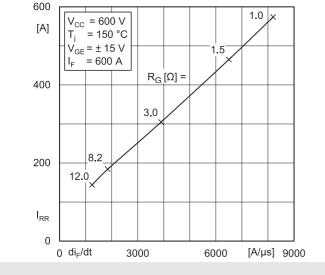


Fig. 10: Typ. CAL diode forward charact., incl. R<sub>CC'+ EE'</sub>

1200

900

600

400

200 1.0

1.5

 $R_G[\Omega] =$ 

5000

= I<sub>F</sub> [A]

[A/µs] 10000



Fig. 11: Typ. CAL diode peak reverse recovery current

150

[µC]

100

50

 $Q_{rr}$ 

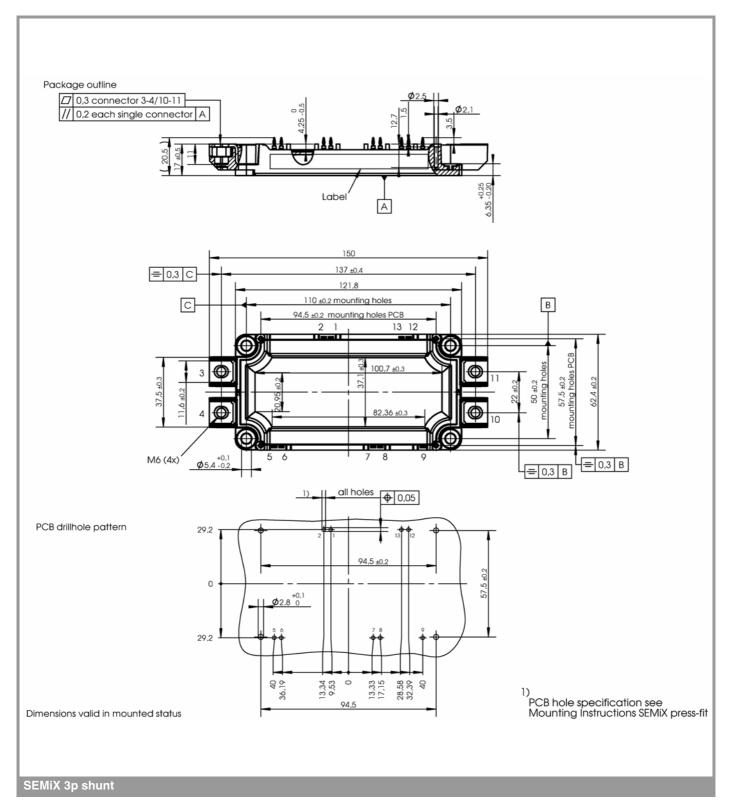
0 di<sub>F</sub>/dt

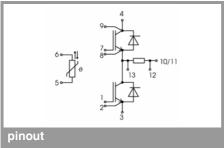
 $V_{CC}$  = 600 V

= 150 °C  $V_{GE} = \pm 15 \text{ V}$ 

12.0-8.2

3.0





This is an electrostatic discharge sensitive device (ESDS), international standard IEC 60747-1, chapter IX.

#### \*IMPORTANT INFORMATION AND WARNINGS

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