

SEMiX® 3p shunt

Trench IGBT Modules

SEMiX603GB12E4Ip

Features

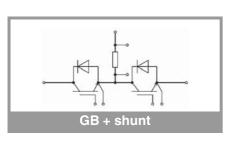
- · Homogeneous Si
- Trench = Trenchgate technology
- V_{CE(sat)} with positive temperature coefficient
- High short circuit capability
- · Press-fit pins as auxiliary contacts
- Thermally optimized ceramic
- Current sensing shunt resistor
- UL recognized, file no. E63532

Typical Applications*

- AC inverter drives
- UPS
- Renewable energy systems

Remarks

- Product reliability results are valid for $T_i=150^{\circ}C$
- V_{isol} between temperature sensor and power section is only 2500V
- For storage and case temperature with TIM see document "TP(*) SEMiX 3p"



Absolute Maximum Ratings							
Symbol	Conditions		Values	Unit			
IGBT							
V _{CES}	T _j = 25 °C		1200	V			
Ic	T _i = 175 °C	T _c = 25 °C	1110	Α			
	1, - 175 0	T _c = 80 °C	853	Α			
I _{Cnom}			600	Α			
I _{CRM}	$I_{CRM} = 3xI_{Cnom}$		1800	Α			
V_{GES}			-20 20	V			
t _{psc}	$V_{CC} = 800 \text{ V}$ $V_{GE} \le 15 \text{ V}$ $V_{CES} \le 1200 \text{ V}$	T _j = 150 °C	10	μs			
T _j			-40 175	°C			
Inverse di	iode						
V_{RRM}	T _j = 25 °C		1200	V			
I _F	T _i = 175 °C	T _c = 25 °C	856	Α			
	11, - 173 0	T _c = 80 °C	640	Α			
I _{Fnom}			600	Α			
I _{FRM}	I _{FRM} = 3xI _{Fnom}		1800	Α			
I _{FSM}	t _p = 10 ms, sin 180°, T _j = 25 °C		3456	Α			
Tj			-40 175	°C			
Module							
I _{t(RMS)}			407	Α			
T _{stg}	module without TIN	Л	-40 125	°C			
V _{isol}	AC sinus 50Hz, t = 1 min		4000	V			

Characteristics							
Symbol	Conditions		min.	typ.	max.	Unit	
IGBT							
V _{CE(sat)}	$I_{\rm C} = 600 {\rm A}$	T _j = 25 °C		1.80	2.05	V	
	V _{GE} = 15 V chiplevel	T _j = 150 °C		2.03	2.30	V	
V _{CE0} ch	chiplevel	T _j = 25 °C		0.87	1.01	V	
	Criipievei	T _j = 150 °C		0.77	0.90	V	
r _{CE}	V _{GE} = 15 V	T _j = 25 °C		1.55	1.73	mΩ	
	chiplevel	T _j = 150 °C		2.1	2.3	mΩ	
$V_{GE(th)}$	V _{GE} =V _{CE} , I _C = 22.2	mA	5.3	5.8	6.3	V	
I _{CES}	$V_{GE} = 0 \text{ V}, V_{CE} = 12$	00 V, T _j = 25 °C			5	mA	
C _{ies}	V 05.V	f = 1 MHz		37.5		nF	
Coes	$V_{CE} = 25 \text{ V}$ $V_{GF} = 0 \text{ V}$	f = 1 MHz		2.31		nF	
C _{res}	VGE - UV	f = 1 MHz		2.04		nF	
Q_G	V _{GE} = - 8 V+ 15 V			3450		nC	
R _{Gint}	T _j = 25 °C			1.2		Ω	
t _{d(on)}	V _{CC} = 600 V	T _j = 150 °C		260		ns	
t _r	$I_C = 600 \text{ A}$ $V_{GF} = +15/-15 \text{ V}$	T _j = 150 °C		85		ns	
E _{on}	$R_{Gon} = 1.5 \Omega$	T _j = 150 °C		63		mJ	
t _{d(off)}		T _j = 150 °C		560		ns	
t _f	$di/dt_{on} = 6800 \text{ A/}\mu\text{s}$	T _j = 150 °C		145		ns	
E _{off}	$\begin{array}{l} \text{di/dt}_{\text{off}} = 3700 \text{ A/}\mu\text{s} \\ \text{du/dt} = 3400 \text{ V/}\mu\text{s} \\ \text{L}_{\text{s}} = 21 \text{ nH} \end{array}$	T _j = 150 °C		80		mJ	
R _{th(j-c)}	per IGBT				0.037	K/W	
R _{th(c-s)}	per IGBT (λ _{grease} =0.81 W/(m*K))			0.035		K/W	
R _{th(c-s)}	per IGBT, pre-applied phase change material			0.025		K/W	



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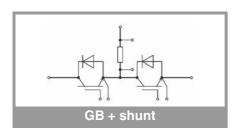
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- Renewable energy systems

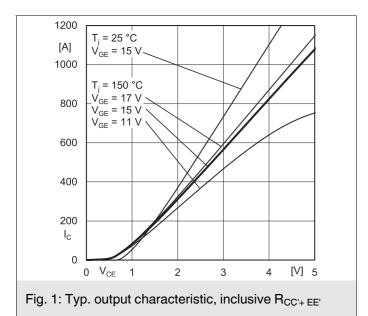
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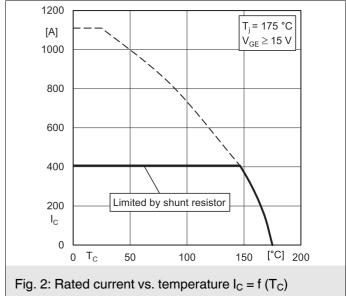
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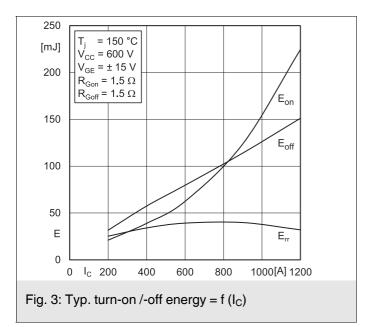
Characte	eristics					
Symbol	Conditions		min.	typ.	max.	Unit
Inverse d	iode					•
$V_F = V_{EC}$	I _F = 600 A	T _j = 25 °C		2.08	2.44	V
	V _{GE} = 0 V chiplevel	T _j = 150 °C		2.08	2.34	٧
V_{F0}	chiplevel	T _j = 25 °C		1.39	1.59	V
		T _j = 150 °C		1.08	1.18	V
r _F	chiplevel	T _j = 25 °C		1.16	1.42	mΩ
	Chipievei	T _j = 150 °C		1.67	1.93	mΩ
I _{RRM}	I _F = 600 A	T _j = 150 °C		465		Α
Q _{rr}	di/dt _{off} = 6500 A/μs V _{GE} = -15 V	T _j = 150 °C		108		μC
E _{rr}	$V_{CC} = 600 \text{ V}$	T _j = 150 °C		40		mJ
R _{th(j-c)}	per diode				0.065	K/W
R _{th(c-s)}	per diode (λ _{grease} =0	.81 W/(m*K))		0.039		K/W
R _{th(c-s)}	per diode, pre-applied phase change material			0.031		K/W
Module	•					
L _{CE}				20		nΗ
R _{CC'+EE'}	measured per	T _C = 25 °C		1.2		mΩ
	switch, shunt excluded	T _C = 125 °C		1.65		mΩ
Rth _{(c-s)1}	calculated without t	hermal coupling		0.009		K/W
Rth _{(c-s)2}	including thermal coupling, Ts underneath module (λ_{grease} =0.81 W/(m*K))			0.015		K/W
Rth _{(c-s)2}	including thermal coupling, Ts underneath module, pre-applied phase change material			0.011		K/W
Ms	to heat sink (M5)		3		6	Nm
M _t		to terminals (M6)	3		6	Nm
						Nm
W					350	g
Temperat	ture Sensor					
R ₁₀₀	T _c =100°C (R ₂₅ =5 k	Ω)		493 ± 5%		Ω
B _{100/125}	$R_{(T)}=R_{100}exp[B_{100/1}]$	₂₅ (1/T-1/T ₁₀₀)]; T[K];		3550 ±2%		К

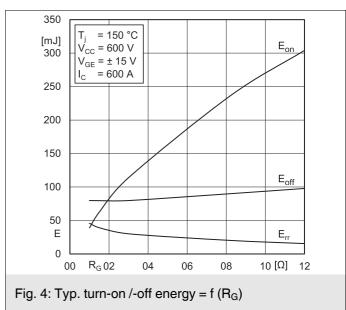
Characteristics						
Symbol	Conditions	min.	typ.	max.	Unit	
Shunt					•	
I _{Shunt}	$T_c = 100 ^{\circ}\text{C}, T_{Shunt,max} = 170 ^{\circ}\text{C}, \ R_{th} = 2.3 \text{K/W}$			407	Α	
R _{Shunt}	Tolerance = ±5 %		0.19		mΩ	
α				75	ppm/K	

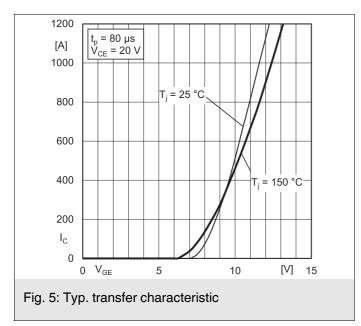


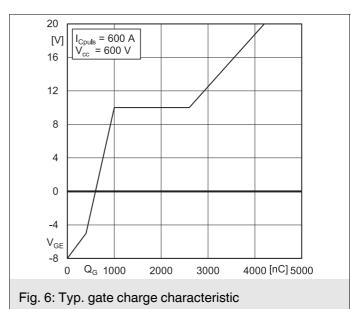


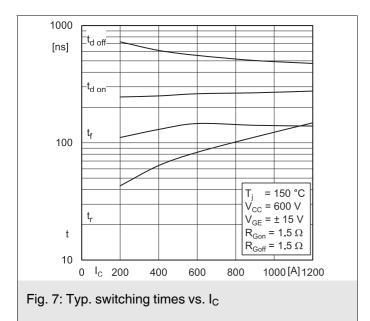


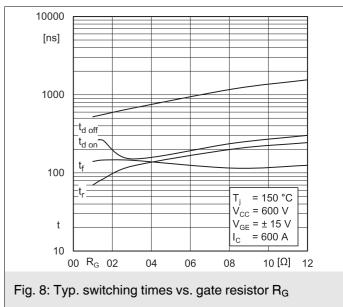


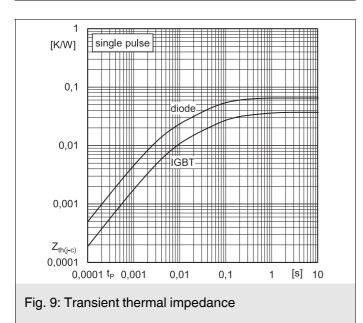


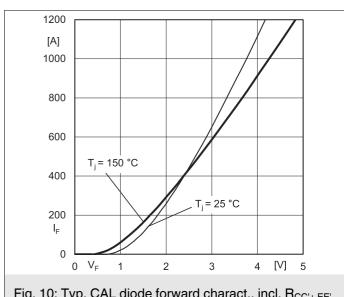


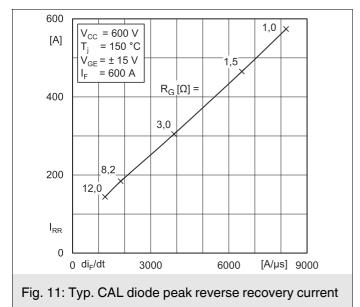


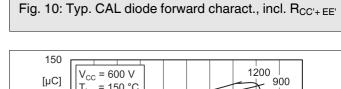












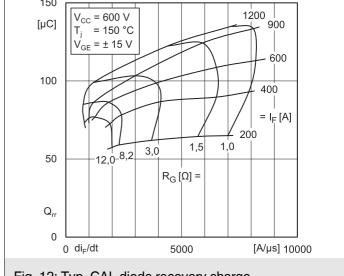
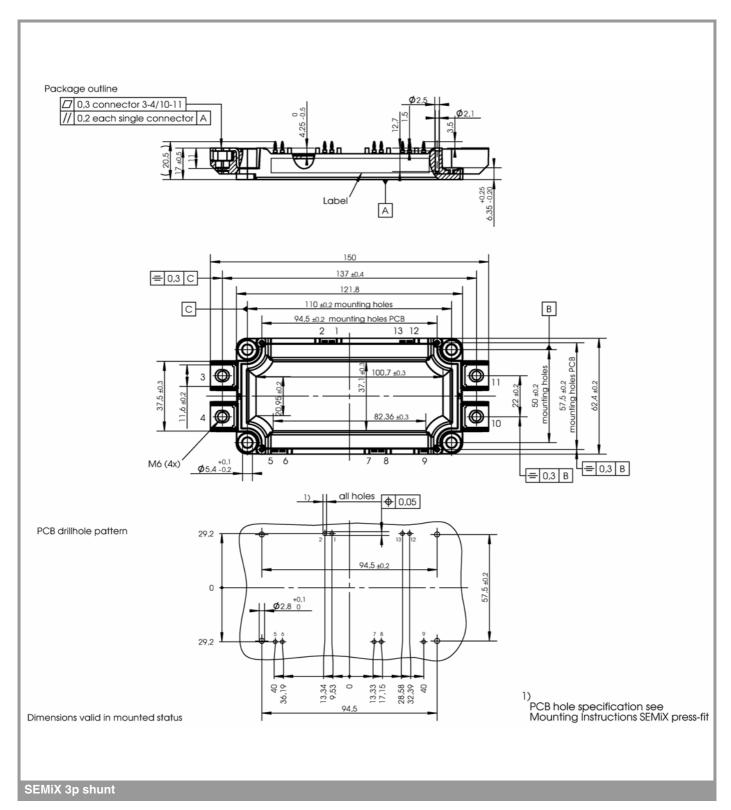
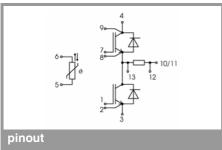


Fig. 12: Typ. CAL diode recovery charge





This is an electrostatic discharge sensitive device (ESDS), international standard IEC 60747-1, chapter IX.

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