



SEMITOP®E1

Sixpack Open Emitter

SK25GD12T7ETE1

Features*

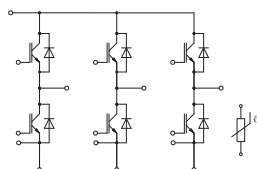
- Optimized design for superior thermal performance
- Low inductive design
- Press-Fit contact technology
- 1200V Generation 7 IGBT (T7)
- Robust and soft switching CAL4F diode technology
- Integrated NTC temperature sensor
- UL recognized file no. E 63 532

Typical Applications

- Motor drives
- Servo drives
- Air conditioning
- Auxiliary Inverters
- UPS

Remarks

- Recommended $T_{j,op} = -40 \dots +150 \text{ }^{\circ}\text{C}$
- $T_{j,op} > 150 \text{ }^{\circ}\text{C}$ during overload (details on AN19-002)



GD-ET

Absolute Maximum Ratings				
Symbol	Conditions		Values	Unit
Inverter - IGBT				
V _{CES}	T _j = 25 °C		1200	V
I _C	λ _{paste} =0.8 W/(mK)	T _s = 70 °C	33	A
	T _j = 175 °C	T _s = 100 °C	27	A
I _C	λ _{paste} =2.5 W/(mK)	T _s = 70 °C	38	A
	T _j = 175 °C	T _s = 100 °C	31	A
I _{Cnom}			25	A
I _{CRM}			50	A
V _{GES}			-20 ... 20	V
t _{psc}	V _{CC} = 800 V V _{GE} ≤ 15 V V _{CES} ≤ 1200 V	T _j = 175 °C	7	µs
T _j			-40 ... 175	°C
Inverse - Diode				
V _{RRM}	T _j = 25 °C		1200	V
I _F	λ _{paste} =0.8 W/(mK)	T _s = 70 °C	24	A
	T _j = 175 °C	T _s = 100 °C	20	A
I _F	λ _{paste} =2.5 W/(mK)	T _s = 70 °C	28	A
	T _j = 175 °C	T _s = 100 °C	23	A
I _{FRM}			50	A
I _{FSM}	t _p = 10 ms, sin 180°, T _j = 150 °C		100	A
T _j			-40 ... 175	°C
Module				
I _{t(RMS)}	, ΔT _{terminal} at PCB joint = 30 K, per pin		30	A
T _{stg}	module without TIM		-40 ... 125	°C
V _{isol}	AC, sinusoidal, t = 1 min		2500	V

Characteristics						
Symbol	Conditions		min.	typ.	max.	Unit
Inverter - IGBT						
V _{CE(sat)}	I _C = 25 A	T _j = 25 °C		1.60	1.75	V
	V _{GE} = 15 V	T _j = 150 °C		1.78	1.93	V
	chiplevel	T _j = 175 °C		1.82	1.97	V
V _{CE0}	chiplevel	T _j = 25 °C		1.00	1.05	V
		T _j = 150 °C		0.80	0.85	V
		T _j = 175 °C		0.75	0.80	V
r _{CE}	V _{GE} = 15 V chiplevel	T _j = 25 °C		24	28	mΩ
		T _j = 150 °C		39	43	mΩ
		T _j = 175 °C		43	47	mΩ
V _{GE(th)}	V _{GE} = V _{CE} , I _C = 0.53 mA		5.15	5.8	6.45	V
I _{CES}	V _{GE} = 0 V, V _{CE} = 1200 V, T _j = 25 °C				1	mA
C _{ies}	V _{CE} = 25 V V _{GE} = 0 V	f = 1 MHz		4.80		nF
C _{oes}		f = 1 MHz		0.06		nF
C _{res}		f = 1 MHz		0.02		nF
Q _G	V _{GE} = -15 V ... +15 V			354		nC
R _{Gint}	T _j = 25 °C			0		Ω



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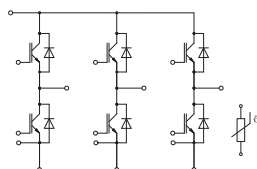
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- $T_{j,op} > 150 \text{ }^{\circ}\text{C}$ during overload (details on AN19-002)



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Characteristics					
Symbol	Conditions		min.	typ.	max. Unit
Inverter - IGBT					
$t_{d(on)}$		$T_j = 25 \text{ }^{\circ}\text{C}$		28	ns
		$T_j = 150 \text{ }^{\circ}\text{C}$		30	ns
		$T_j = 175 \text{ }^{\circ}\text{C}$		32	ns
t_r		$T_j = 25 \text{ }^{\circ}\text{C}$		23	ns
		$T_j = 150 \text{ }^{\circ}\text{C}$		25	ns
		$T_j = 175 \text{ }^{\circ}\text{C}$		26	ns
E_{on}	$V_{CC} = 600 \text{ V}$ $I_C = 25 \text{ A}$ $R_{G on} = 6.2 \text{ } \Omega$ $R_{G off} = 6.2 \text{ } \Omega$ $V_{GE} = +15/-15 \text{ V}$	$T_j = 25 \text{ }^{\circ}\text{C}$		1.65	mJ
		$T_j = 150 \text{ }^{\circ}\text{C}$		2.42	mJ
		$T_j = 175 \text{ }^{\circ}\text{C}$		2.72	mJ
$t_{d(off)}$	$(T_j = 150 \text{ }^{\circ}\text{C})$ $di/dt_{on} = 880 \text{ A}/\mu\text{s}$ $di/dt_{off} = 210 \text{ A}/\mu\text{s}$ $dv/dt = 5400 \text{ V}/\mu\text{s}$	$T_j = 25 \text{ }^{\circ}\text{C}$		191	ns
		$T_j = 150 \text{ }^{\circ}\text{C}$		231	ns
		$T_j = 175 \text{ }^{\circ}\text{C}$		251	ns
t_f		$T_j = 25 \text{ }^{\circ}\text{C}$		66	ns
		$T_j = 150 \text{ }^{\circ}\text{C}$		101	ns
		$T_j = 175 \text{ }^{\circ}\text{C}$		108	ns
E_{off}		$T_j = 25 \text{ }^{\circ}\text{C}$		2.04	mJ
		$T_j = 150 \text{ }^{\circ}\text{C}$		2.71	mJ
		$T_j = 175 \text{ }^{\circ}\text{C}$		3.09	mJ
$R_{th(j-s)}$	per IGBT, $\lambda_{paste}=0.8 \text{ W}/(\text{mK})$			1.32	K/W
$R_{th(j-s)}$	per IGBT, $\lambda_{paste}=2.5 \text{ W}/(\text{mK})$			1.06	K/W

Characteristics					
Symbol	Conditions		min.	typ.	max. Unit
Inverse - Diode					
$V_F = V_{EC}$	$I_F = 25 \text{ A}$ chiplevel	$T_j = 25 \text{ }^{\circ}\text{C}$		2.41	2.74 V
		$T_j = 150 \text{ }^{\circ}\text{C}$		2.45	2.79 V
		$T_j = 175 \text{ }^{\circ}\text{C}$		2.30	2.62 V
V_{F0}	chiplevel	$T_j = 25 \text{ }^{\circ}\text{C}$		1.30	1.50 V
		$T_j = 150 \text{ }^{\circ}\text{C}$		0.90	1.10 V
		$T_j = 175 \text{ }^{\circ}\text{C}$		0.82	0.98 V
r_F	chiplevel	$T_j = 25 \text{ }^{\circ}\text{C}$		44	50 m Ω
		$T_j = 150 \text{ }^{\circ}\text{C}$		62	68 m Ω
		$T_j = 175 \text{ }^{\circ}\text{C}$		59	66 m Ω
I_{RRM}		$T_j = 25 \text{ }^{\circ}\text{C}$		20	A
		$T_j = 150 \text{ }^{\circ}\text{C}$		28	A
		$T_j = 175 \text{ }^{\circ}\text{C}$		30	A
Q_{rr}	$I_F = 25 \text{ A}$ $V_{GE} = +15/-15 \text{ V}$ $V_{CC} = 600 \text{ V}$ $(T_j = 150 \text{ }^{\circ}\text{C})$ $di/dt_{off} = 1050 \text{ A}/\mu\text{s}$	$T_j = 25 \text{ }^{\circ}\text{C}$		1.41	μC
		$T_j = 150 \text{ }^{\circ}\text{C}$		3.71	μC
		$T_j = 175 \text{ }^{\circ}\text{C}$		4.19	μC
E_{rr}		$T_j = 25 \text{ }^{\circ}\text{C}$		0.51	mJ
		$T_j = 150 \text{ }^{\circ}\text{C}$		1.61	mJ
		$T_j = 175 \text{ }^{\circ}\text{C}$		2.46	mJ
$R_{th(j-s)}$	per Diode, $\lambda_{paste}=0.8 \text{ W}/(\text{mK})$			1.66	K/W
$R_{th(j-s)}$	per Diode, $\lambda_{paste}=2.5 \text{ W}/(\text{mK})$			1.29	K/W
Module					
L_{CE}				30	nH
M_s	to heatsink		1.6	2.3	Nm
w				25	g

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Characteristics

Symbol	Conditions	min.	typ.	max.	Unit
Temperature Sensor					
R_{100}	$T_c=100^{\circ}\text{C}$ ($R_{25}=5\text{ k}\Omega$)		$493 \pm 5\%$		Ω
$B_{25/85}$	$R(T)=R_{25} \cdot \exp[B_{25/85} \cdot (1/T - 1/298)]$, $T[\text{K}]$		3420		K

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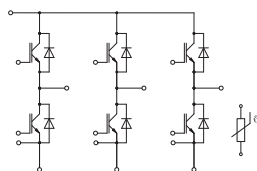
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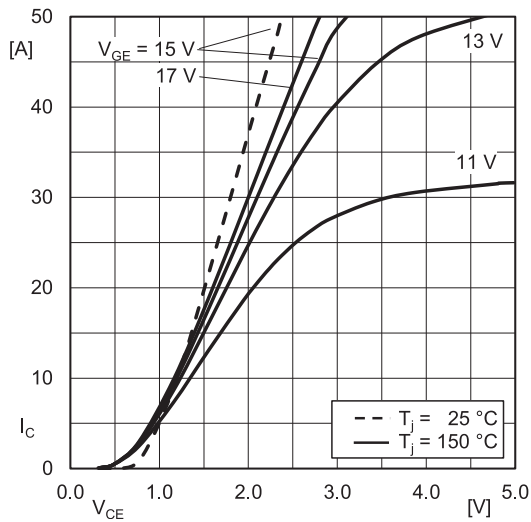


Fig. 1: Typ. IGBT output characteristic, incl. $R_{CC+EE'}$

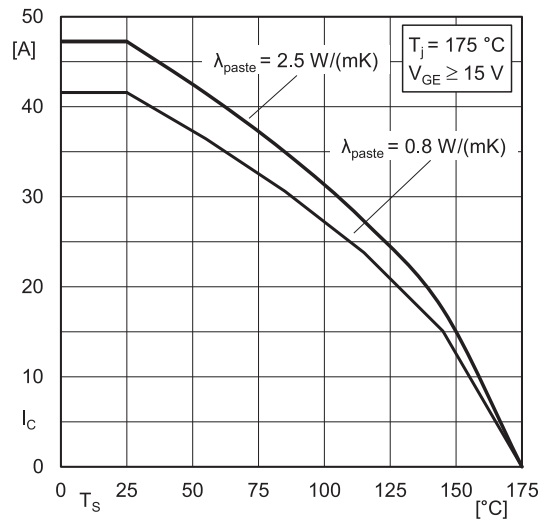


Fig. 2: IGBT rated current vs. temperature $I_C=f(T_s)$

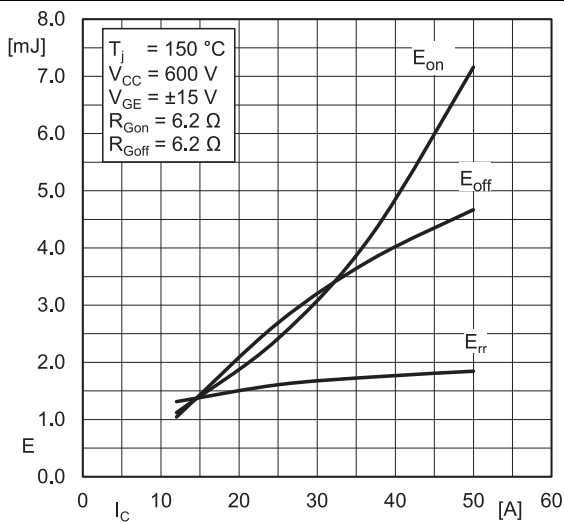


Fig. 3: Typ. turn-on /-off energy = $f(I_C)$

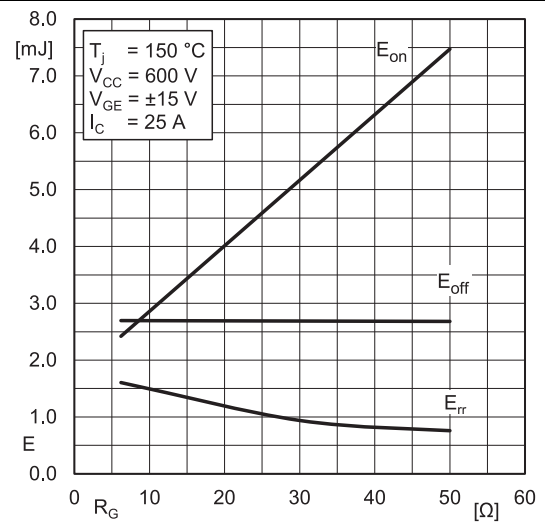


Fig. 4: Typ. turn-on /-off energy = $f(R_G)$

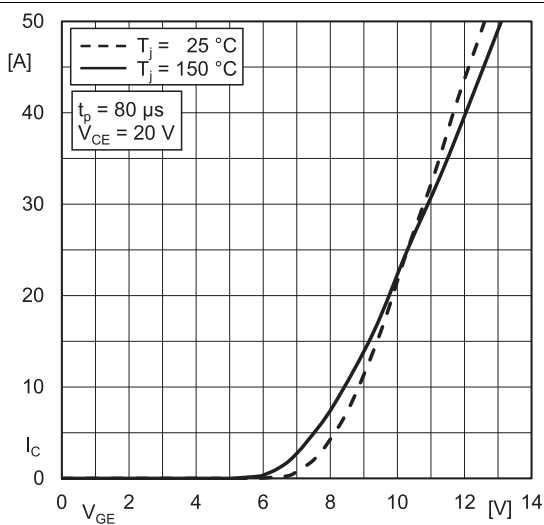


Fig. 5: Typ. IGBT transfer characteristic

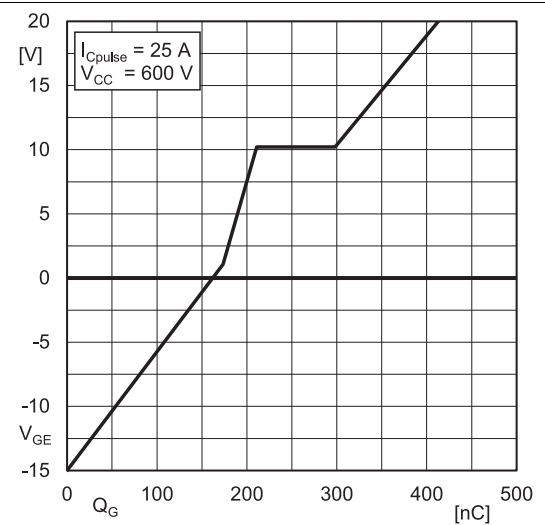


Fig. 6: Typ. IGBT gate charge characteristic

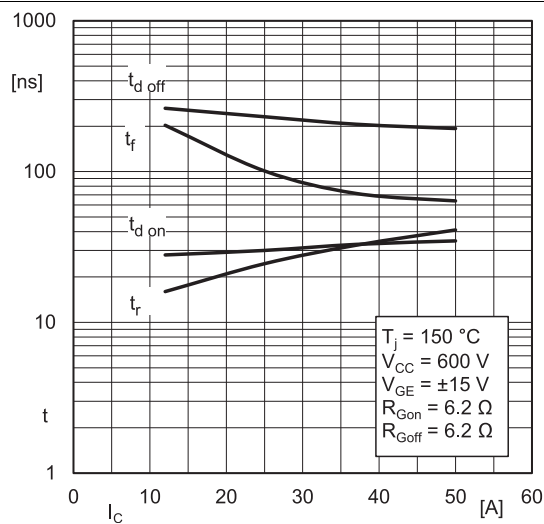


Fig. 7: Typ. switching times = $f(I_C)$

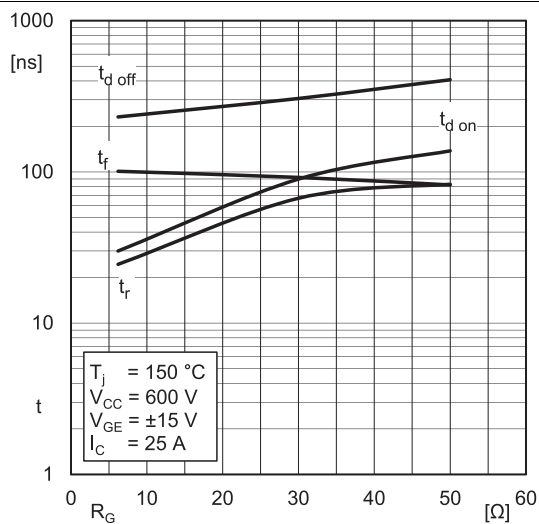


Fig. 8: Typ. switching times = $f(R_G)$

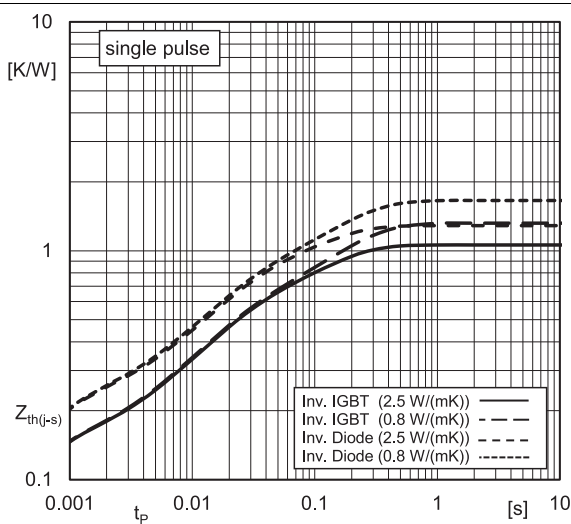


Fig. 9: Typ. transient thermal impedance

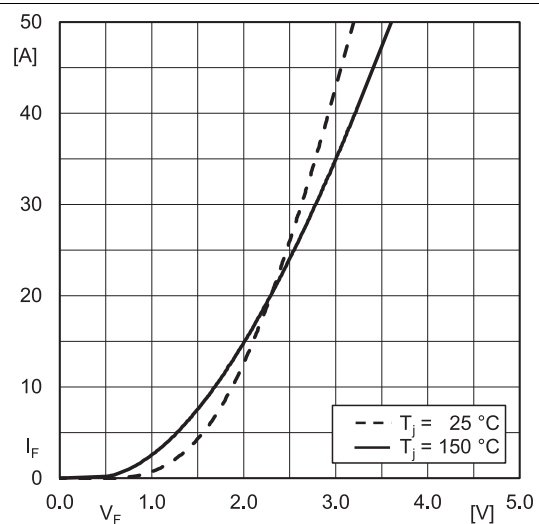


Fig. 10: Typ. Inv. diode forward charact., incl. $R_{GG} + EE'$

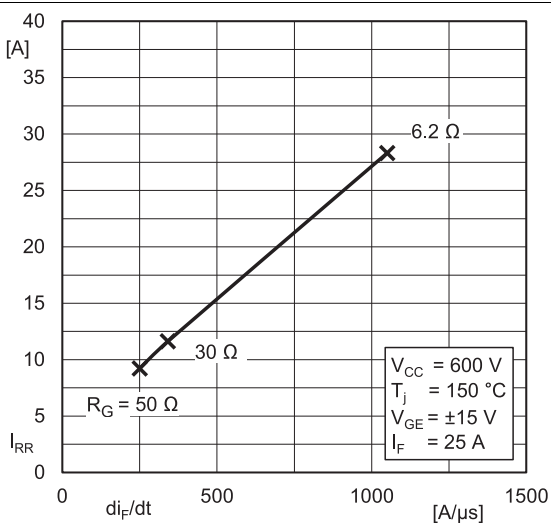


Fig. 11: Typ. Inv. diode peak reverse recovery current

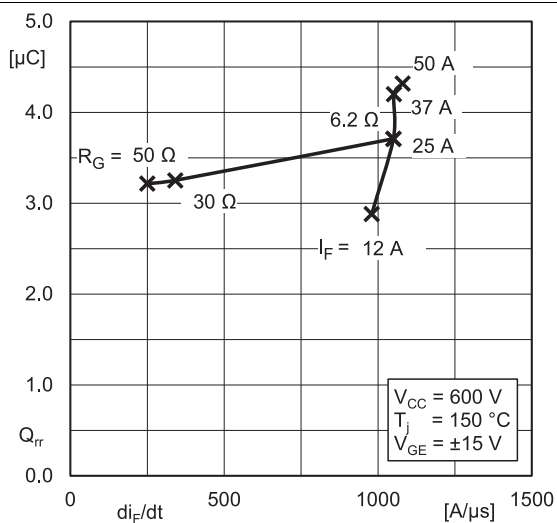


Fig. 12: Typ. Inv. diode reverse recovery charge

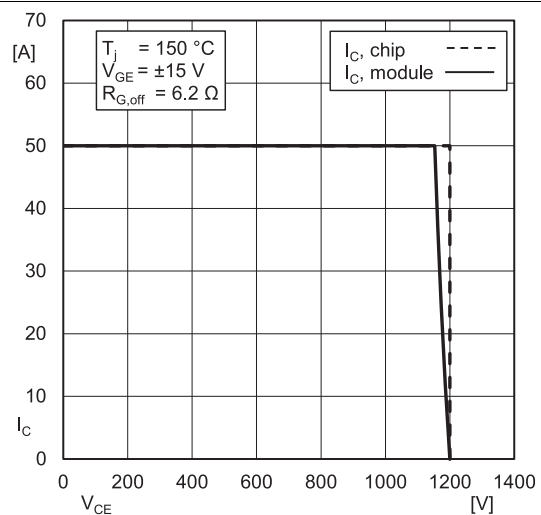
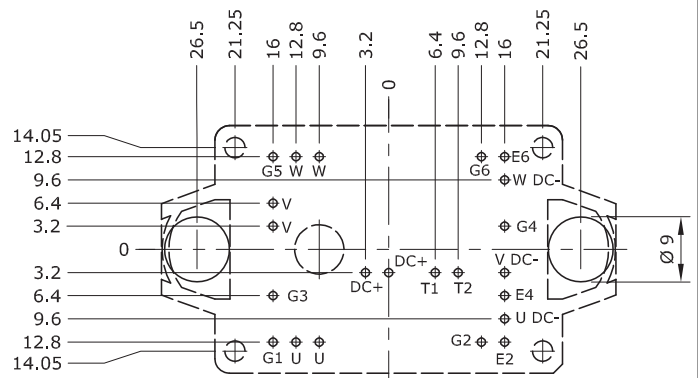
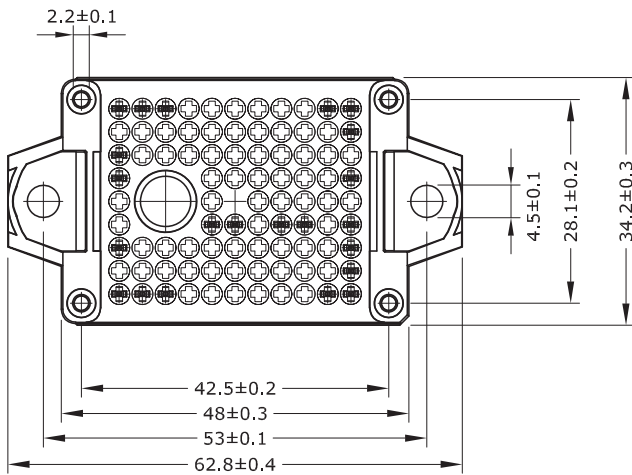
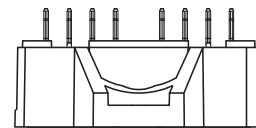
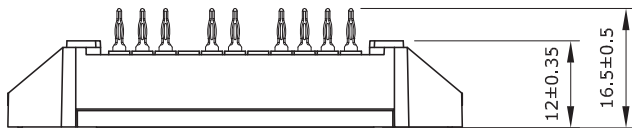


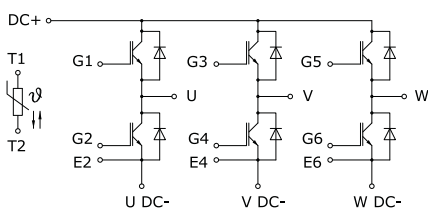
Fig. 13: IGBT Reverse Bias Safe Operating Area (RBSOA)

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- Pin-Grid 3.2 mm
- Tolerance of PCB hole pattern ± 0.1
- Diameters of drill $\varnothing 1.15\text{mm}$
- Copper thickness in hole 25 - 50 μm
- Hole specification for contacts:
refer to SEMITOP E1/E2 Mounting Instruction

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This is an electrostatic discharge sensitive device (ESDS) due to international standard IEC 61340.

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