

# Sixpack Open Emitter

### SK35GD12T7ETE1

### Features\*

- Optimized design for superior thermal performance
- Low inductive design
- Press-Fit contact technology
- 1200V Generation 7 IGBT (T7)
- Robust and soft switching CAL4F diode technology
- Integrated NTC temperature sensor
- UL recognized file no. E 63 532

### **Typical Applications**

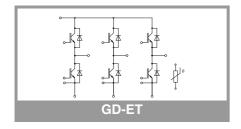
- · Motor drives
- Servo drives
- Air conditioning
- · Auxiliary Inverters
- UPS

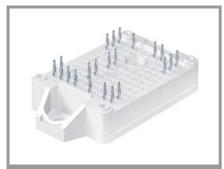
### **Remarks**

- Recommended  $T_{j,op} = -40 \dots +150 \,^{\circ}C$
- T<sub>j,op</sub> > 150 °C during overload (details on AN19-002)

Absolute	Maximum Ratings	<b>S</b>			
Symbol	Conditions		Values	Unit	
Inverter -	IGBT				
V <sub>CES</sub>	T <sub>j</sub> = 25 °C		1200	V	
Ic	$\lambda_{paste}$ =0.8 W/(mK) T <sub>j</sub> = 175 °C	T <sub>s</sub> = 70 °C	41	Α	
		T <sub>s</sub> = 100 °C	34	Α	
I <sub>C</sub> λ <sub>pas</sub>	λ <sub>paste</sub> =2.5 W/(mK)	T <sub>s</sub> = 70 °C	50	Α	
	T <sub>j</sub> = 175 °C	T <sub>s</sub> = 100 °C	41	Α	
I <sub>Cnom</sub>		•	35	Α	
I <sub>CRM</sub>			70	Α	
$V_{GES}$			-20 20	V	
t <sub>psc</sub>	$V_{CC} = 800 \text{ V}$ $V_{GE} \le 15 \text{ V}$ $V_{CES} \le 1200 \text{ V}$	T <sub>j</sub> = 175 °C	7	μѕ	
Tj			-40 175	°C	
Inverse -	Diode				
$V_{RRM}$	T <sub>j</sub> = 25 °C		1200	V	
I <sub>F</sub>	λ <sub>paste</sub> =0.8 W/(mK)	T <sub>s</sub> = 70 °C	33	Α	
	T <sub>j</sub> = 175 °C	T <sub>s</sub> = 100 °C	27	Α	
l <sub>F</sub>	$\lambda_{paste}$ =2.5 W/(mK) T <sub>j</sub> = 175 °C	T <sub>s</sub> = 70 °C	39	Α	
		T <sub>s</sub> = 100 °C	32	Α	
I <sub>FRM</sub>			70	Α	
I <sub>FSM</sub>	t <sub>p</sub> = 10 ms, sin 180°, T <sub>j</sub> = 150 °C		170	Α	
Tj			-40 175	°C	
Module	•				
I <sub>t(RMS)</sub>	, ΔT <sub>terminal</sub> at PCB joint = 30 K, per pin		30	Α	
T <sub>stg</sub>	module without TIN	Л	-40 125	°C	
V <sub>isol</sub>	AC, sinusoidal, t =	1 min	2500	V	

Characte	ristics					
Symbol	Conditions	min.	typ.	max.	Unit	
Inverter -	IGBT					
V <sub>CE(sat)</sub>	I <sub>C</sub> = 35 A	T <sub>j</sub> = 25 °C		1.60	1.75	V
V <sub>GE</sub> = 15 V chiplevel	<u></u>	T <sub>j</sub> = 150 °C		1.78	1.93	V
	chiplevel	T <sub>j</sub> = 175 °C		1.82	1.97	V
V <sub>CE0</sub> chiple		T <sub>j</sub> = 25 °C		1.00	1.05	V
	chiplevel	T <sub>j</sub> = 150 °C		0.80	0.85	V
		T <sub>j</sub> = 175 °C		0.75	0.80	V
r <sub>CE</sub>	V 45.V	T <sub>j</sub> = 25 °C		17	20	mΩ
	V <sub>GE</sub> = 15 V chiplevel	T <sub>j</sub> = 150 °C		28	31	mΩ
		T <sub>j</sub> = 175 °C		31	33	mΩ
$V_{GE(th)}$	$V_{GE} = V_{CE}, I_{C} = 0.75 \text{ mA}$		5.15	5.8	6.45	V
I <sub>CES</sub>	V <sub>GE</sub> = 0 V, V <sub>CE</sub> = 1200 V, T <sub>j</sub> = 25 °C				1	mA
C <sub>ies</sub>	V <sub>CE</sub> = 25 V V <sub>GE</sub> = 0 V	f = 1 MHz		6.60		nF
C <sub>oes</sub>		f = 1 MHz		0.09		nF
C <sub>res</sub>		f = 1 MHz		0.02		nF
$Q_G$	V <sub>GE</sub> = -15 V +15 V			487		nC
R <sub>Gint</sub>	T <sub>j</sub> = 25 °C			0		Ω





## SEMITOP®E1

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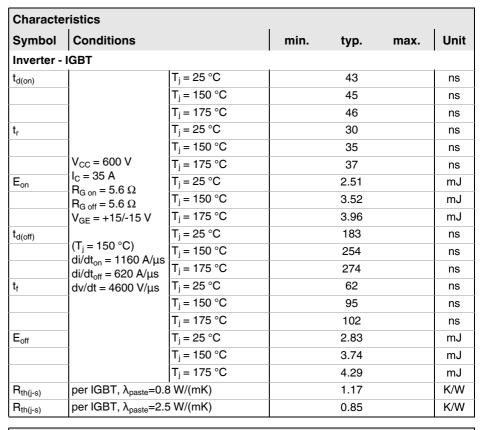
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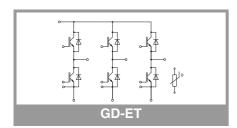
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Characte	eristics					
Symbol	Conditions		min.	typ.	max.	Unit
Inverse -	Diode					
$V_F = V_{EC}$	I <sub>F</sub> = 35 A	T <sub>j</sub> = 25 °C		2.30	2.62	V
		T <sub>j</sub> = 150 °C		2.29	2.62	V
	chiplevel	T <sub>j</sub> = 175 °C		2.14	2.46	V
$V_{F0}$		T <sub>j</sub> = 25 °C		1.30	1.50	V
	chiplevel	T <sub>j</sub> = 150 °C		0.90	1.10	V
		T <sub>j</sub> = 175 °C		0.82	0.98	V
r <sub>F</sub>	chiplevel	T <sub>j</sub> = 25 °C		29	32	mΩ
		T <sub>j</sub> = 150 °C		40	43	mΩ
		T <sub>j</sub> = 175 °C		38	42	mΩ
I <sub>RRM</sub>		T <sub>j</sub> = 25 °C		25		Α
		T <sub>j</sub> = 150 °C		31		Α
le -	I <sub>F</sub> = 35 A	T <sub>j</sub> = 175 °C		37		Α
Q <sub>rr</sub>	$V_{GE} = +15/-15 \text{ V}$ $V_{CC} = 600 \text{ V}$ $(T_j = 150 \text{ °C})$ $di/dt_{off} = 1030 \text{ A/µs}$	T <sub>j</sub> = 25 °C		2.15		μC
		T <sub>j</sub> = 150 °C		4.85		μC
		T <sub>j</sub> = 175 °C		5.48		μC
E <sub>rr</sub>		T <sub>j</sub> = 25 °C		1.46		mJ
		T <sub>j</sub> = 150 °C		2.39		mJ
		T <sub>j</sub> = 175 °C		3.65		mJ
R <sub>th(j-s)</sub>	per Diode, λ <sub>paste</sub> =0.8 W/(mK)			1.34		K/W
R <sub>th(j-s)</sub>	per Diode, λ <sub>paste</sub> =2.5 W/(mK)			1.01		K/W
Module	•					•
L <sub>CE</sub>				30		nH
Ms	to heatsink		1.6		2.3	Nm
W				25		g





Characteristics							
Symbol	Conditions	min.	typ.	max.	Unit		
Temperature Sensor							
R <sub>100</sub>	T <sub>c</sub> =100°C (R <sub>25</sub> =5 kΩ)	493 ± 5%		Ω			
B <sub>25/85</sub>	$R_{(T)}=R_{25}*exp[B_{25/85}*(1/T-1/298)], T[K]$	3420		K			

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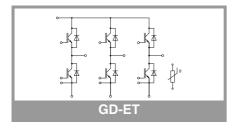
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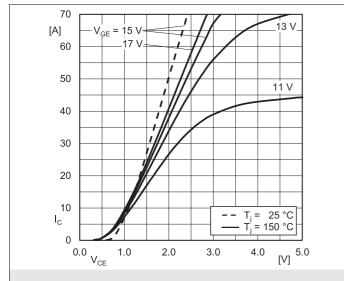


Fig. 1: Typ. IGBT output characteristic, incl. R<sub>CC+ EE</sub>

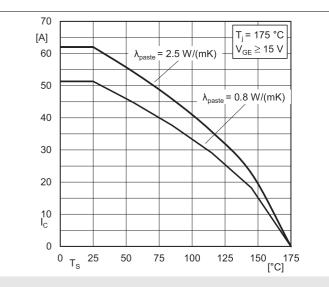


Fig. 2: IGBT rated current vs. temperature I<sub>c</sub>=f(T<sub>s</sub>)

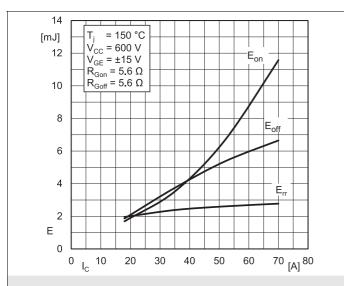


Fig. 3: Typ. turn-on /-off energy =  $f(I_C)$ 

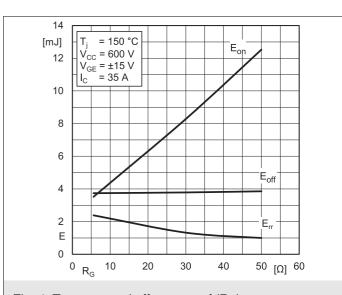


Fig. 4: Typ. turn-on /-off energy =  $f(R_G)$ 

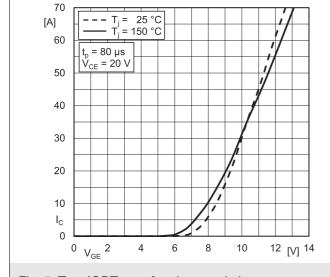


Fig. 5: Typ. IGBT transfer characteristic

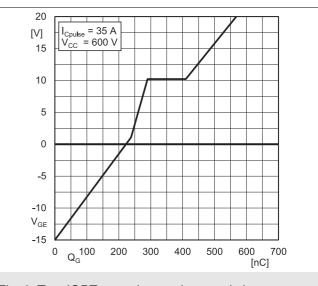
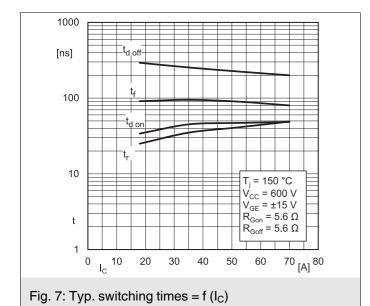
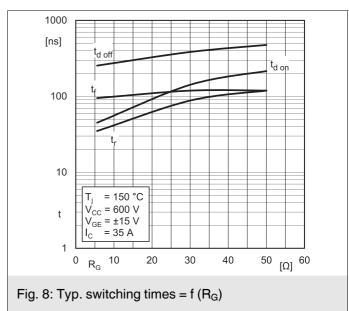
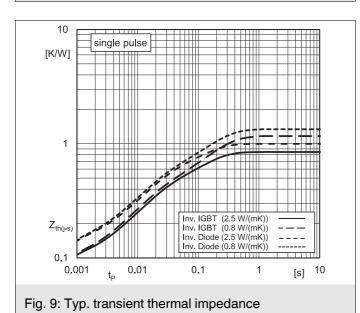
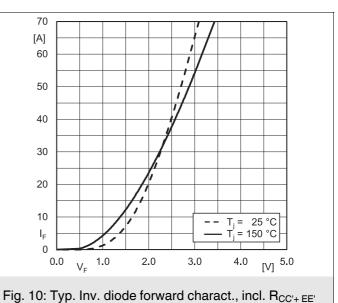


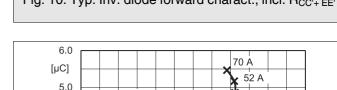
Fig. 6: Typ. IGBT gate charge characteristic











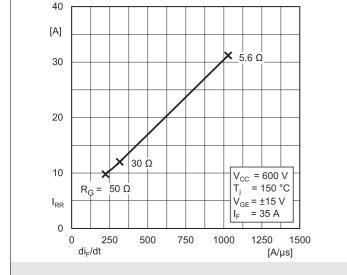


Fig. 11: Typ. Inv. diode peak reverse recovery current

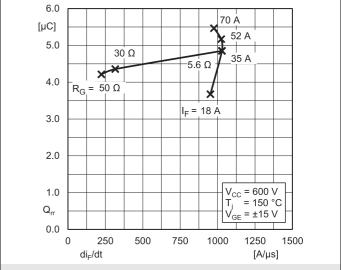
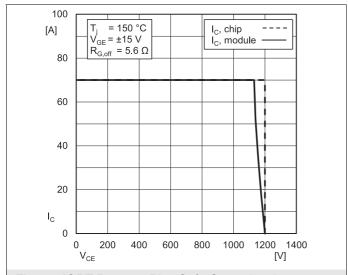
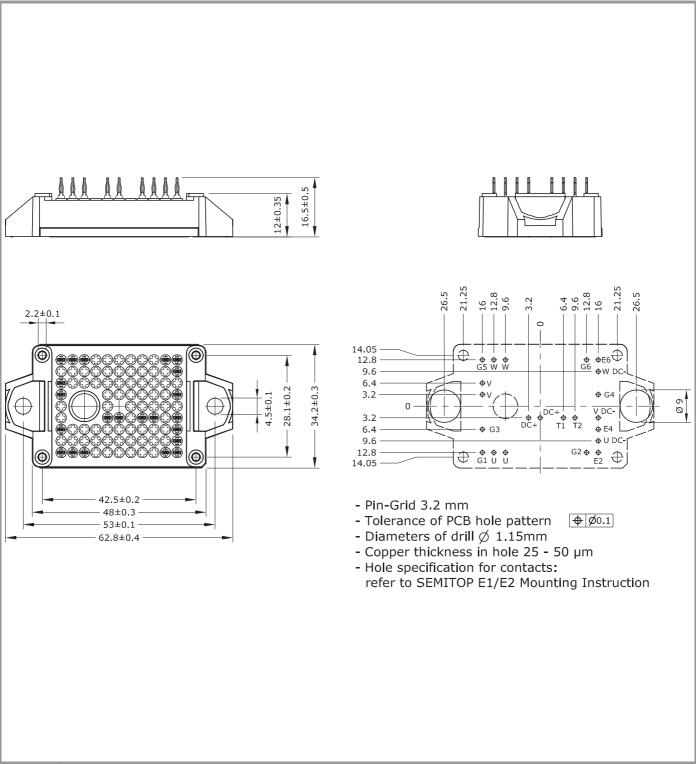
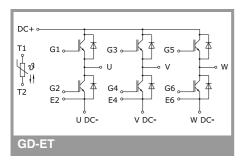


Fig. 12: Typ. Inv. diode reverse recovery charge





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This is an electrostatic discharge sensitive device (ESDS) due to international standard IEC 61340.

#### \*IMPORTANT INFORMATION AND WARNINGS

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