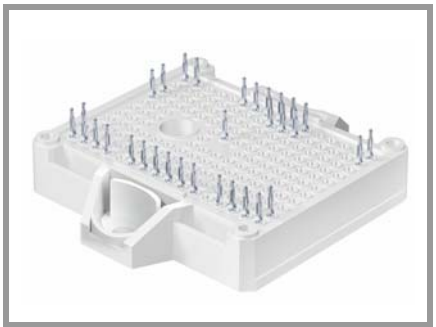
SK75GD12T7ETE2



SEMITOP®E2

Sixpack Open Emitter

SK75GD12T7ETE2

Features*

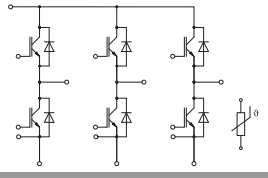
- Optimized design for superior thermal performance
- Low inductive design
- Press-Fit contact technology
- 1200V Generation 7 IGBT (T7)
- Robust and soft switching CAL4F diode technology
- Integrated NTC temperature sensor
- UL recognized file no. E 63 532

Typical Applications

- Motor drives
- Servo drives
- Air conditioning
- Auxiliary Inverters
- UPS

Remarks

- Recommended $T_{j,op}$ = -40 ...+150 °C
- $T_{j,op}$ > 150 °C during overload (details on AN19-002)



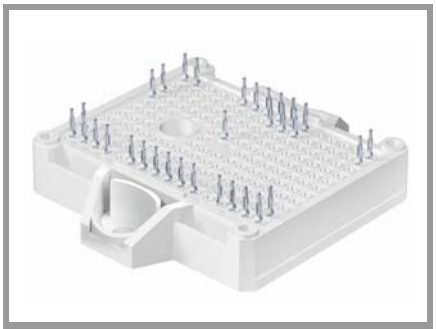
GD-ET

Absolute Maximum Ratings

Symbol	Conditions		Values	Unit
Inverter - IGBT				
V_{CES}	T_j = 25 °C		1200	V
I_C	λ_{paste}=0.8 W/(mK) T_j = 175 °C	T_s = 70 °C	75	A
		T_s = 100 °C	60	A
I_C	λ_{paste}=2.5 W/(mK) T_j = 175 °C	T_s = 70 °C	96	A
		T_s = 100 °C	78	A
I_{Cnom}			75	A
I_{CRM}			150	A
V_{GES}			-20 ... 20	V
t_{psc}	V_{CC} = 800 V $V_{GE} \leq$ 15 V $V_{CES} \leq$ 1200 V	T_j = 175 °C	7	µs
T_j			-40 ... 175	°C
Inverse - Diode				
V_{RRM}	T_j = 25 °C		1200	V
I_F	λ_{paste}=0.8 W/(mK) T_j = 175 °C	T_s = 70 °C	63	A
		T_s = 100 °C	50	A
I_F	λ_{paste}=2.5 W/(mK) T_j = 175 °C	T_s = 70 °C	81	A
		T_s = 100 °C	65	A
I_{FRM}			150	A
I_{FSM}	t_p = 10 ms, sin 180°, T_j = 150 °C		430	A
T_j			-40 ... 175	°C
Module				
$I_{t(RMS)}$	, $\Delta T_{terminal}$ at PCB joint = 30 K, per pin		30	A
T_{stg}	module without TIM		-40 ... 125	°C
V_{isol}	AC, sinusoidal, t = 1 min		2500	V

Characteristics

Symbol	Conditions		min.	typ.	max.	Unit
Inverter - IGBT						
$V_{CE(sat)}$	I_C = 75 A V_{GE} = 15 V chiplevel	T_j = 25 °C		1.55	1.70	V
		T_j = 150 °C		1.73	1.88	V
		T_j = 175 °C		1.77	1.92	V
V_{CE0}	chiplevel	T_j = 25 °C		1.00	1.05	V
		T_j = 150 °C		0.80	0.85	V
		T_j = 175 °C		0.75	0.80	V
r_{CE}	V_{GE} = 15 V chiplevel	T_j = 25 °C		7.3	8.7	mΩ
		T_j = 150 °C		12	14	mΩ
		T_j = 175 °C		14	15	mΩ
$V_{GE(th)}$	V_{GE} = V_{CE}, I_C = 1.7 mA		5.15	5.8	6.45	V
I_{CES}	V_{GE} = 0 V, V_{CE} = 1200 V, T_j = 25 °C				1	mA
C_{ies}	V_{CE} = 25 V V_{GE} = 0 V	f = 1 MHz		15.10		nF
C_{oes}		f = 1 MHz		0.19		nF
C_{res}		f = 1 MHz		0.54		nF
Q_G	V_{GE} = -15V...+15V			1218		nC
R_{Gint}	T_j = 25 °C			2.0		Ω



SEMITOP®E2

Sixpack Open Emitter

SK75GD12T7ETE2

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- Optimized design for superior thermal performance
- Low inductive design
- Press-Fit contact technology
- 1200V Generation 7 IGBT (T7)
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- Integrated NTC temperature sensor
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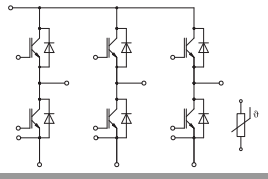
Typical Applications

- Motor drives
- Servo drives
- Air conditioning
- Auxiliary Inverters
- UPS

Remarks

- Recommended $T_{j,op}$ = -40 ...+150 °C
- $T_{j,op}$ > 150 °C during overload (details on AN19-002)



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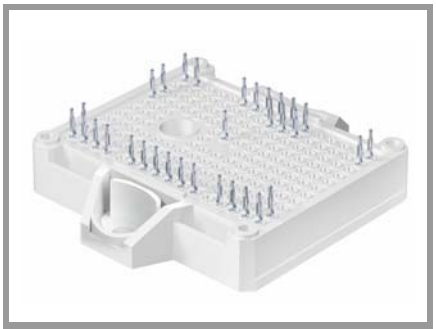
Characteristics

Symbol	Conditions		min.	typ.	max.	Unit
Inverter - IGBT						
$t_{d(on)}$	V_{CC} = 600 V I_C = 75 A $R_{G\ on}$ = 2.2 Ω $R_{G\ off}$ = 2.2 Ω V_{GE} = +15/-15 V (T_j = 150 °C) di/dt_{on} = 2400 A/µs di/dt_{off} = 700 A/µs dv/dt = 4000 V/µs	T_j = 25 °C		111		ns
		T_j = 150 °C		126		ns
		T_j = 175 °C		131		ns
t_r		T_j = 25 °C		25		ns
		T_j = 150 °C		31		ns
		T_j = 175 °C		33		ns
E_{on}		T_j = 25 °C		4.06		mJ
		T_j = 150 °C		7.06		mJ
		T_j = 175 °C		7.81		mJ
$t_{d(off)}$		T_j = 25 °C		231		ns
		T_j = 150 °C		307		ns
		T_j = 175 °C		325		ns
t_f		T_j = 25 °C		49		ns
		T_j = 150 °C		73		ns
		T_j = 175 °C		76		ns
E_{off}		T_j = 25 °C		4.62		mJ
		T_j = 150 °C		7.85		mJ
		T_j = 175 °C		8.65		mJ
$R_{th(j-s)}$	per IGBT, λ_{paste}=0.8 W/(mK)			0.73		K/W
$R_{th(j-s)}$	per IGBT, λ_{paste}=2.5 W/(mK)			0.49		K/W

Characteristics

Symbol	Conditions		min.	typ.	max.	Unit
Inverse - Diode						
$V_F = V_{EC}$	I_F = 75 A chiplevel	T_j = 25 °C		2.17	2.49	V
		T_j = 150 °C		2.11	2.42	V
		T_j = 175 °C		1.96	2.27	V
V_{F0}	chiplevel	T_j = 25 °C		1.30	1.50	V
		T_j = 150 °C		0.90	1.10	V
		T_j = 175 °C		0.82	0.98	V
r_F	chiplevel	T_j = 25 °C		12	13	mΩ
		T_j = 150 °C		16	18	mΩ
		T_j = 175 °C		15	17	mΩ
I_{RRM}	I_F = 75 A V_{GE} = +15/-15 V V_{CC} = 600 V (T_j = 150 °C) di/dt_{off} = 2600 A/µs	T_j = 25 °C		97		A
		T_j = 150 °C		136		A
		T_j = 175 °C		143		A
Q_{rr}		T_j = 25 °C		5.54		µC
		T_j = 150 °C		13.97		µC
		T_j = 175 °C		16.11		µC
E_{rr}		T_j = 25 °C		2.48		mJ
		T_j = 150 °C		6.44		mJ
		T_j = 175 °C		7.35		mJ
$R_{th(j-s)}$	per Diode, λ_{paste}=0.8 W/(mK)			0.82		K/W
$R_{th(j-s)}$	per Diode, λ_{paste}=2.5 W/(mK)			0.55		K/W
Module						
L_{CE}				40		nH
M_s	to heatsink		1.6		2.3	Nm
w				35		g



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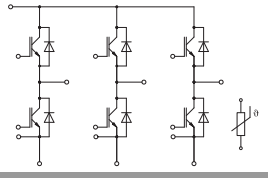
Remarks

- Recommended $T_{j,op}$ = -40 ...+150 °C
- $T_{j,op}$ > 150 °C during overload (details on AN19-002)

Characteristics					
Symbol	**Conditions**	**min.**	**typ.**	**max.**	**Unit**
Temperature Sensor					
R_{100}	T_c=100°C (R_{25}=5 kΩ)		493 ± 5%		Ω
$B_{25/85}$	$R_{(T)}=R_{25}*\exp[B_{25/85}*(1/T-1/298)]$, T[K]		3420		K



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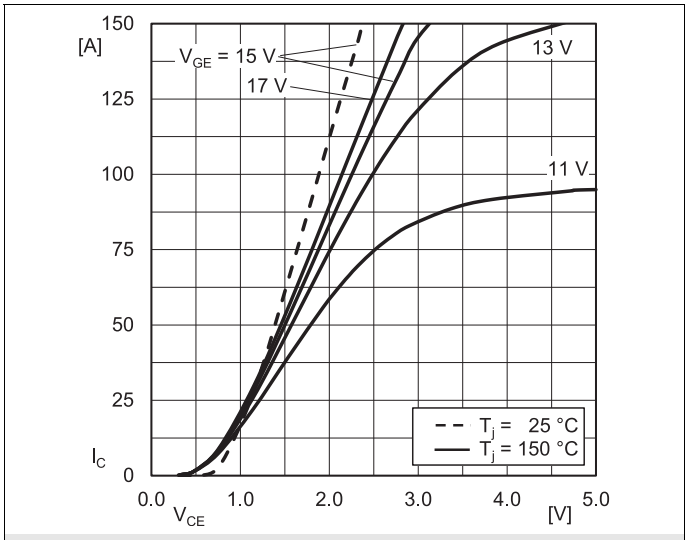


Fig. 1: Typ. IGBT output characteristic, incl. $R_{CC+ EE'}$

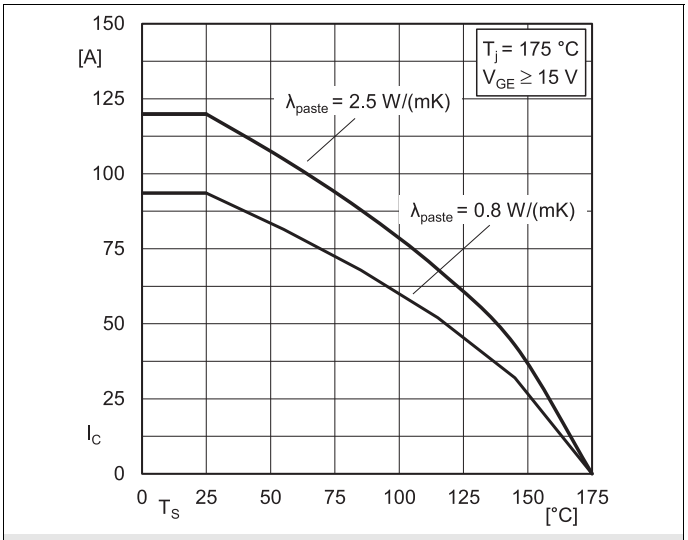


Fig. 2: IGBT rated current vs. temperature $I_c=f(T_s)$

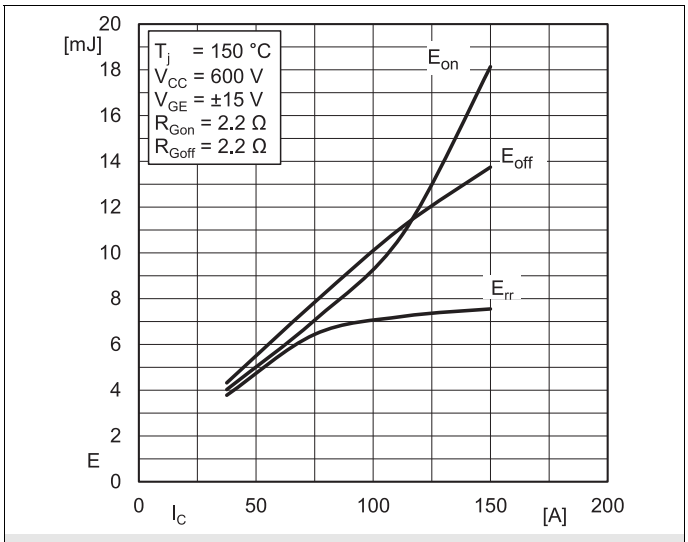


Fig. 3: Typ. turn-on /-off energy = f (I_C)

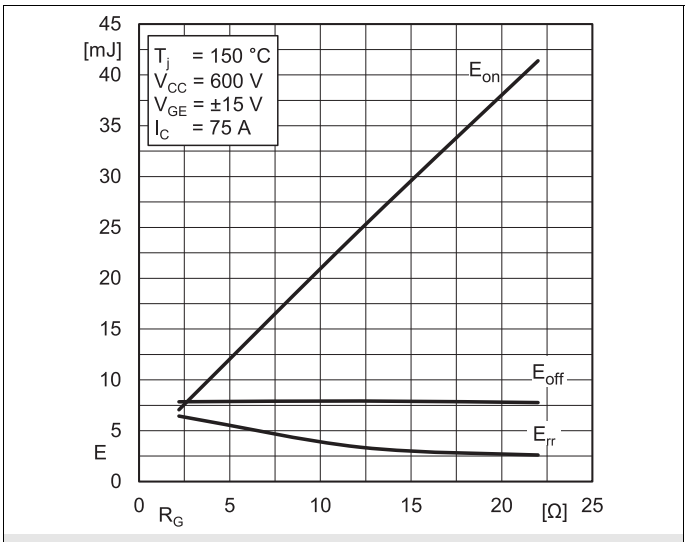


Fig. 4: Typ. turn-on /-off energy = f (R_G)

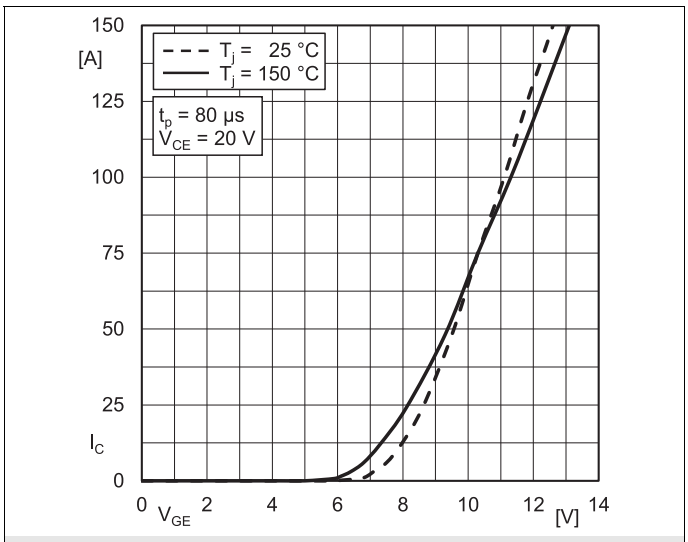


Fig. 5: Typ. IGBT transfer characteristic

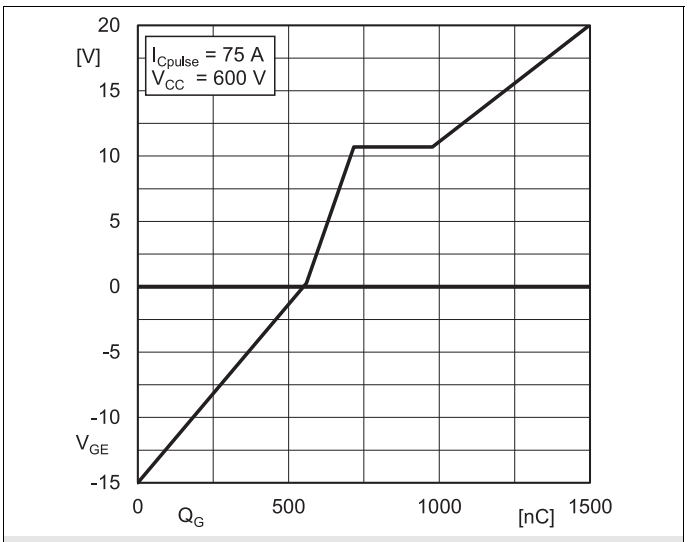


Fig. 6: Typ. IGBT gate charge characteristic

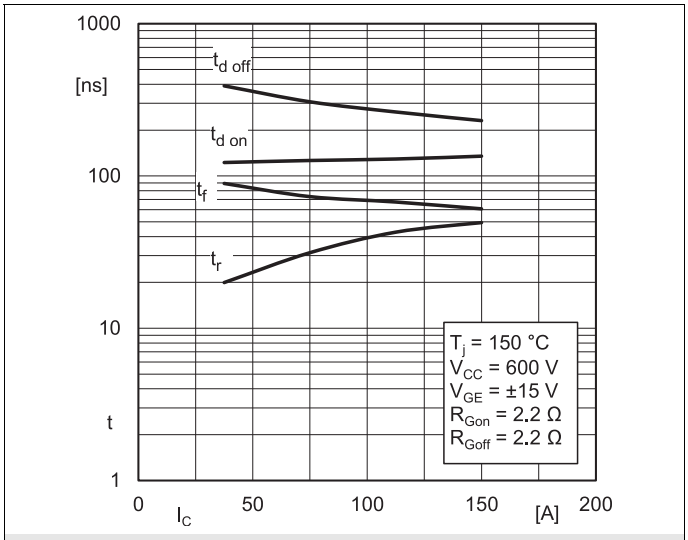


Fig. 7: Typ. switching times = f (I_C)

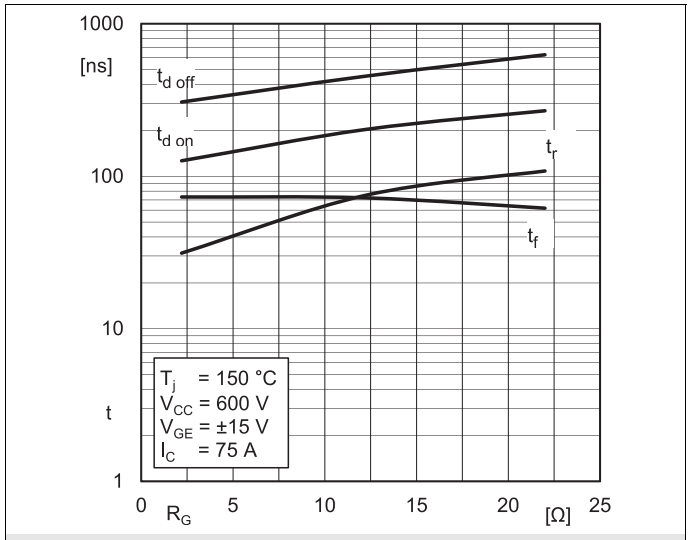


Fig. 8: Typ. switching times = f (R_G)

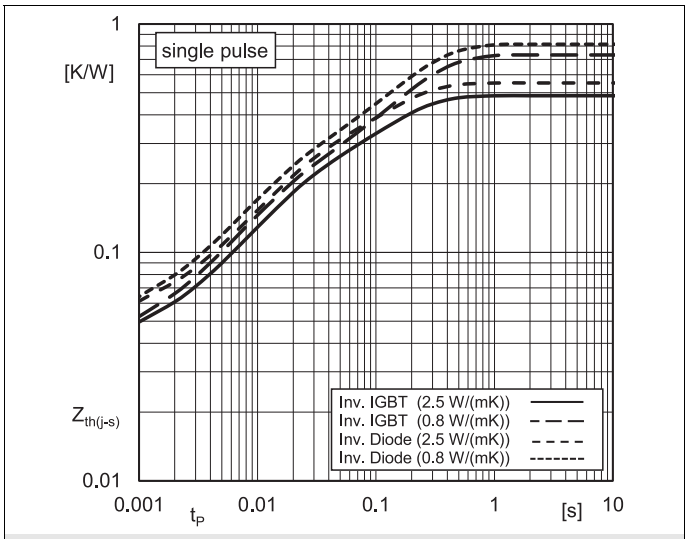


Fig. 9: Typ. transient thermal impedance

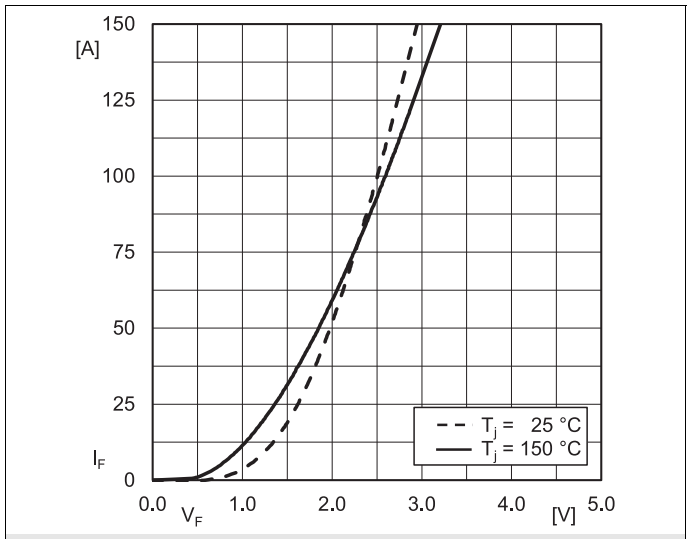


Fig. 10: Typ. Inv. diode forward charact., incl. $R_{CC'+EE'}$

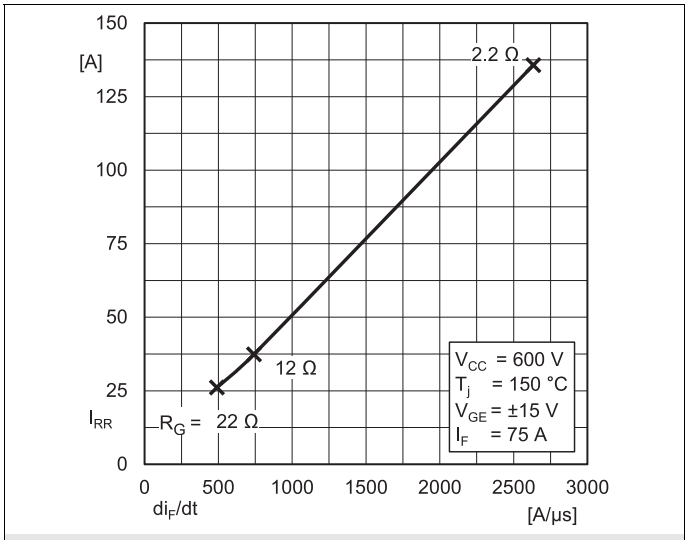


Fig. 11: Typ. Inv. diode peak reverse recovery current

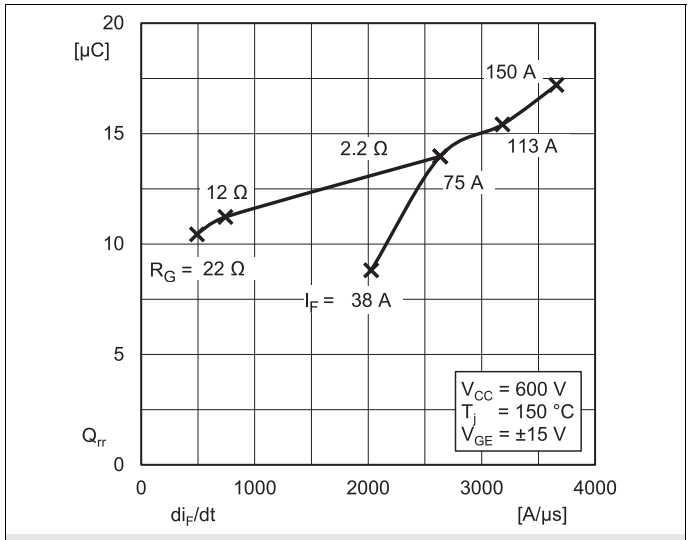


Fig. 12: Typ. Inv. diode reverse recovery charge

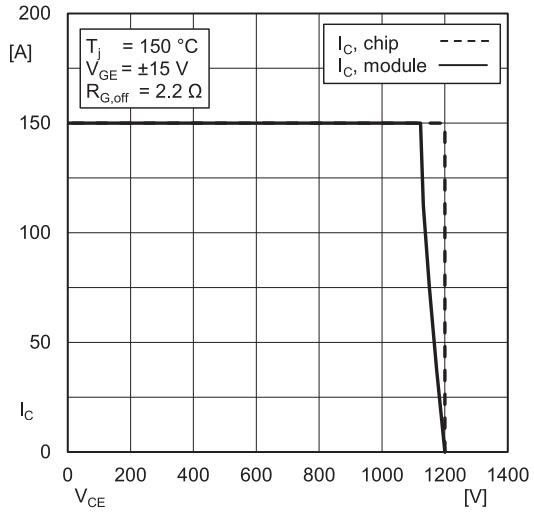
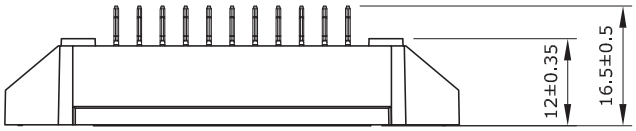
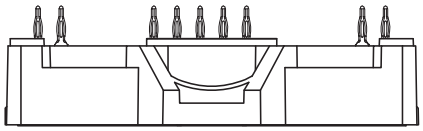
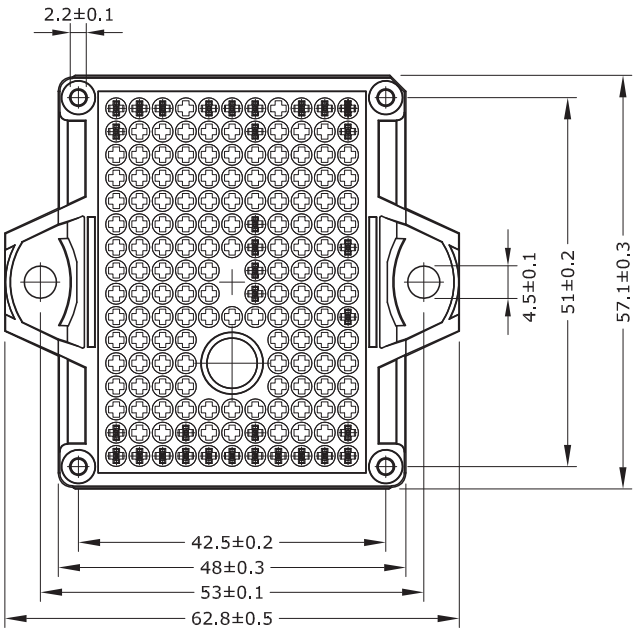
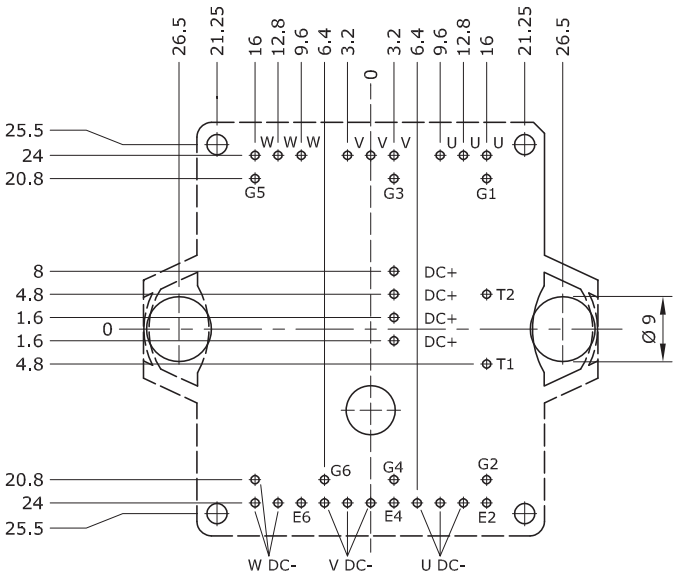


Fig. 13: IGBT Reverse Bias Safe Operating Area (RBSOA)



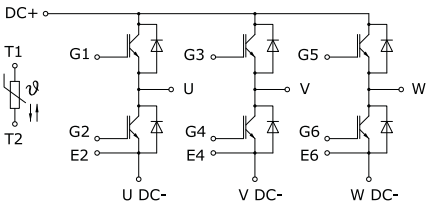






- Pin-Grid 3.2 mm
- Tolerance of PCB hole pattern ⌖ Ø0.1
- Diameters of drill ∅ 1.15mm
- Copper thickness in hole 25 - 50 µm
- Hole specification for contacts:
  refer to SEMITOP E1/E2 Mounting Instruction

SEMITOP®E2



GD-ET

This is an electrostatic discharge sensitive device (ESDS) due to international standard IEC 61340.

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