

SEMITOP[®] 2

Thyristor and Diode separated in the same housing

SK 100 TAE 12

Features*

- Compact design
- One screw mounting
- High current density due to double mesa technology
- Heat transfer and insulation through direct copper bonded aluminum oxide ceramic (DBC)
- Glass passivated thyristor chips
- High surge currents
- UL recognized, file no. E 63 532

Typical Applications

Controlled rectifier circuit

•	Solid	stat	e rel	ays	

Absolute Maximum Ratings						
Symbol	Conditions		Values	Unit		
Diode 1						
V _{RRM}	T _j = 25 °C		1200	V		
l _F	T _j = 150 °C	T _s = 25 °C T _s = 70 °C	253	Α		
		T _s = 70 °C	180	А		
I _{FSM}	10 ms, T _j = 150 °C		2300	Α		
i ² t	10 ms, T _j = 150 °C		26450	A ² s		
Tj			-40 150	°C		

Absolute Maximum Ratings

Symbol	Conditions	Values	Unit				
Thyristo	Thyristor 1						
V _{RRM}		1200	V				
V _{DRM}		1200	V				
I _{T(AV)}	T _j = 130 °C, T _s = 70 °C	96	А				
I _{TSM}	$t_p = 10 \text{ ms}, \sin 180^\circ, T_j = 25 \text{ °C}$	2000	Α				
i²t	$t_p = 10 \text{ ms}, \sin 180^\circ, T_j = 25 ^\circ\text{C}$	20000	A²s				
Tj		-40 130	°C				

Absolute Maximum Ratings					
Symbol	Conditions	Values	Unit		
Module					
I _{t(RMS)}	$\Delta T_{terminal}$ at PCB joint = 30 K, per pin	60	А		
T _{stg}	module without TIM	-40 125	°C		
V _{isol}	AC, sinusoidal, t = 1 min	2500	V		

Characteristics							
Symbol	Conditions		min.	typ.	max.	Unit	
Diode 1							
V _F	I _F = 160 A	T _j = 25 °C		1.00	1.21	V	
	chiplevel	T _j = 125 °C		0.90	1.10	V	
V _{F0}	chiplevel	T _j = 25 °C		0.88	0.98	V	
		T _j = 125 °C		0.73	0.83	V	
r _F	abialay al	T _j = 25 °C		0.75	1.44	mΩ	
	chiplevel	T _j = 125 °C		1.06	1.69	mΩ	
I _R	T _j = 120 °C, V _{RRM}				4	mA	
R _{th(j-s)}	per diode, λ_{paste} =0.8 W/(mK)			0.4		K/W	





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Typical Applications

- Controlled rectifier circuit
- Solid state relays

Symbol	Conditions	min.	typ.	max.	Unit
Thyristor	1				
V _T	T _j = 25 °C, I _T = 150 A			1.26	V
V _{T(TO)}	T _j = 130 °C			0.85	V
r _T	T _j = 130 °C			2.20	mΩ
I _{DD} ;I _{RD}	$T_j = 130 \ ^\circ C, V_{DD} = V_{DRM}; V_{RD} = V_{RRM}$			21	mA
t _{gd}	$T_j = 25 \text{ °C}, I_G = 1 \text{ A}, di_G/dt = 1 \text{ A}/\mu \text{s}$		1		μs
t _{gr}	V _D = 0.67 * V _{DRM}		2		μs
t _q	T _j = 130 °C		150		μs
I _H	T _j = 25 °C	220			mA
IL.	$T_j = 25 \ ^\circ C, R_G = 33 \ \Omega$	550			mA
V _{GT}	$T_{j} = 25 \ ^{\circ}C, \ d.c.$	2			V
I _{GT}	T _j = 25 °C, d.c.	100			mA
V _{GD}	T _j = 130 °C, d.c.			0.25	V
I _{GD}	T _j = 130 °C, d.c.			6	mA
R _{th(j-s)}	per thyristor, λ _{paste} =0.8 W/(mK), sin. 180°		0.45		K/W

Characteristics						
Symbol	Conditions	min.	typ.	max.	Unit	
Module					_	
Ms	to heatsink	1.8		2	Nm	
w	weight		19		g	











Fig. 2: Typ. transient thermal impedance vs. time



Fig. 4: Typ. Rect. characteristic of Diode, incl. $R_{CC'+\,EE'}$





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This is an electrostatic discharge sensitive device (ESDS) due to international standard IEC 61340.

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