

SEMITOP[®] 3

IGBT module

SK 120 GB 12F4 T

Features*

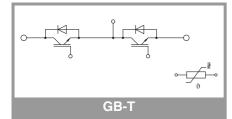
- Compact design
- One screw mounting module
- Optimum heat transfer and isolation through AIN direct copper bonding (DBC)
- Trench4 Fast IGBT technology
- CAL4F diode technology
- Integrated NTC temperature sensor
- UL recognized, file no. E 63 532

Typical Applications

- Switching (not for linear use)
- Inverter
- Switched mode power supplies
- UPS

Absolute	Maximum Ratings		
Symbol	Conditions	Values	Unit
Inverter -	IGBT		
V _{CES}	T _j = 25 °C	1200	V
I _C	$T_i = 175 ^{\circ}C$ $T_s = 25 ^{\circ}C$	C 174	А
	$T_{s} = 70^{\circ}$	C 143	А
I _{Cnom}		120	А
I _{CRM}		240	Α
V _{GES}		-20 20	V
t _{psc}		°C 10	μs
Tj		-40 175	°C
Inverse -	Diode		
V _{RRM}	T _j = 25 °C	1200	V
I _F	$T_s = 25^{\circ}$	C 29	А
	$T_j = 175 ^{\circ}C$ $T_s = 70 ^{\circ}C$	C 24	А
I _{FRM}		30	А
I _{FSM}	10 ms, sin 180°, T _j = 150 °C	65	A
Tj		-40 175	°C
Module		·	
I _{t(RMS)}	$\Delta T_{terminal}$ at PCB joint = 30 K	, per pin 60	А
T _{stg}	module without TIM	-40 125	°C
Visol	AC, sinusoidal, t = 1 min	2500	V

Characteristics Symbol Conditions min. Unit typ. max. **Inverter - IGBT** I_C = 120 A $T_i = 25 \circ C$ V_{CE(sat)} 2.05 2.40 ٧ $V_{GE} = 15 V$ T_i = 150 °C 2.59 2.85 V chiplevel T_i = 25 °C V_{CE0} 0.80 0.90 v chiplevel T_i = 150 °C 0.70 0.80 V T_i = 25 °C 10 13 mΩ r_{CE} $V_{GF} = 15 V$ chiplevel T_i = 150 °C 17 16 mΩ V_{GE(th)} $V_{GE} = V_{CE}$, $I_C = 4.5$ mA 5.2 5.8 6.4 V V_{GE} = 0 V, V_{CE} = 1200 V, T_j = 25 °C ICES 1.6 mA Cies f = 1 MHz 6.90 nF $V_{CE} = 25 V$ f = 1 MHz Coes nF 0.56 $V_{GE} = 0 V$ Cres f = 1 MHz0.41 nF Q_{G} V_{GE} = - 15 V...+ 15 V 412 nC T_i = 25 °C R_{Gint} 2.7 Ω 156 T_i = 150 °C $V_{CC} = 600 V$ ns t_{d(on)} I_C = 120 A T_i = 150 °C 51 tr ns $R_{G \text{ on}} = 2.2 \Omega$ E_{on} T_i = 150 °C 8.8 mJ $R_{G off} = 2.2 \Omega$ T_j = 150 °C 346 ns $t_{d(off)}$ di/dt_{on} = 2354 A/µs di/dt_{off} = 2264 A/µs T_j = 150 °C 42 tf ns T_i = 150 °C V_{GE} = +15/-15 V $\mathsf{E}_{\mathsf{off}}$ 7.47 mJ R_{th(j-s)} per IGBT, λ_{paste}=0.8 W/(mK) 0.22 K/W





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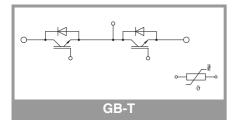
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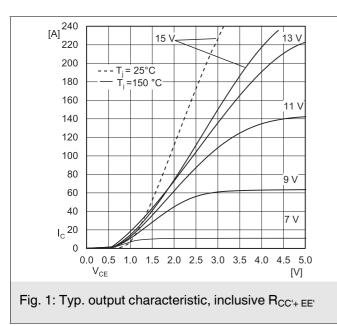
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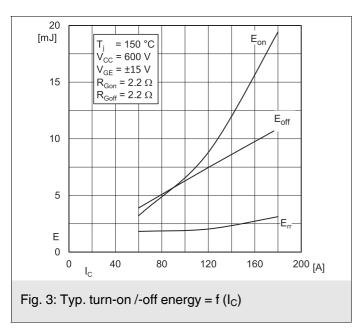
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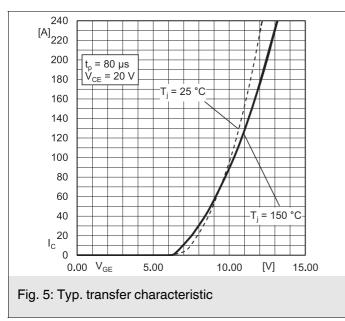
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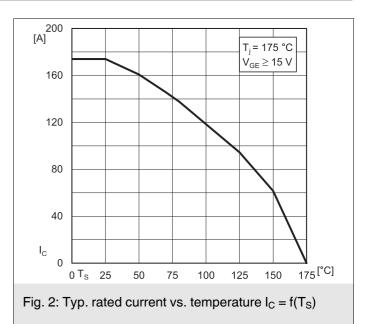
Characte	eristics					
Symbol	Conditions		min.	typ.	max.	Unit
Inverse -	Diode					
$V_F = V_{EC}$	I _F = 15 A	T _j = 25 °C		2.38	2.71	V
	chiplevel	T _j = 150 °C		2.44	2.77	V
V _{F0}	chiplevel	T _j = 25 °C		1.30	1.50	V
		T _j = 150 °C		0.90	1.10	V
r _F	chiplevel	T _j = 25 °C		72	81	mΩ
		T _j = 150 °C		103	111	mΩ
I _{RRM}	di/dt _{off} = 2350 A/µs	T _j = 150 °C		43.4		Α
Q _{rr}		T _j = 150 °C		5.7		μC
E _{rr}	V _{GE} = -15 V V _{CC} = 600 V	T _j = 150 °C		2.04		mJ
R _{th(j-s)}	per diode, $\lambda_{paste}=0.8$		1.25		K/W	
Module	·					
L _{CE}				-		nH
Ms	to heatsink		2.25		2.5	Nm
w				29		g
Temperat	ture Sensor					
R ₁₀₀	T _c =100°C (R ₂₅ =5 kΩ)			493 ± 5%		Ω
B _{100/125}	R _(T) =R ₁₀₀ exp[B _{100/1}		3550 ±2%		к	

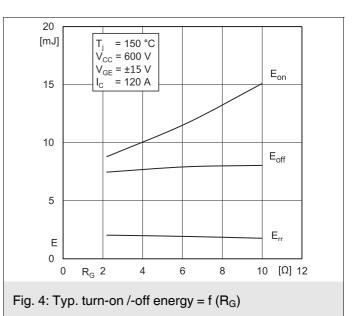


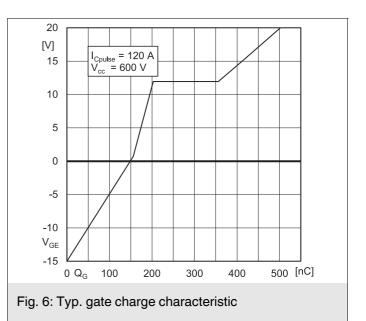


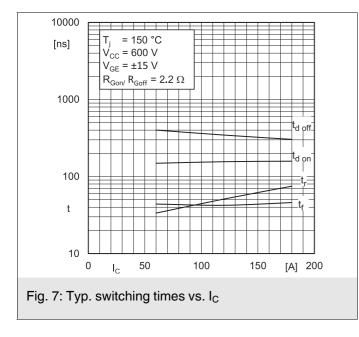


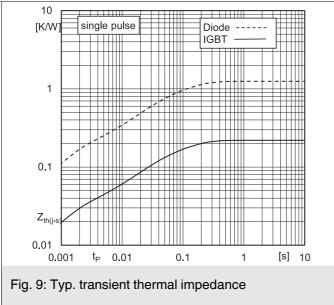


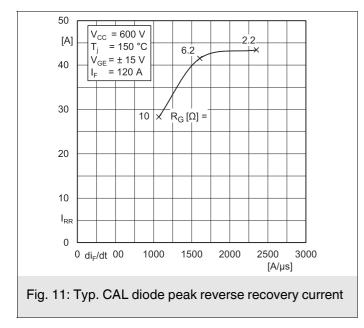












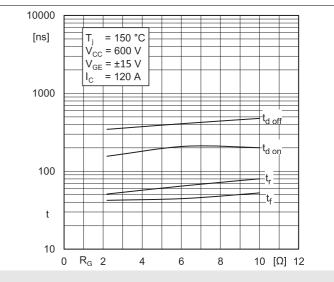
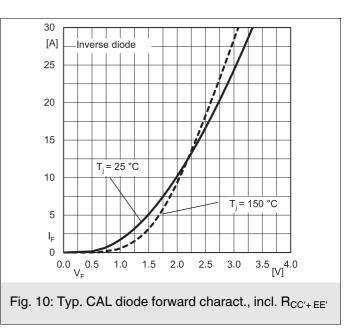
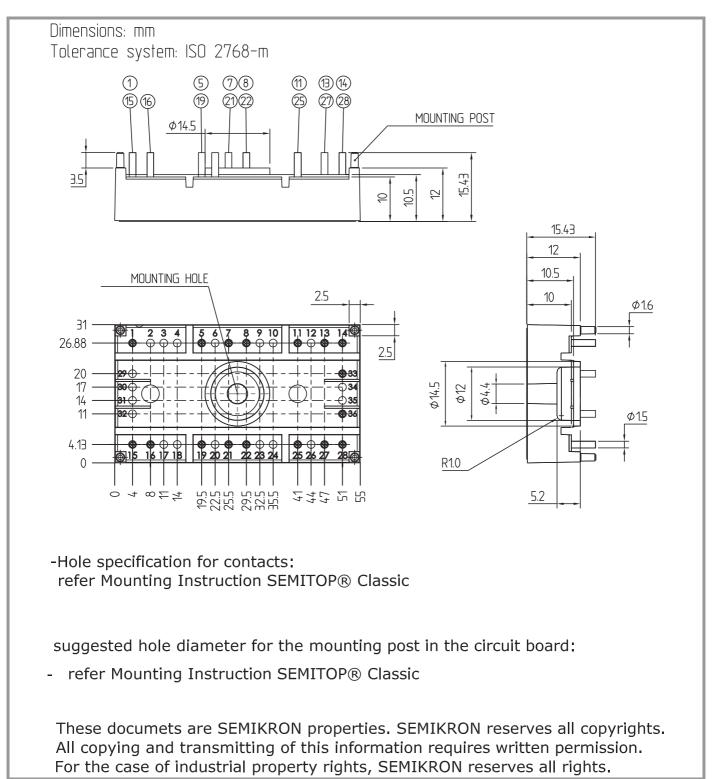
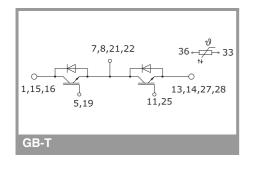


Fig. 8: Typ. switching times vs. gate resistor R_G





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This is an electrostatic discharge sensitive device (ESDS) due to international standard IEC 61340.

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