

## SEMITOP®E2

Half-Bridge (Full SiC)

#### SK150MB120CR03TE2

#### Features\*

- Optimized design for superior thermal performance
- Extremely low inductance design
- Press-Fit contact technology
- 1200V Planar Gen3 SiC MOSFET
- Simple to drive with +15V gate voltage
- Optimized switching stability thanks to module integrated gate resistors
- Integrated NTC temperature sensor
- UL recognized file no. E 63 532

### **Typical Applications**

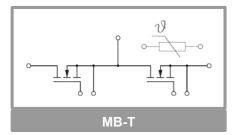
- Switched Mode Power Supplies
- Energy Storage Systems
- Electric Vehicle charging
- UPS
- Solar

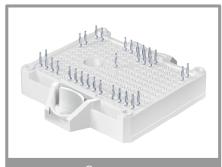
### Remarks

- Recommended  $T_{jop}$  = -40°C...+150°C
- Recommended turn-off / turn-on gate voltage V<sub>GS</sub> = -4...0/+15V

Absolute Maximum Ratings						
Symbol	Conditions		Values	Unit		
MOSFET						
$V_{DSS}$	T <sub>j</sub> = 25 °C		1200	V		
I <sub>D</sub>	HPTP / HP-PCM Tj = 175 °C	T <sub>s</sub> = 25 °C	185	Α		
		T <sub>s</sub> = 70 °C	154	Α		
I <sub>DM</sub>	Pulse witdh t <sub>p</sub> limited by T <sub>vjmax</sub>		480	Α		
$V_{GS}$	Transient Gate - Source voltage (t<100ns)		-8 19	\ \		
Tj			-40 175	°C		
Integrate	d body diode					
I <sub>FM</sub>	Pulse width t <sub>p</sub> limited by T <sub>vjmax</sub>		480	Α		
I <sub>FSM</sub>	$t_p = 10 \text{ ms, sin } 180^\circ, T_j = 150 ^\circ\text{C}$		807	Α		

Absolute Maximum Ratings						
Symbol	Conditions	Values	Unit			
Module						
I <sub>t(RMS)</sub>	ΔT <sub>terminal</sub> at PCB joint = 30 K, per pin	30	Α			
T <sub>stg</sub>	module without TIM	-40 125	°C			
V <sub>isol</sub>	AC, sinusoidal, t = 1 min	2500	V			





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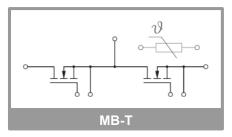
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Characteristics							
Symbol	Conditions		min.	typ.	max.	Unit	
MOSFET							
V <sub>(BR)DSS</sub>	V <sub>GS</sub> = 0 V, I <sub>D</sub> = 0.4 mA, T <sub>j</sub> = 25 °C		1200			٧	
V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_{D} = 46 I$	mA, T <sub>j</sub> = 25 °C	1.8	2.5	3.6	V	
I <sub>DSS</sub>	V <sub>GS</sub> = 0 V, V <sub>DS</sub> = 1200 V, T <sub>j</sub> = 25 °C				0.4	mA	
$I_{GSS}$	V <sub>DS</sub> = 0 V, V <sub>GS</sub> = 15 V, T <sub>j</sub> = 25 °C				400	nA	
D	V <sub>GS</sub> = 15 V,	T <sub>j</sub> = 25 °C		8.0	11	mΩ	
R <sub>DS(on)</sub>	I <sub>D</sub> = 166 A, chiplevel	T <sub>j</sub> = 150 °C		13		mΩ	
C <sub>iss</sub>	V <sub>GS</sub> = 0 V,	f = 0.1 MHz		13600		pF	
Coss	$V_{DS} = 1000 V$ ,	f = 0.1 MHz		520		pF	
C <sub>rss</sub>	T <sub>j</sub> = 25 °C	f = 0.1 MHz		40		pF	
$Q_G$	$V_{GS} = -415V, V_{DI}$	$_{\rm D}$ = 800V, $I_{\rm D}$ = 166 A		472		nC	
R <sub>Gint</sub>	T <sub>j</sub> = 25 °C			3.4		Ω	
t <sub>d(on)</sub>	$V_{DD} = 600 \text{ V}$	T <sub>j</sub> = 150 °C		49		ns	
$t_{\text{d(off)}}$	$I_D = 160 A$	T <sub>j</sub> = 150 °C		120		ns	
t <sub>r</sub>	$\begin{array}{l} V_{GS} = -4/+15 \ V \\ R_{Gon} = 0.8 \ \Omega \\ R_{Goff} = 0.8 \ \Omega \\ di/dt_{off} = 10 \ kA/\mu s \\ di/dt_{on} = 15 \ kA/\mu s \\ dv/dt = 27 \ kV/\mu s \\ L_s = 5 \ nH \end{array}$	T <sub>j</sub> = 150 °C		17		ns	
t <sub>f</sub>		T <sub>j</sub> = 150 °C		29		ns	
E <sub>on</sub>		T <sub>j</sub> = 150 °C		1.98		mJ	
E <sub>off</sub>		T <sub>j</sub> = 150 °C		1.71		mJ	
R <sub>th(j-s)</sub>	per MOSFET, HPTP / HP-PCM			0.27		K/W	
Integrate	d body diode						
\	-I <sub>D</sub> = 83 A V <sub>GS</sub> = -4 V	T <sub>j</sub> = 25 °C		4.6		V	
$V_F = V_{SD}$	chiplevel	T <sub>j</sub> = 150 °C		4.3		V	
\/ -\/	chiplevel	T <sub>j</sub> = 25 °C		3.8		V	
V <sub>F0</sub> - V <sub>SD0</sub>		T <sub>j</sub> = 150 °C		3.6		V	
r - r	chiplevel	T <sub>j</sub> = 25 °C		9.7		mΩ	
$r_F = r_{SD}$		T <sub>j</sub> = 150 °C		8.5		mΩ	
t <sub>rr</sub>	$V_{DD} = 600 \text{ V}$ $-I_{D} = 160 \text{ A}$ $V_{GS} = -4 \text{ V}$ $R_{Gon} = 0.8 \Omega$	T <sub>j</sub> = 150 °C		40		μs	
Qrr		T <sub>j</sub> = 150 °C		4.5		μC	
Irr		T <sub>j</sub> = 150 °C		225		Α	
Err	$di/dt_{off} = 16 \text{ kA/}\mu\text{s}$	T <sub>j</sub> = 150 °C		1.48		mJ	

Characteristics					
Symbol	Conditions	min.	typ.	max.	Unit
Module					
L <sub>CE</sub>			6		nΗ
Ms	to heatsink	1.6		2.3	Nm
W	weight		35		g

Characteristics						
Symbol	Conditions	min.	typ.	max.	Unit	
Temperature Sensor						
R <sub>100</sub>	T <sub>r</sub> = 100 °C	493 ± 5%			Ω	
B <sub>100/125</sub>	$R_{(T)} = R_{100} * \exp[B_{100/125} * (1/T-1/T_{100})], T[K];$		3550 (± 2%)		K	



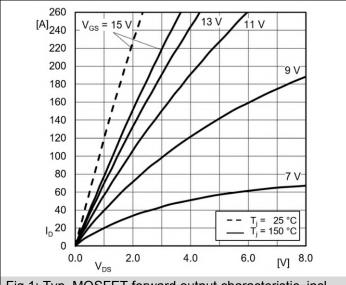


Fig.1: Typ. MOSFET forward output characteristic, incl.

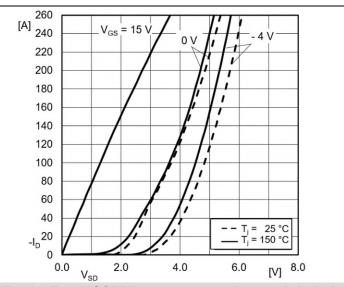


Fig. 1a: Typ. MOSFET reverse output characteristic, incl.

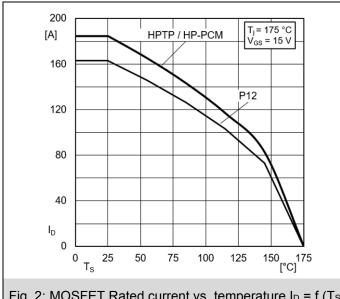


Fig. 2: MOSFET Rated current vs. temperature  $I_D = f(T_S)$ 

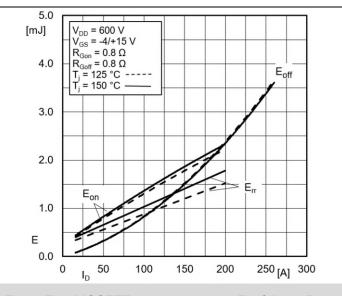
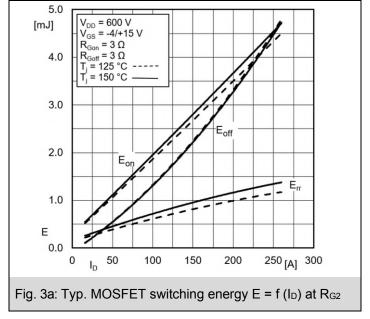
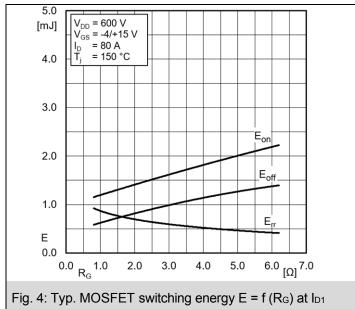
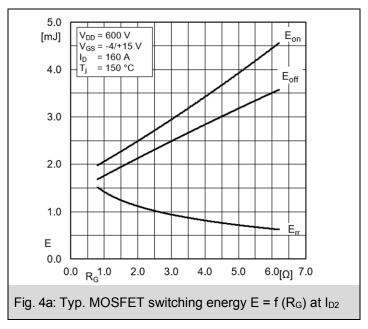
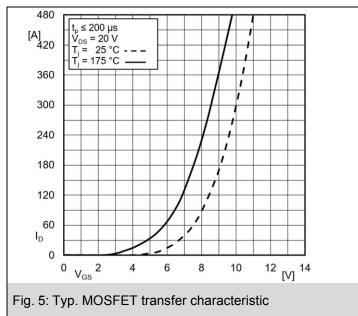


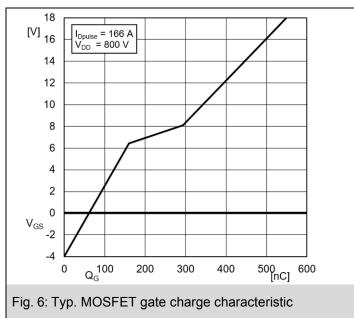
Fig. 3: Typ. MOSFET switching energy E = f (I<sub>D</sub>) at R<sub>G1</sub>

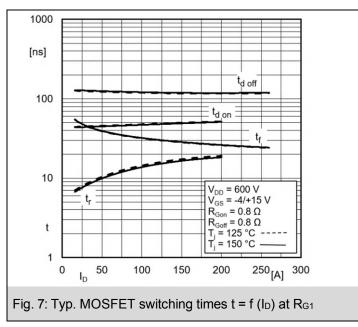


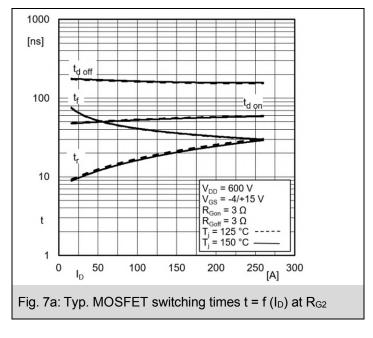


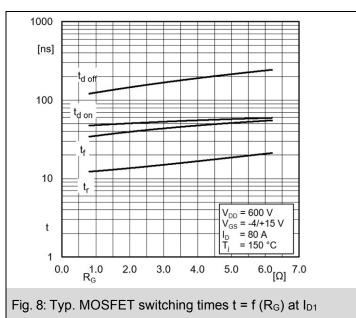


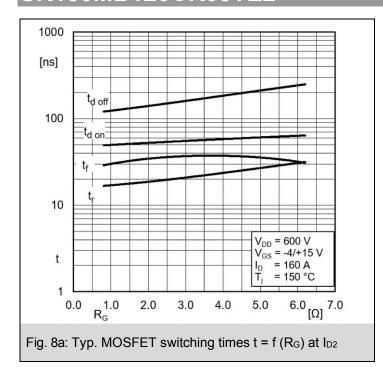












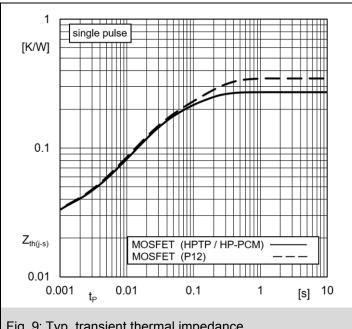


Fig. 9: Typ. transient thermal impedance

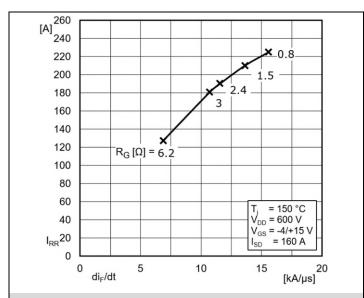


Fig. 10: Typ. body diode peak reverse recovery current  $I_{RR} = f (di_F/dt)$ 

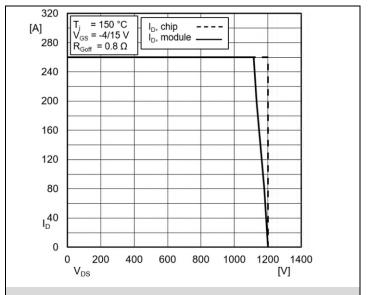
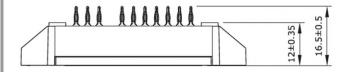
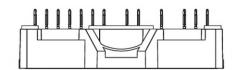
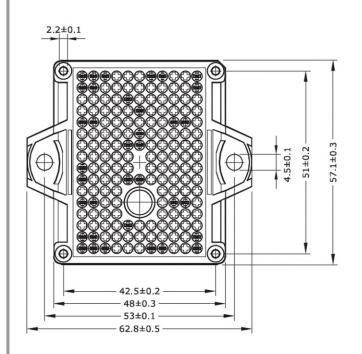
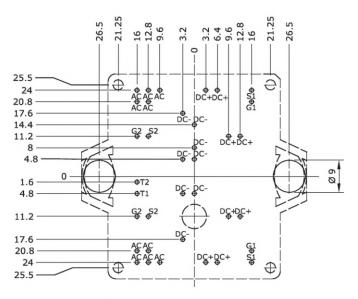


Fig. 11: MOSFET Reverse Bias Safe Operating Area (RBSOA)



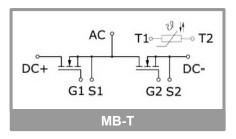






- Pin-Grid 3.2 mm
- Tolerance of PCB hole pattern  $\Phi$  Ø0.1
- Diameters of drill  $ot \emptyset$  1.15mm
- Copper thickness in hole 25 50  $\mu m$
- Hole specification for contacts: refer to SEMITOP E1/E2 Mounting Instruction

### **Pinout and Dimensions**



This is an electrostatic discharge sensitive device (ESDS) due to international standard IEC 61340.

#### \*IMPORTANT INFORMATION AND WARNINGS

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