

# MiniSKiiP® 2

## Twelvepack Open Emitter

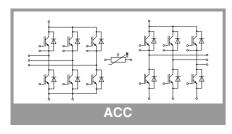
#### SKiiP 22ACC12T7V22

#### Features\*

- 1200V Generation 7 IGBTs (T7)
- Robust and soft switching freewheeling diodes in CAL technology
- New SKR PEP diode technology for enhanced power and environmental robustness
- Highly reliable spring contacts for electrical connections
- UL recognized: File no. E63532
- NTC T-Sensor

### Remarks

- Max. case temperature limited to TC=TS=125 °C
- Product reliability results valid for Tj≤150 °C; Tj,op >150°C during overload (Details see AN19-002)
- MiniSKiiP "Technical Explanations" and "Mounting Instructions" are part of the data sheet. Please refer to both documents for further information.
- For storage and case temperature with TIM see document "Technical Explanations Thermal Interface Materials"
- All diagrams refer to IGBT 7-12 and Diode 7-12



Absolute	e Maximum Ratings	<b>S</b>		
Symbol	Conditions		Values	Unit
IGBT 1 -			1 4.4.00	•
V <sub>CES</sub>	T <sub>i</sub> = 25 °C		1200	V
Ic	$\lambda_{\text{paste}} = 0.8 \text{ W/(mK)}$	T <sub>s</sub> = 70 °C	23	A
10	$T_i = 175 ^{\circ}\text{C}$	T <sub>s</sub> = 100 °C	19	A
I <sub>C</sub>	λ <sub>paste</sub> =2.5 W/(mK)	T <sub>s</sub> = 70 °C	26	A
	$T_i = 175 ^{\circ}\text{C}$	T <sub>s</sub> = 100 °C	21	A
I <sub>Cnom</sub>	,	1.5	15	A
I <sub>CRM</sub>			30	A
V <sub>GES</sub>			-20 20	V
GLO	V <sub>CC</sub> = 800 V			
t <sub>psc</sub>	$V_{GE} \le 15 \text{ V}$ $V_{CES} \le 1200 \text{ V}$	T <sub>j</sub> = 175 °C	7	μs
$T_j$			-40 175	°C
IGBT 7 -	12			
$V_{\text{CES}}$	T <sub>j</sub> = 25 °C		1200	V
Ic	λ <sub>paste</sub> =0.8 W/(mK)	T <sub>s</sub> = 70 °C	56	Α
	T <sub>j</sub> = 175 °C	T <sub>s</sub> = 100 °C	46	Α
I <sub>C</sub>	λ <sub>paste</sub> =2.5 W/(mK)	T <sub>s</sub> = 70 °C	64	Α
	T <sub>j</sub> = 175 °C	T <sub>s</sub> = 100 °C	52	Α
I <sub>Cnom</sub>			50	Α
I <sub>CRM</sub>			100	Α
$V_{GES}$			-20 20	V
t <sub>psc</sub>	$V_{CC} = 800 \text{ V}$ $V_{GE} \le 15 \text{ V}$ $V_{CES} \le 1200 \text{ V}$	T <sub>j</sub> = 175 °C	7	μs
T <sub>j</sub>	020		-40 175	°C
Diode 1 -	- 6			<u> </u>
$V_{RRM}$	T <sub>i</sub> = 25 °C		1600	V
I <sub>F</sub>	λ <sub>paste</sub> =0.8 W/(mK)	T <sub>s</sub> = 70 °C	44	Α
	T <sub>j</sub> = 175 °C	T <sub>s</sub> = 100 °C	35	Α
IF	$\lambda_{\text{paste}}$ =2.5 W/(mK)	T <sub>s</sub> = 70 °C	49	Α
	T <sub>j</sub> = 175 °C	T <sub>s</sub> = 100 °C	39	Α
I <sub>FRM</sub>		<u>I</u>		Α
I <sub>FSM</sub>	10 ms, sin 180°, T <sub>i</sub>	= 150 °C	200	Α
T <sub>j</sub>			-40 175	°C
Diode 7 -	- 12			
V <sub>RRM</sub>	T <sub>i</sub> = 25 °C		1200	V
I <sub>F</sub>	λ <sub>paste</sub> =0.8 W/(mK)	T <sub>s</sub> = 70 °C	24	Α
	$T_j = 175 ^{\circ}\text{C}$	T <sub>s</sub> = 100 °C	20	Α
l <sub>F</sub>	λ <sub>paste</sub> =2.5 W/(mK)	T <sub>s</sub> = 70 °C	27	Α
	T <sub>j</sub> = 175 °C	T <sub>s</sub> = 100 °C	22	А
I <sub>FRM</sub>		1	50	Α
I <sub>FSM</sub>	10 ms, sin 180°, T <sub>j</sub>	= 150 °C	100	Α
Tj	,		-40 175	°C
Module	1		ı	ı
I <sub>t(RMS)</sub>	20 A per spring		40	Α
-\			-	
T <sub>stg</sub>	module without TIN	1	-40 125	°C



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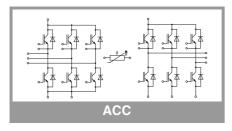
#### SKiiP 22ACC12T7V22

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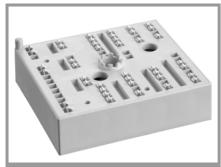
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Characte	eristics					
Symbol	Conditions		min.	tun	may	Unit
IGBT 1 - 6			111111.	typ.	max.	Ollit
	1	T <sub>i</sub> = 25 °C	1	1.60	1.75	V
V <sub>CE(sat)</sub>	I <sub>C</sub> = 15 A V <sub>GE</sub> = 15 V	T <sub>i</sub> = 150 °C		1.78	1.73	V
	chiplevel	T <sub>i</sub> = 175 °C				V
V	1	$T_i = 175 \text{ C}$ $T_i = 25 \text{ °C}$		1.82	1.97	V
V <sub>CE0</sub>	ahinlayal	T <sub>i</sub> = 150 °C		1.00	1.05	V
	chiplevel	,		0.80	0.85	
		T <sub>j</sub> = 175 °C		0.76	0.81	V
r <sub>CE</sub>	V <sub>GE</sub> = 15 V	T <sub>j</sub> = 25 °C		40	47	mΩ
	chiplevel	T <sub>j</sub> = 150 °C		65	72	mΩ
1.7	N N N I	T <sub>j</sub> = 175 °C	- 45	70	77	mΩ
V <sub>GE(th)</sub>	$V_{GE} = V_{CE} V, I_{C} =$		5.15	5.8	6.45	V
I <sub>CES</sub>	$V_{GE} = 0 \text{ V}, V_{CE} = 1$	_ <del>'</del>		0.00	1	mA
C <sub>ies</sub>	V <sub>CE</sub> = 25 V	f = 1 MHz		2.80		nF
C <sub>oes</sub>	$V_{GE} = 0 V$	f = 1 MHz		0.04		nF nF
C <sub>res</sub>		f = 1 MHz				
Q <sub>G</sub>	V <sub>GE</sub> = - 8V + 15	V		210		nC
R <sub>Gint</sub>	T <sub>j</sub> = 25 °C	I <del>-</del>		0		Ω
t <sub>d(on)</sub>		T <sub>j</sub> = 25 °C		-		ns
	_	T <sub>j</sub> = 150 °C		-		ns
	_	T <sub>j</sub> = 175 °C		-		ns
t <sub>r</sub>		T <sub>j</sub> = 25 °C		-		ns
	V <sub>CC</sub> = 600 V	T <sub>j</sub> = 150 °C		-		ns
	I <sub>C</sub> = 15 A	T <sub>j</sub> = 175 °C		-		ns
E <sub>on</sub>	_	T <sub>j</sub> = 25 °C		-		mJ
	V <sub>GE</sub> = +15/-15 V	T <sub>j</sub> = 150 °C		-		mJ
	VGE - +15/-15 V	T <sub>j</sub> = 175 °C		-		mJ
t <sub>d(off)</sub>		T <sub>j</sub> = 25 °C		-		ns
	@ T <sub>j</sub> = 150 °C:	T <sub>j</sub> = 150 °C		-		ns
		T <sub>j</sub> = 175 °C		-		ns
t <sub>f</sub>		T <sub>j</sub> = 25 °C		ns		
		T <sub>j</sub> = 150 °C		-		ns
		T <sub>j</sub> = 175 °C		-		ns
E <sub>off</sub>		T <sub>j</sub> = 25 °C		-		mJ
	_	T <sub>j</sub> = 150 °C		-		mJ
		T <sub>j</sub> = 175 °C		-		mJ
$R_{\text{th(j-s)}}$	per IGBT, λ <sub>paste</sub> =0			1.71		K/W
R <sub>th(j-s)</sub>	per IGBT, λ <sub>paste</sub> =2	2.5 W/(mK)		1.46		K/W
IGBT 7 - 1	12					
$V_{\text{CE(sat)}}$	I <sub>C</sub> = 50 A	T <sub>j</sub> = 25 °C		1.55	1.70	V
	V <sub>GE</sub> = 15 V	T <sub>j</sub> = 150 °C		1.73	1.88	V
	chiplevel	T <sub>j</sub> = 175		1.77	1.92	V
V <sub>CE0</sub>		T <sub>j</sub> = 25 °C		1.00	1.05	V
	chiplevel	T <sub>j</sub> = 150 °C		0.80	0.85	V
		T <sub>j</sub> = 175 °C		0.75	0.80	V
r <sub>CE</sub>	V 45.V	T <sub>j</sub> = 25 °C		11	13	mΩ
	V <sub>GE</sub> = 15 V chiplevel	T <sub>j</sub> = 150 °C		19	21	mΩ
	- Othbiesei	T <sub>j</sub> = 175 °C		20	22	mΩ
V <sub>GE(th)</sub>	$V_{GE} = V_{CE} V, I_{C} =$	1.27 mA	5.15	5.8	6.45	٧
I <sub>CES</sub>	$V_{GE} = 0 \text{ V}, V_{CE} = 1$		1		1	mA
C <sub>ies</sub>		f = 1 MHz		10.00		nF
C <sub>oes</sub>	V <sub>CE</sub> = 25 V	f = 1 MHz		0.13		nF
C <sub>res</sub>	$V_{GE} = 0 V$	f = 1 MHz		0.04		nF
100	1	1	Ì			1



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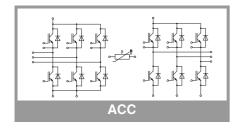
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Characte	eristics					
Symbol	Conditions		min.	typ.	max.	Unit
IGBT 7 - 1	12					
Q <sub>G</sub>	V <sub>GE</sub> = - 8V + 15 V	1		nC		
R <sub>Gint</sub>	T <sub>j</sub> = 25 °C			Ω		
t <sub>d(on)</sub>		T <sub>j</sub> = 25 °C		ns		
	1	T <sub>j</sub> = 150 °C			ns	
		T <sub>j</sub> = 175 °C		38		ns
t <sub>r</sub>		T <sub>j</sub> = 25 °C		ns		
	V <sub>CC</sub> = 600 V	T <sub>j</sub> = 150 °C		41		ns
	I <sub>C</sub> = 50 A	T <sub>j</sub> = 175 °C		ns		
E <sub>on</sub>	$R_{G \text{ on}} = 6.2 \Omega$	T <sub>j</sub> = 25 °C		mJ		
	$R_{G \text{ off}} = 6.2 \Omega$	T <sub>j</sub> = 150 °C	5.3			mJ
	V <sub>GE</sub> = +15/-15 V	T <sub>j</sub> = 175 °C 5.5		5.51		mJ
t <sub>d(off)</sub>		T <sub>j</sub> = 25 °C 243				ns
	@ T <sub>j</sub> = 150 °C:	T <sub>j</sub> = 150 °C		355		ns
	$di/dt_{on} = 1020 \text{ A/}\mu\text{s}$	T <sub>j</sub> = 175 °C 349				ns
t <sub>f</sub>	$di/dt_{off} = 460 \text{ A/}\mu\text{s}$	T <sub>j</sub> = 25 °C		52		ns
		T <sub>j</sub> = 150 °C		93		ns
		T <sub>j</sub> = 175 °C	91			ns
E <sub>off</sub>		T <sub>j</sub> = 25 °C	3			mJ
		T <sub>j</sub> = 150 °C	5.63			mJ
		$T_j = 175$		6.18		mJ
R <sub>th(j-s)</sub>	per IGBT, λ <sub>paste</sub> =0.8	W/(mK)		0.91		K/W
$R_{th(j-s)}$	per IGBT, λ <sub>paste</sub> =2.5	5 W/(mK)		K/W		
Diode 1 -	6					
$V_F = V_{EC}$	I <sub>F</sub> = 8 A	T <sub>j</sub> = 25 °C		0.97	1.20	V
	$V_{GE} = 0 V$	T <sub>j</sub> = 150 °C		0.84	1.07	V
	chiplevel	T <sub>j</sub> = 175 °C		0.82	1.05	V
$V_{F0}$		T <sub>j</sub> = 25 °C		0.89	1.09	V
	chiplevel	T <sub>j</sub> = 150 °C		0.73	0.92	V
		T <sub>j</sub> = 175 °C		0.69	0.88	V
$r_{F}$		T <sub>j</sub> = 25 °C		10	14	$m\Omega$
	chiplevel	T <sub>j</sub> = 150 °C 14		19	$m\Omega$	
		T <sub>j</sub> = 175 °C	16 21		21	mΩ
$I_{RRM}$		T <sub>j</sub> = 25 °C		-		Α
		T <sub>j</sub> = 150 °C		-		Α
	I <sub>F</sub> = 8 A	T <sub>j</sub> = 175 °C		-		Α
Q <sub>rr</sub>	$V_{GE} = -15 \text{ V}$	T <sub>j</sub> = 25 °C	-			μC
	V <sub>CC</sub> = 600 V	T <sub>j</sub> = 150 °C	-		μC	
	@ T <sub>j</sub> = 150 °C:	T <sub>j</sub> = 175 °C	-			μC
E <sub>rr</sub>		T <sub>j</sub> = 25 °C	-			mJ
		T <sub>j</sub> = 150 °C	-			mJ
		T <sub>j</sub> = 175 °C		-		mJ
$R_{\text{th(j-s)}}$	per Diode, $\lambda_{paste}=0$ .			1.31		K/W
$R_{th(j-s)}$	per Diode, $\lambda_{paste}$ =2.	5 W/(mK)		K/W		





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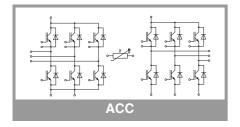
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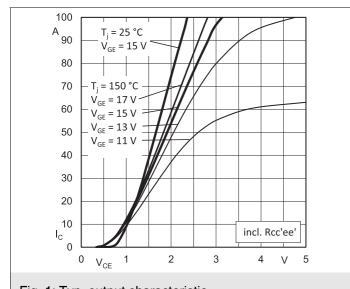
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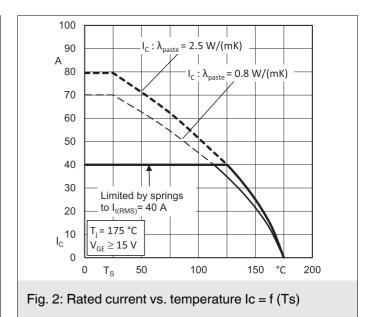
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Characteristics								
Symbol	Conditions		min.	typ.	max.	Unit		
Diode 7 -	12					•		
$V_F = V_{EC}$	I <sub>F</sub> = 25 A	T <sub>j</sub> = 25 °C		2.41	2.74	V		
	V <sub>GE</sub> = 0 V chiplevel	T <sub>j</sub> = 150 °C		2.45	2.79	V		
		T <sub>j</sub> = 175 °C		2.30	2.62	V		
$V_{F0}$		T <sub>j</sub> = 25 °C		1.30	1.50	V		
	chiplevel	T <sub>j</sub> = 150 °C		0.90	1.10	V		
		T <sub>j</sub> = 175 °C		0.82	0.98	V		
r <sub>F</sub>		T <sub>j</sub> = 25 °C		44	50	mΩ		
	chiplevel	T <sub>j</sub> = 150 °C		62	68	mΩ		
		T <sub>j</sub> = 175 °C		59	66	mΩ		
I <sub>RRM</sub>		T <sub>j</sub> = 25 °C		20		Α		
		T <sub>j</sub> = 150 °C		26		Α		
	I <sub>F</sub> = 50 A	T <sub>j</sub> = 175 °C		42		Α		
Q <sub>rr</sub>	V <sub>GE</sub> = -15 V V <sub>CC</sub> = 600 V	T <sub>j</sub> = 25 °C		3		μC		
		T <sub>j</sub> = 150 °C		6.85		μC		
	@ T <sub>i</sub> = 150 °C:	T <sub>j</sub> = 175 °C		7.23		μC		
E <sub>rr</sub>	di/dt <sub>off</sub> = 1030 A/μs	T <sub>j</sub> = 25 °C		0.78		mJ		
		T <sub>j</sub> = 150 °C		3		mJ		
		T <sub>j</sub> = 175 °C		3.45		mJ		
R <sub>th(j-s)</sub>	per Diode, λ <sub>paste</sub> =0.	8 W/(mK)		1.68		K/W		
R <sub>th(j-s)</sub>	per Diode, λ <sub>paste</sub> =2.	.5 W/(mK)		1.44		K/W		
Module	•							
L <sub>CE</sub>				-		nΗ		
Ms	to heat sink		2		2.5	Nm		
w					55	g		
Temperat	ture Sensor					•		
R <sub>100</sub>	T <sub>c</sub> =100°C (R <sub>25</sub> =5 k	Ω)	493 ± 5%			Ω		
B <sub>100/125</sub>	$R_2=R_1*exp[B(1/T_1-$				K			
	· · · · · · · · · · · · · · · · · · ·							

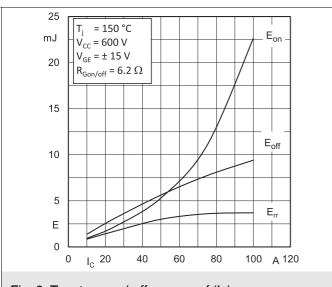
Creepage and clearance distance (spring to spring) between temperature sensor and phase DC- = 1mm (CTI 600)

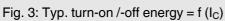












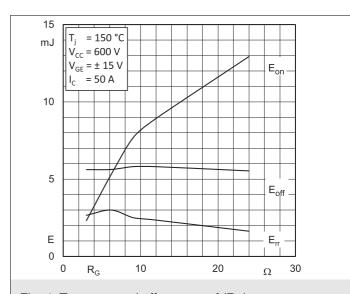


Fig. 4: Typ. turn-on /-off energy =  $f(R_G)$ 

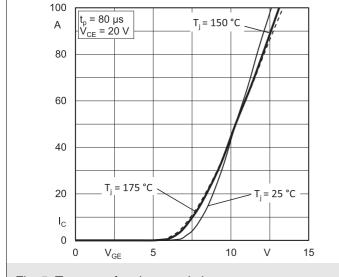


Fig. 5: Typ. transfer characteristic

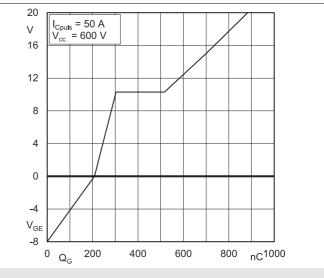


Fig. 6: Typ. gate charge characteristic

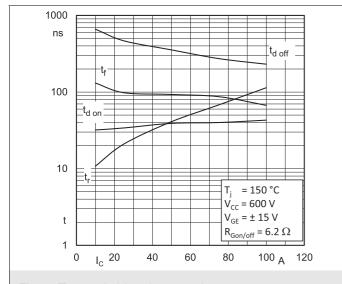


Fig. 7: Typ. switching times vs.  $I_{\text{C}}$ 

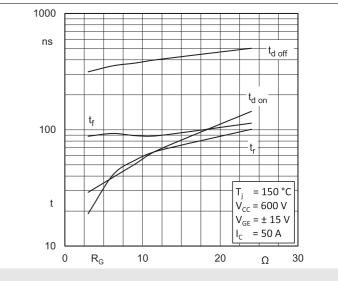


Fig. 8: Typ. switching times vs. gate resistor  $R_{\text{G}}$ 

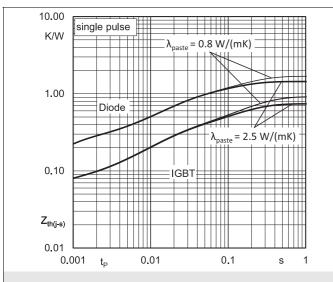


Fig. 9: Typ. transient thermal impedance

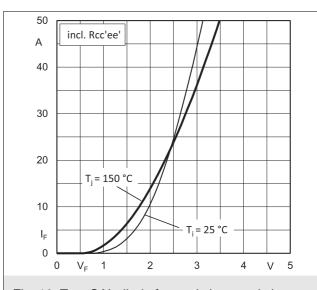


Fig. 10: Typ. CAL diode forward characteristic

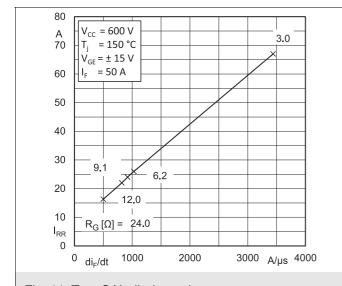


Fig. 11: Typ. CAL diode peak reverse recovery current

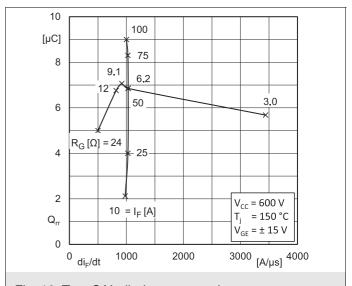
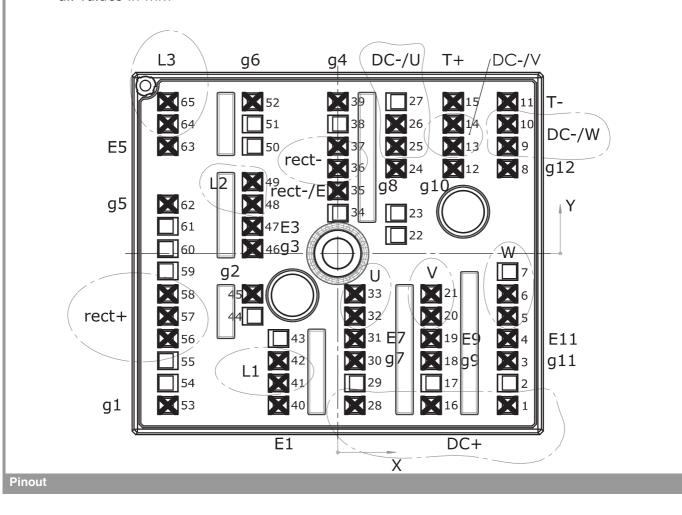
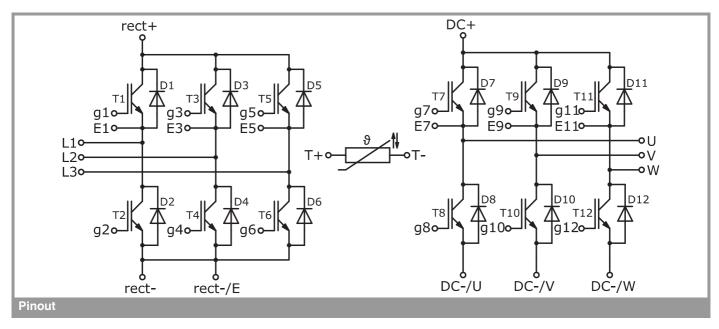


Fig. 12: Typ. CAL diode recovery charge

Pin out											
Pin	X	Y	Function	Pin	Χ	Y	Function	Pin	Χ	Y	Function
1	24,38	-21,80	DC+	23	8,38	5,80		45	-12,23	-5,80	g2
2	24,38	-18,60		24	8,38	12,20	g8	46	-12,23	0,70	g3
3	24,38	-15,40	g11	25	8,38	15,40	DC-/U	47	-12,23	3,90	E3
4	24,38	-12,20	E11	26	8,38	18,60	DC-/U	48	-12,23	7,10	L2
5	24,38	-9,00	W	27	8,38	21,80		49	-12,23	10,30	L2
6	24,38	-5,80	W	28	2,46	-21,80	DC+	50	-12,23	15,40	
7	24,38			29	2,46			51	-12,23		
8	24,38			30	2,46		_	52	-12,23		
9	24,38	15,40	DC-/W	31	2,46	-12,20	E7	53	-24,38	-21,80	g1
10	24,38	18,60		32	2,46	-9,00	U	54	-24,38		
11	24,38			33	2,46	-5,80		55	-24,38		
12	16,58	12,20		34	0,03	5,80		56	-24,38	-12,20	rect+
13	16,58	15,40	DC-/V	35	0,03	9,00	rect-/E	57	-24,38	-9,00	rect+
14	16,58			36	0,03	12,20	rect-	58	-24,38		rect+
15	16,58			37	0,03	15,40	rect-	59	-24,38	-2,50	
16	13,42	-21,80		38	0,03	18,60		60	-24,38		
17	13,42	-18,60		39	0,03	21,80		61	-24,38	3,90	
18	13,42	-15,40		40	-8,51	-21,80		62	-24,38	7,10	g5
19	13,42	-12,20		41	-8,51	-18,60	L1	63	-24,38		
20	13,42	-9,00		42	-8,51	-15,40	L1	64	-24,38	18,60	L3
21	13,42	-5,80	V	43	-8,51	-12,20		65	-24,38	21,80	L3
22	8,38	2,60		44	-12,23	-9,00					

all values in mm





This is an electrostatic discharge sensitive device (ESDS) due to international standard IEC 61340.

#### \*IMPORTANT INFORMATION AND WARNINGS

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