

# SEMIPACK® 1

### Thyristor / Diode Modules

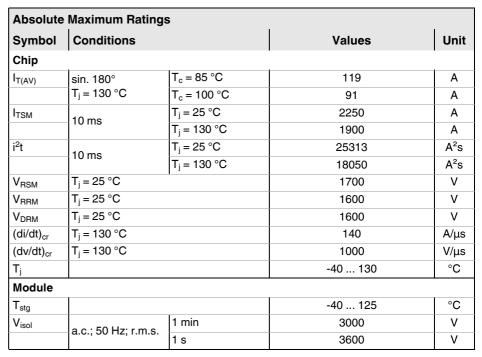
#### **SKKH 107/16 E**

#### Features\*

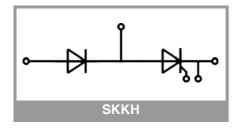
- Heat transfer through aluminium oxide ceramic insulated metal baseplate
- UL recognized, file no. E63532

#### **Typical Applications**

- · Rectifier for motor drives
- Process control
- · Rectifier for power supplies



Characteristics						
Symbol	Conditions		min.	typ.	max.	Unit
Chip	•					
V <sub>T</sub>	$T_j = 25 ^{\circ}\text{C}, I_T = 300 \text{A}$			1.6	1.75	V
$V_{T(TO)}$	T <sub>j</sub> = 130 °C			0.8	0.90	V
r <sub>T</sub>	T <sub>j</sub> = 130 °C			2.80	3.35	mΩ
$I_{DD};I_{RD}$	$T_j = 130  ^{\circ}\text{C},  V_{DD} = V_{DRM};  V_{RD} = V_{RRM}$				20	mA
t <sub>gd</sub>	$T_j = 25 ^{\circ}\text{C},  I_G = 1  \text{A},  di_G/dt = 1  \text{A}/\mu\text{s}$			1		μs
t <sub>gr</sub>	$V_{D} = 0.67 * V_{DRM}$			2		μs
tq	T <sub>j</sub> = 130 °C			200		μs
I <sub>H</sub>	T <sub>j</sub> = 25 °C			150	250	mA
IL	$T_j = 25 ^{\circ}\text{C},  R_G = 33 \Omega$			300	600	mA
V <sub>GT</sub>	$T_j = 25$ °C, d.c.		2.5			V
I <sub>GT</sub>	$T_j = 25$ °C, d.c.		100			mA
$V_{GD}$	$T_j = 130 ^{\circ}\text{C},  \text{d.c.}$				0.25	V
I <sub>GD</sub>	T <sub>j</sub> = 130 °C, d.c.				4	mA
$R_{th(j-c)}$	continuous DC	per chip			0.15	K/W
		per module			0.075	K/W
R <sub>th(j-c)</sub>	sin. 180°	per chip			0.2	K/W
		per module			0.1	K/W
R <sub>th(j-c)</sub>	rec. 120°	per chip			0.21	K/W
		per module			0.105	K/W
Module						
R <sub>th(c-s)</sub>	chip			0.09		K/W
	module			0.05		K/W
Ms	to heatsink M5		4.25		5.75	Nm
$M_t$	to terminals M5		2.55		3.45	Nm
а					5 * 9.81	m/s²
W				75		g



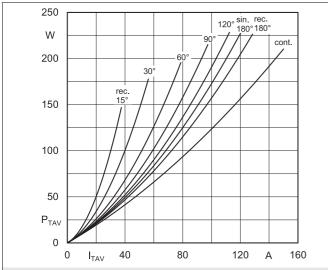


Fig. 1L: Power dissipation per thyristor/diode vs. on-state current

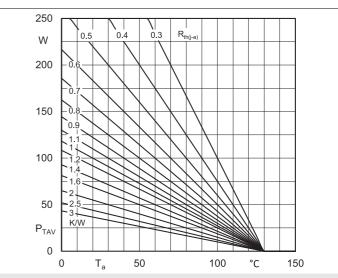


Fig. 1R: Max. power dissipation per chip vs. ambient temperature

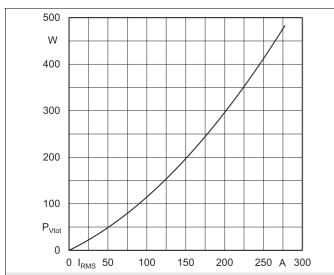


Fig. 2L: Max. power dissipation of one module vs. rms current

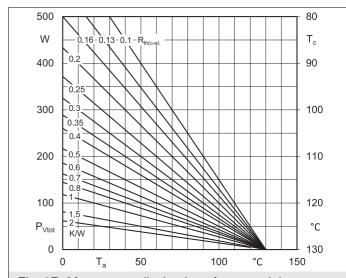


Fig. 2R: Max. power dissipation of one module vs. case temperature

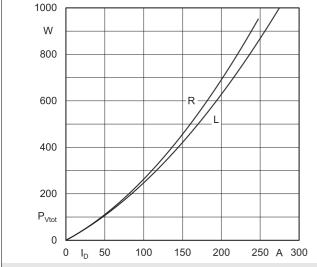


Fig. 3L: Max. power dissipation of two modules vs. direct current

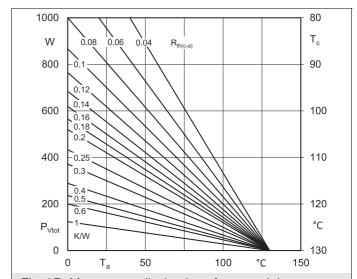


Fig. 3R: Max. power dissipation of two modules vs. case temperature

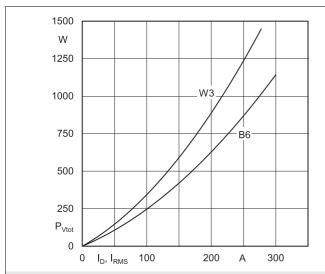


Fig. 4L: Max. power dissipation of three modules vs. direct current

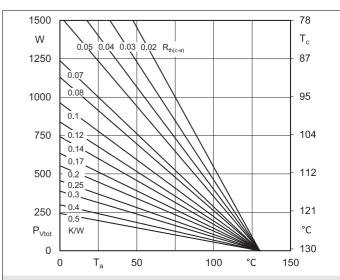


Fig. 4R: Max. power dissipation of three modules vs. case temperature

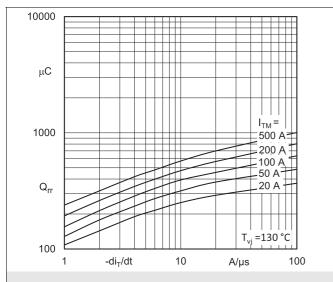


Fig. 5: Recovered charge vs. current decrease

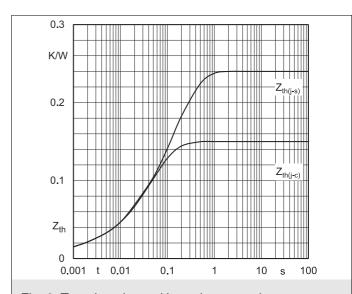


Fig. 6: Transient thermal impedance vs. time

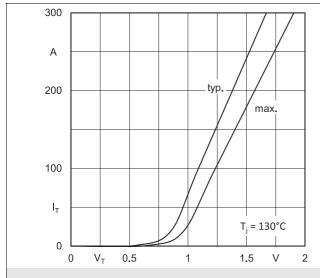


Fig. 7: On-state characteristics

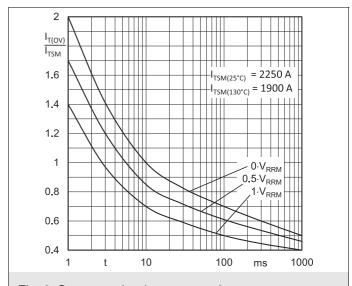
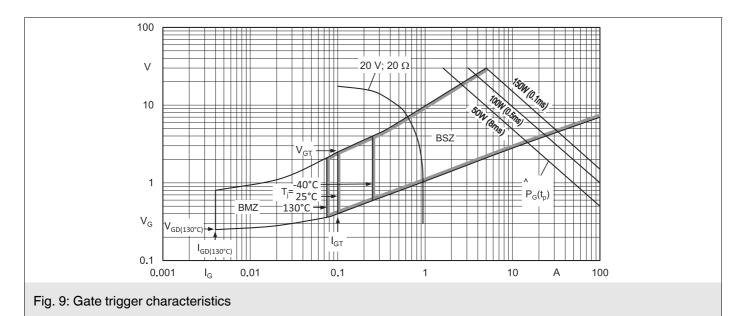
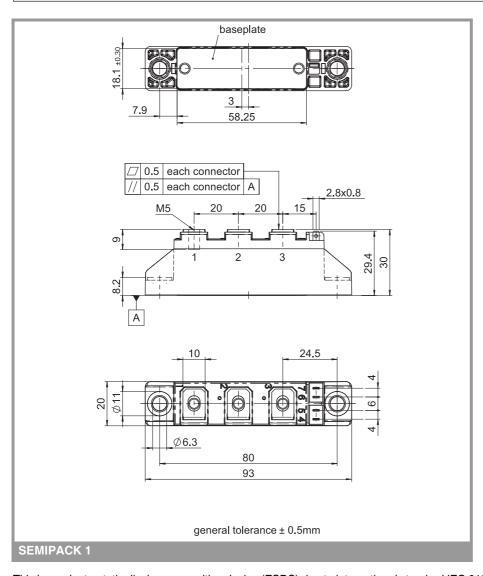
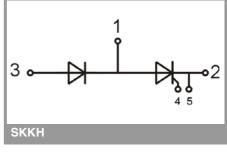


Fig. 8: Surge overload current vs. time







This is an electrostatic discharge sensitive device (ESDS) due to international standard IEC 61340.

#### \*IMPORTANT INFORMATION AND WARNINGS

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