

SEMITRANS 3

IGBT M7 Modules

SKM300GB12M7

Features*

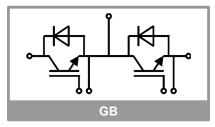
- V_{CE(sat)} with positive temperature coefficient
- High overload capability
- Low loss high density IGBT's
- Fast & soft switching inverse CAL diodes
- Large clearance (10 mm) and creepage distances (20 mm)
- Insulated copper baseplate using DBC Technology (Direct Bonded Copper)
- UL recognized, file no. E63532

Typical Applications

- · AC inverter drives
- UPS

Remarks

- Max. case temperature limited to $T_C = T_S = 125 \, ^{\circ}C$
- Product reliability results are valid for T_j = 150 °C (recommended $T_{j,op}$ = -40...+150 °C)
- For storage and case temperature with TIM see document: "Technical Explanations Thermal Interface Materials"



Absolute	Maximum Rating	S		
Symbol	Conditions		Values	Unit
IGBT				
V _{CES}	T _j = 25 °C		1200	V
I _C	T _j = 175 °C	T _c = 25 °C	419	Α
		T _c = 80 °C	321	Α
I _{Cnom}			300	Α
I _{CRM}			600	Α
V_{GES}			-20 20	V
t _{psc}	$V_{CC} = 800 \text{ V}$ $V_{GE} \le 15 \text{ V}$ $V_{CES} \le 1200 \text{ V}$	T _j = 150 °C	8	μs
Tj			-40 175	°C
Inverse di	iode			
V_{RRM}	T _j = 25 °C		1200	٧
I _F	T _i = 175 °C	T _c = 25 °C	346	Α
	1] = 175 C	T _c = 80 °C	258	Α
I _{FRM}			600	Α
I _{FSM}	t_p = 10 ms, sin 180°, T_j = 25 °C		1548	Α
T_j			-40 175	°C
Module				
I _{t(RMS)}			500	А
T _{stg}	module without TIM		-40 125	°C
V _{isol}	AC sinus 50 Hz, t = 1 min		4000	V

Characteristics							
Symbol	Conditions		min.	typ.	max.	Unit	
IGBT			•			•	
V _{CE(sat)}	$I_C = 300 \text{ A}$ $V_{GE} = 15 \text{ V}$ chiplevel	T _j = 25 °C		1.56	1.88	V	
		T _j = 150 °C		1.80		V	
	chiplevel	T _j = 25 °C		0.84	0.91	V	
V_{CE0}		T _j = 150 °C		0.72		V	
r _{CE}	V _{GE} = 15 V chiplevel	T _j = 25 °C		2.4	3.2	mΩ	
		T _j = 150 °C		3.6		mΩ	
$V_{GE(th)}$	V _{CE} = 10V, I _C = 30 mA		5.4	6	6.6	V	
I _{CES}	V _{GE} = 0 V, V _{CE} = 1200 V, T _j = 25 °C				3.0	mA	
C _{ies}	V _{CE} = 10 V V _{GF} = 0 V	f = 1 MHz		64.0		nF	
Coes		f = 1 MHz		1.99		nF	
C _{res}		f = 1 MHz		0.76		nF	
Q _G	V _{GE} = - 8V + 15 V			3000		nC	
R _{Gint}	T _j = 25 °C			1		Ω	
t _{d(on)}	V _{CC} = 600 V	T _j = 150 °C		177		ns	
t _r	$\begin{array}{l} I_{C} = 300 \text{ A} \\ V_{GE} = +15/-15 V \\ R_{G \text{ on}} = 1.2 \Omega \\ R_{G \text{ off}} = 1 \Omega \\ \text{di/dt}_{on} = 8145 \text{ A/µs} \\ \text{di/dt}_{off} = 2552 \text{ A/µs} \end{array}$	T _j = 150 °C		40		ns	
Eon		T _j = 150 °C		17		mJ	
$t_{d(off)}$		T _j = 150 °C		370		ns	
t _f		,	_	96		ns	
E _{off}		T _j = 150 °C		35		mJ	
R _{th(j-c)}	per IGBT				0.119	K/W	
R _{th(c-s)}	per IGBT, P12 (reference)			0.038		K/W	
R _{th(c-s)}	per IGBT, HP-PCM			0.031		K/W	



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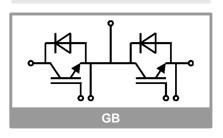
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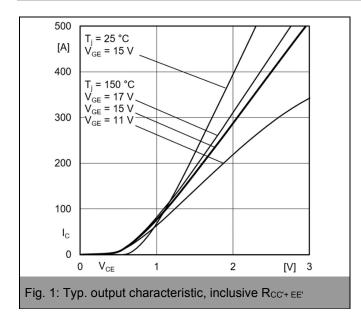
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- UPS

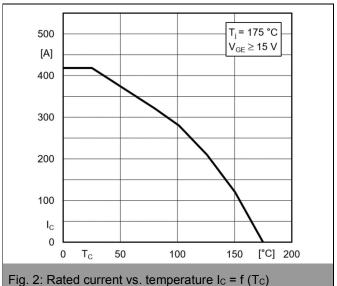
Remarks

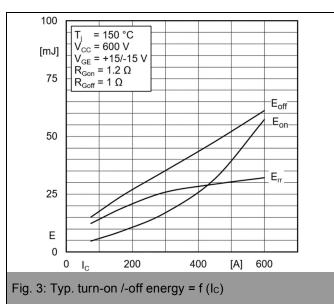
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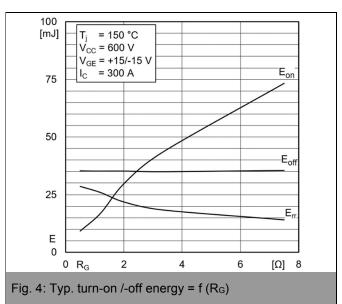


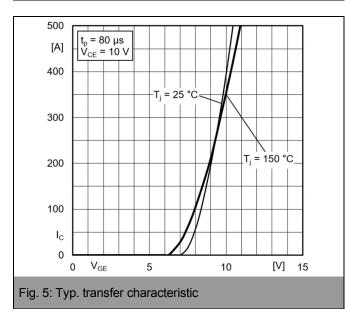
Characte	eristics					
Symbol	Conditions	min.	typ.	max.	Unit	
Inverse o	diode					
V _F = V _{EC}	I _F = 300 A V _{GE} = 0 V chiplevel	T _j = 25 °C		2.17	2.49	V
		T _j = 150 °C		2.10		V
V _{F0}	chiplevel	T _j = 25 °C		1.30	1.50	V
		T _j = 150 °C		0.90		V
r _F		T _j = 25 °C		2.9	3.3	mΩ
	chiplevel	T _j = 150 °C		4.0		mΩ
I _{RRM}	V _{CC} = 600 V	T _j = 150 °C		412		Α
Q _{rr}	I _F = 300 A V _{GE} = -15 V	T _j = 150 °C		52.7		μC
Err	di/dt _{off} = 8755 A/µ	s T _j = 150 °C		26		mJ
R _{th(j-c)}	per diode			0.176	K/W	
R _{th(c-s)}	per diode, P12 (reference)			0.042		K/W
R _{th(c-s)}	per diode, HP-PCM			0.036		K/W
Module						
L _{CE}				15		nΗ
R _{cc+EE'}	measured per	T _j = 25 °C		0.55		mΩ
CC'+EE'	switch	T _j = 150 °C		0.85		mΩ
R _{th(c-s)1}	calculated without thermal coupling, P12 (reference)			0.01		K/W
R _{th(c-s)2}	including thermal coupling, T _s underneath module, P12 (reference)			0.016		K/W
R _{th(c-s)2}	including thermal coupling, T _s underneath module, HP-PCM			0.0087		K/W
Ms	to heat sink M6		3		5	Nm
Mt	t	o terminal M6	2.5		5	Nm
				-		Nm
W					325	g

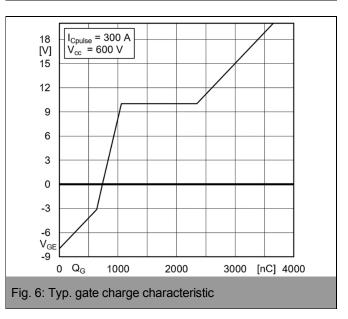


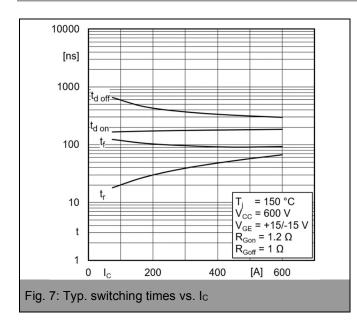


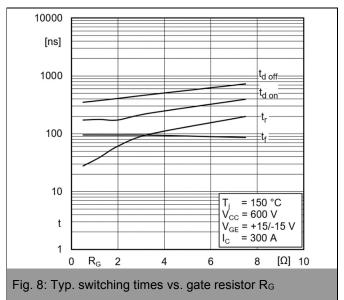


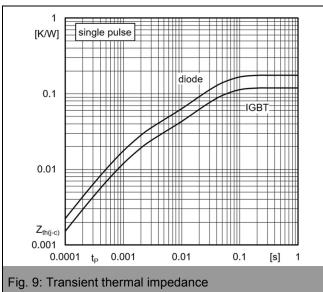


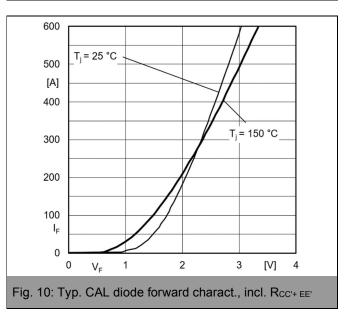


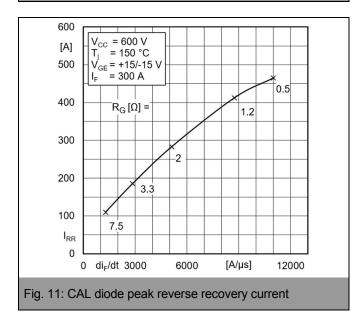


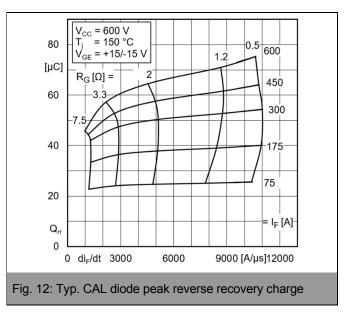


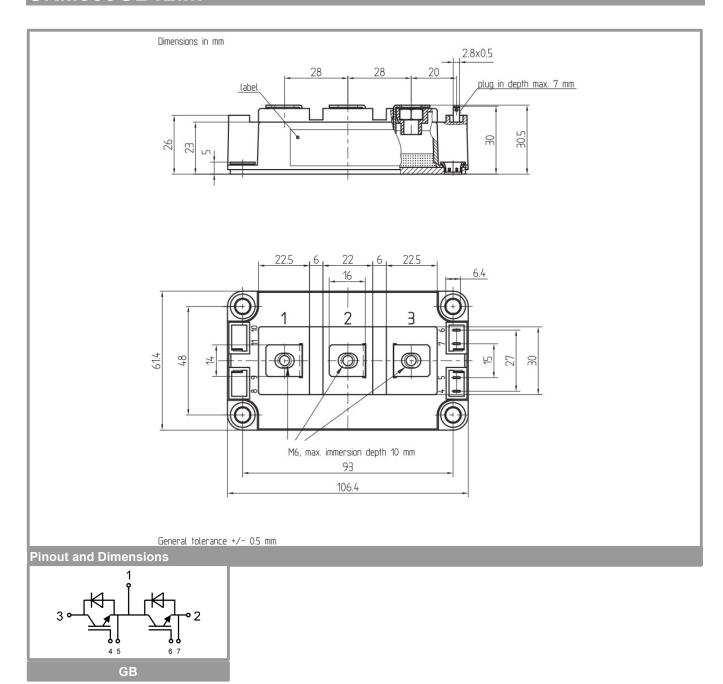












This is an electrostatic discharge sensitive device (ESDS) according to international standard IEC 61340.

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