



SEMITRANS® 2

SKM100GB12V

Features

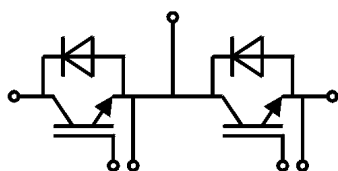
- V-IGBT = 6. Generation Trench V-IGBT (Fuji)
- CAL4 = Soft switching 4. Generation CAL-diode
- Isolated copper baseplate using DBC technology (Direct Copper Bonding)
- UL recognized, file no. E63532
- Increased power cycling capability
- With integrated gate resistor
- Low switching losses at high di/dt

Typical Applications*

- AC inverter drives
- UPS
- Electronic welders

Remarks

- Case temperature limited to $T_c = 125^\circ\text{C}$ max, recomm. $T_{op} = -40 \dots +150^\circ\text{C}$, product rel. results valid for $T_j = 150^\circ$



GB

Absolute Maximum Ratings

Symbol	Conditions		Values	Unit
IGBT				
V _{CES}	T _j = 25 °C		1200	V
I _C	T _j = 175 °C	T _c = 25 °C	159	A
		T _c = 80 °C	121	A
I _{Cnom}			100	A
I _{CRM}	I _{CRM} = 3xI _{Cnom}		300	A
V _{GES}			-20 ... 20	V
t _{psc}	V _{CC} = 720 V V _{GE} ≤ 20 V V _{CES} ≤ 1200 V	T _j = 125 °C	10	μs
T _j			-40 ... 175	°C
Inverse diode				
I _F	T _j = 175 °C	T _c = 25 °C	121	A
		T _c = 80 °C	91	A
I _{Fnom}			100	A
I _{FRM}	I _{FRM} = 3xI _{Fnom}		300	A
I _{FSM}	t _p = 10 ms, sin 180°, T _j = 25 °C		550	A
T _j			-40 ... 175	°C
Module				
I _{t(RMS)}	T _{terminal} = 80 °C		200	A
T _{stg}			-40 ... 125	°C
V _{isol}	AC sinus 50Hz, t = 1 min		4000	V

Characteristics

Symbol	Conditions	min.	typ.	max.	Unit
IGBT					
$V_{CE(sat)}$	$I_C = 100\text{ A}$ $V_{GE} = 15\text{ V}$ chipelevel	$T_j = 25^\circ\text{C}$	1.75	2.20	V
		$T_j = 150^\circ\text{C}$	2.20	2.50	V
V_{CE0}		$T_j = 25^\circ\text{C}$	0.94	1.04	V
		$T_j = 150^\circ\text{C}$	0.88	0.98	V
r_{CE}	$V_{GE} = 15\text{ V}$	$T_j = 25^\circ\text{C}$	8.10	11.6	$\text{m}\Omega$
		$T_j = 150^\circ\text{C}$	13.20	15.20	$\text{m}\Omega$
$V_{GE(th)}$	$V_{GE} = V_{CE}, I_C = 4\text{ mA}$	5.5	6	6.5	V
I_{CES}	$V_{GE} = 0\text{ V}$ $V_{CE} = 1200\text{ V}$	$T_j = 25^\circ\text{C}$	0.1	0.3	mA
		$T_j = 150^\circ\text{C}$			mA
C_{ies}	$V_{CE} = 25\text{ V}$ $V_{GE} = 0\text{ V}$	$f = 1\text{ MHz}$	6.01		nF
C_{oes}		$f = 1\text{ MHz}$	0.59		nF
C_{res}		$f = 1\text{ MHz}$	0.589		nF
Q_G	$V_{GE} = -8\text{ V} \dots +15\text{ V}$		1150		nC
R_{Gint}			7.5		Ω
$t_{d(on)}$	$V_{CC} = 600\text{ V}$	$T_j = 150^\circ\text{C}$	294		ns
t_r	$I_C = 100\text{ A}$	$T_j = 150^\circ\text{C}$	38		ns
E_{on}	$V_{GE} = \pm 15\text{ V}$	$T_j = 150^\circ\text{C}$	10.7		mJ
$t_{d(off)}$	$R_{G on} = 1\text{ }\Omega$	$T_j = 150^\circ\text{C}$	418		ns
t_f	$R_{G off} = 1\text{ }\Omega$	$T_j = 150^\circ\text{C}$	62		ns
E_{off}	$di/dt_{on} = 3230\text{ A}/\mu\text{s}$ $di/dt_{off} = 1330\text{ A}/\mu\text{s}$ $du/dt_{off} = 9350\text{ V}/\mu\text{s}$	$T_j = 150^\circ\text{C}$	8.7		mJ
$R_{th(j-c)}$	per IGBT			0.27	K/W



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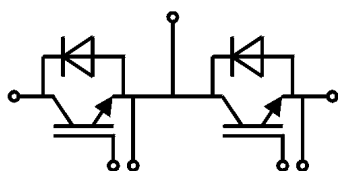
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- AC inverter drives
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Characteristics						
Symbol	Conditions		min.	typ.	max.	Unit
Inverse diode						
V _F = V _{EC}	I _F = 100 A	T _j = 25 °C		2.20	2.52	V
	V _{GE} = 0 V chip	T _j = 150 °C		2.15	2.47	V
V _{F0}		T _j = 25 °C		1.3	1.5	V
		T _j = 150 °C		0.9	1.1	V
r _F		T _j = 25 °C		9.0	10.2	mΩ
		T _j = 150 °C		12.5	13.7	mΩ
I _{RRM}	I _F = 100 A	T _j = 150 °C		90		A
Q _{rr}	di/dt _{off} = 3050 A/μs	T _j = 150 °C		15		μC
E _{rr}	V _{GE} = ±15 V	T _j = 150 °C		5.7		mJ
	V _{CC} = 600 V					
R _{th(j-c)}	per diode				0.48	K/W
Module						
L _{CE}					30	nH
R _{CC'+EE'}	terminal-chip	T _C = 25 °C		0.65		mΩ
		T _C = 125 °C		1		mΩ
R _{th(c-s)}	per module			0.04	0.05	K/W
M _s	to heat sink M6		3		5	Nm
M _t		to terminals M5	2.5		5	Nm
						Nm
w					160	g



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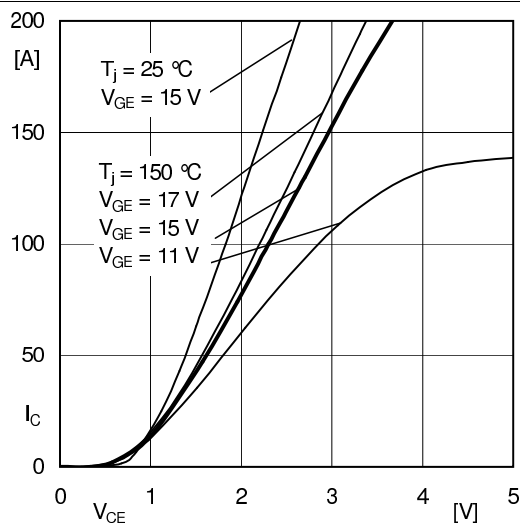


Fig. 1: Typ. output characteristic, inclusive $R_{CC'} + E_{E'}$

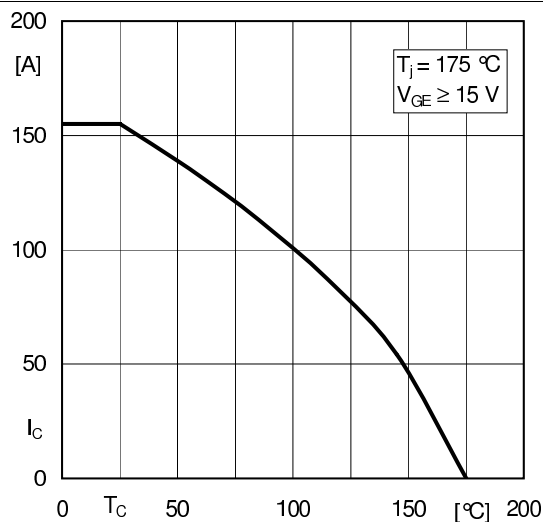


Fig. 2: Rated current vs. temperature $I_C = f(T_C)$

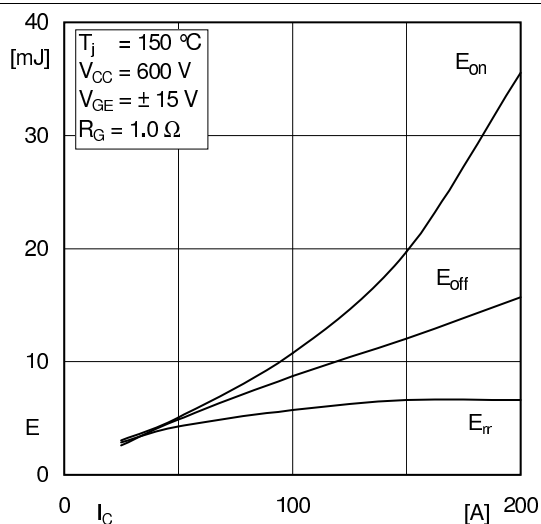


Fig. 3: Typ. turn-on /-off energy = $f(I_C)$

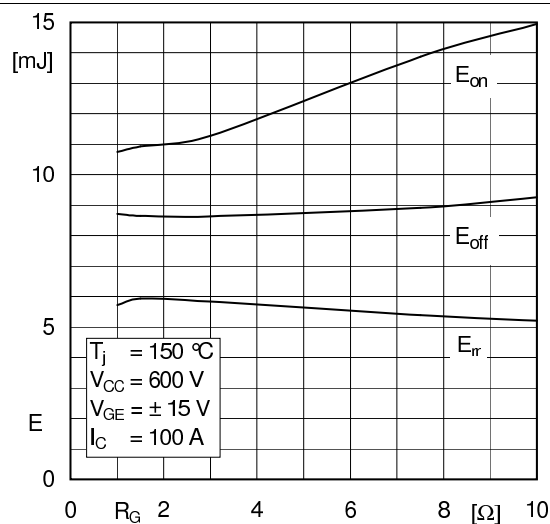


Fig. 4: Typ. turn-on /-off energy = $f(R_G)$

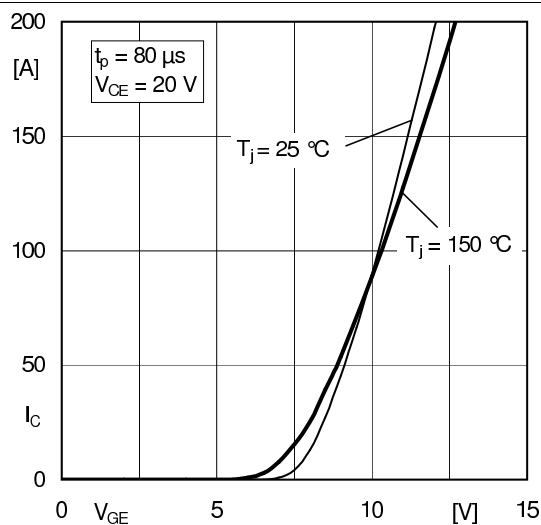


Fig. 5: Typ. transfer characteristic

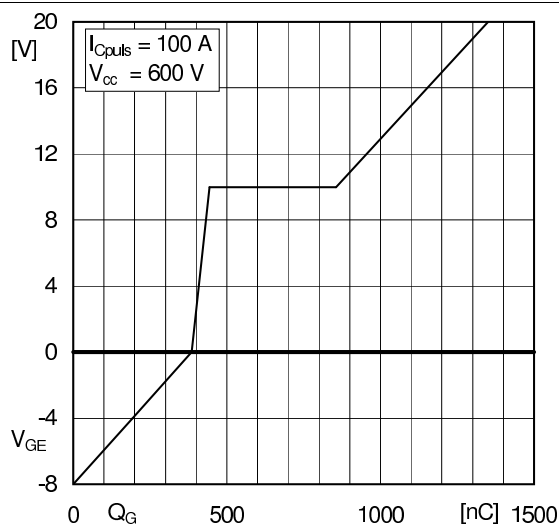


Fig. 6: Typ. gate charge characteristic

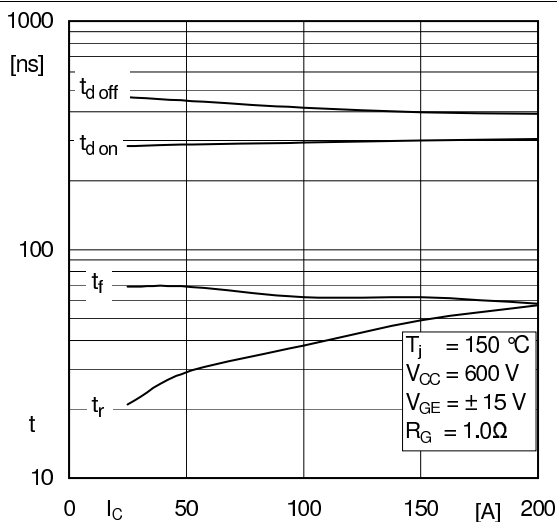


Fig. 7: Typ. switching times vs. I_C

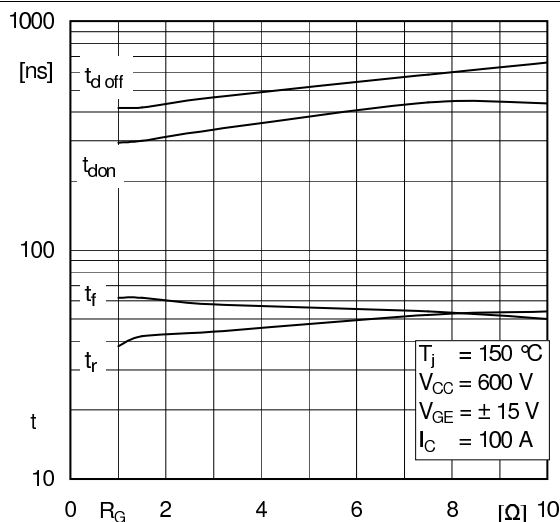


Fig. 8: Typ. switching times vs. gate resistor R_G

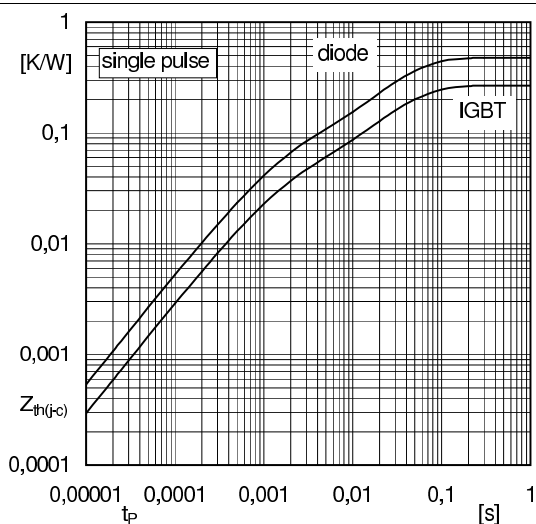


Fig. 9: Transient thermal impedance

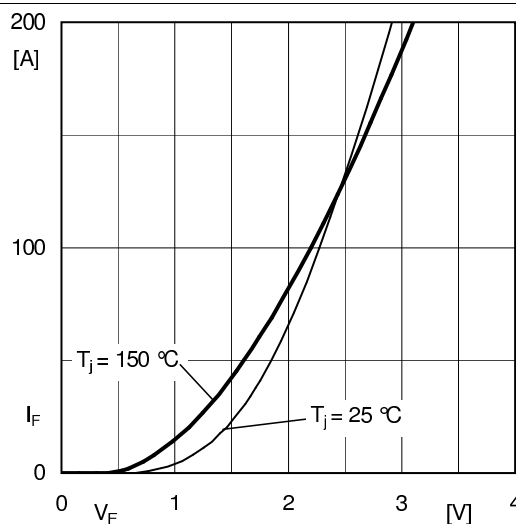


Fig. 10: Typ. CAL diode forward charact., incl. $R_{CC'+EE'}$

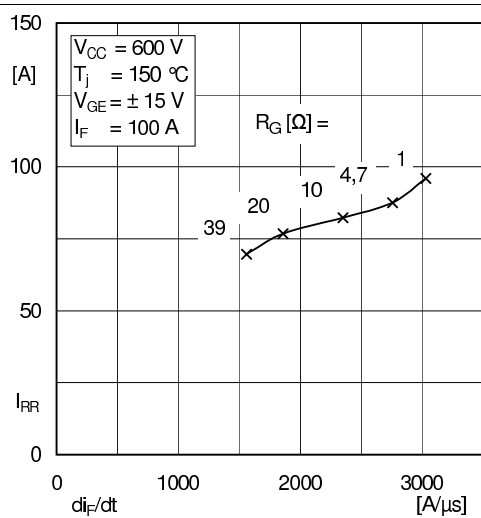


Fig. 11: CAL diode peak reverse recovery current

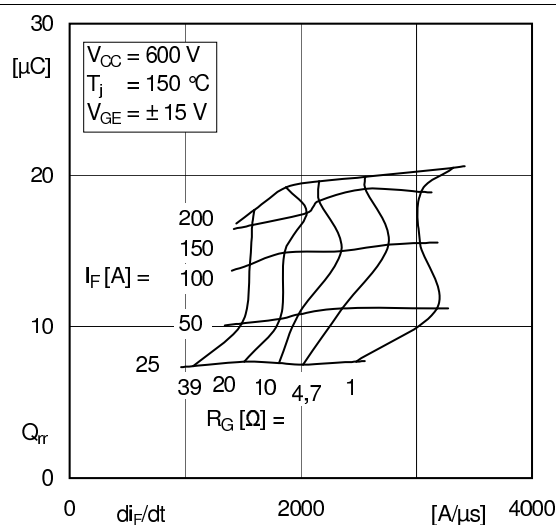
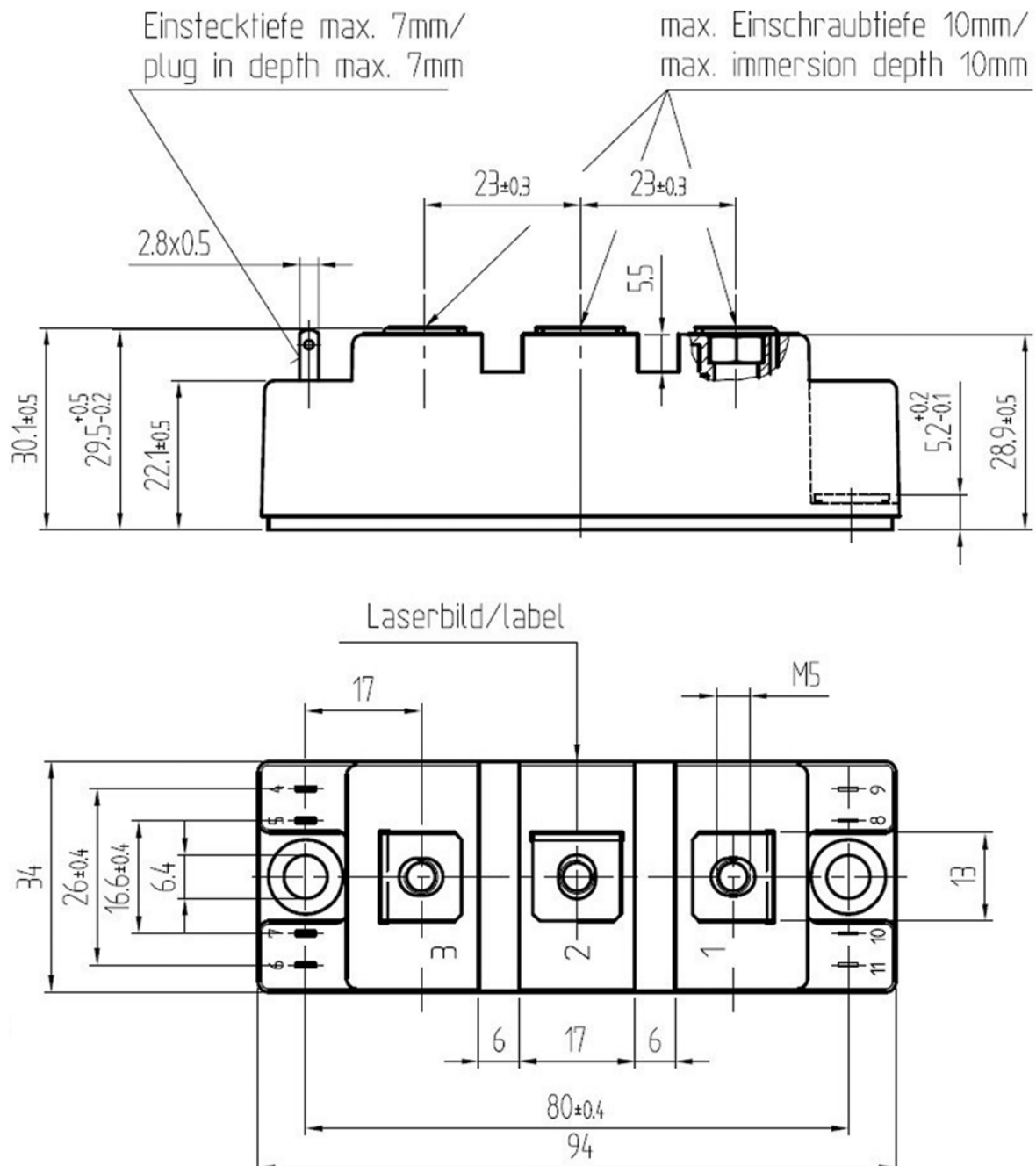
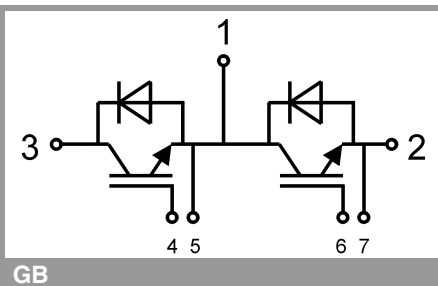


Fig. 12: Typ. CAL diode peak reverse recovery charge



SEMITRANS 2



This is an electrostatic discharge sensitive device (ESDS), international standard IEC 60747-1, Chapter IX

* The specifications of our components may not be considered as an assurance of component characteristics. Components have to be tested for the respective application. Adjustments may be necessary. The use of SEMIKRON products in life support appliances and systems is subject to prior specification and written approval by SEMIKRON. We therefore strongly recommend prior consultation of our staff.