

SKM150GAL12V

Features

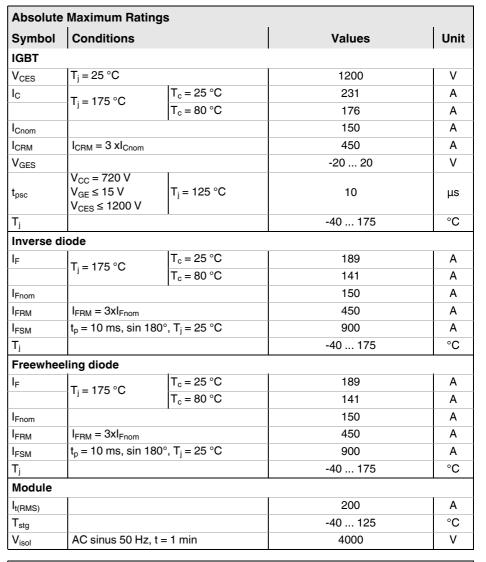
- V-IGBT = 6. Generation Trench V-IGBT (Fuji)
- CAL4 = Soft switching 4. Generation CAL-diode
- Insulated copper baseplate using DBC technology (Direct Copper Bonding)
- · Increased power cycling capability
- · With integrated gate resistor
- UL recognized, file no. E63532
- Lowest switching losses at High di/dt

Typical Applications*

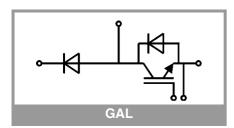
- DC/DC converter
- Brake chopper
- · Switched reluctance motor
- DC Motor

Remarks

- Case temperature limited to T_c = 125°C
- Recommended T_{op} = -40 ... +150°C
- Product reliability results valid for T_j = 150°C



| Characteristics | | | | | | | | | |
|----------------------|---|-------------------------|------|------|------|----|--|--|--|
| Symbol | Conditions | min. | typ. | max. | Unit | | | | |
| IGBT | | | | | | • | | | |
| V _{CE(sat)} | $I_C = 150 \text{ A}$ $V_{GE} = 15 \text{ V}$ chiplevel | T _j = 25 °C | | 1.75 | 2.20 | V | | | |
| | | T _j = 150 °C | | 2.20 | 2.48 | V | | | |
| V _{CE0} | chiplevel | T _j = 25 °C | | 0.94 | 1.04 | V | | | |
| | | T _j = 150 °C | | 0.88 | 0.98 | V | | | |
| r _{CE} | V _{GE} = 15 V chiplevel | T _j = 25 °C | | 5.4 | 7.7 | mΩ | | | |
| | | T _j = 150 °C | | 8.8 | 10 | mΩ | | | |
| $V_{GE(th)}$ | $V_{GE}=V_{CE}$, $I_{C}=6$ mA | | 5.5 | 6 | 6.5 | V | | | |
| I _{CES} | V _{GE} = 0 V V _{CE} = 1200 V | T _j = 25 °C | | | 0.3 | mA | | | |
| | | T _j = 150 °C | | - | | mA | | | |
| C _{ies} | V _{CE} = 25 V V _{GE} = 0 V | f = 1 MHz | | 9.0 | | nF | | | |
| C _{oes} | | f = 1 MHz | | 0.89 | | nF | | | |
| C _{res} | | f = 1 MHz | | 0.88 | | nF | | | |
| Q_{G} | V _{GE} = - 8 V+ 15 V | | | 1650 | | nC | | | |
| R _{Gint} | T _j = 25 °C | | | 5.0 | | Ω | | | |





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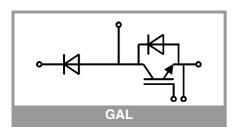
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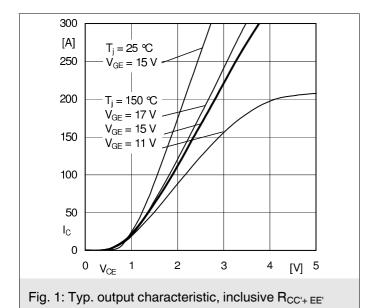
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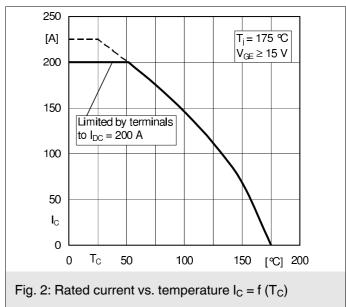
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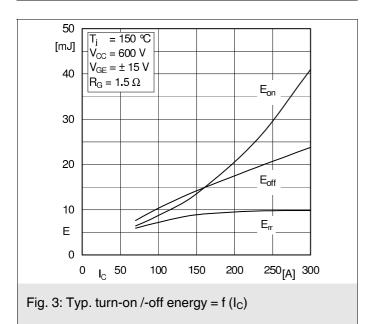
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- Recommended T_{op} = -40 ... +150°C
- Product reliability results valid for T_j = 150°C

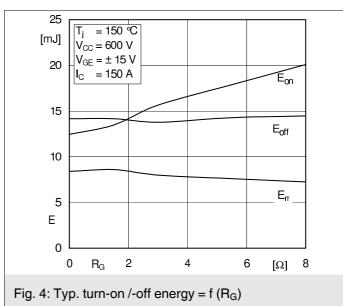
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|------------------------------------|--|-------------------------|------|------|------|-----|--|--|--|--|
| Symbol | Conditions | min. | typ. | max. | Unit | | | | | |
| t _{d(on)} | V _{CC} = 600 V | T _i = 150 °C | | 258 | | ns | | | | |
| t _r | I _C = 150 A | T _j = 150 °C | | 32 | | ns | | | | |
| E _{on} | $V_{GE} = +15/-15 \text{ V}$ $R_{G \text{ on}} = 1.5 \Omega$ | T _j = 150 °C | | 13.5 | | mJ | | | | |
| t _{d(off)} | $R_{G \text{ off}} = 1.5 \Omega$ | T _j = 150 °C | | 388 | | ns | | | | |
| t _f | $di/dt_{on} = 5400 \text{ A/}\mu\text{s}$ | T _j = 150 °C | | 62 | | ns | | | | |
| E _{off} | di/dt _{off} = 1800 A/μs du/dt = 8100 V/μs | T _j = 150 °C | | 14.2 | | mJ | | | | |
| R _{th(j-c)} | per IGBT | | | | 0.19 | K/W | | | | |
| Inverse di | ode | | | | | | | | | |
| $V_F = V_{EC}$ | I _F = 150 A | T _j = 25 °C | | 2.14 | 2.46 | V | | | | |
| | V _{GE} = 0 V chiplevel | T _j = 150 °C | | 2.07 | 2.38 | V | | | | |
| V _{F0} | chiplevel | T _j = 25 °C | | 1.30 | 1.50 | V | | | | |
| | | T _j = 150 °C | | 0.90 | 1.10 | V | | | | |
| r _F | chiplevel | T _j = 25 °C | | 5.6 | 6.4 | mΩ | | | | |
| | | T _j = 150 °C | | 7.8 | 8.5 | mΩ | | | | |
| I _{RRM} | $I_F = 150 \text{ A}$ $di/dt_{off} = 5800 \text{ A/}\mu\text{s}$ $V_{GE} = \pm 15 \text{ V}$ $V_{CC} = 600 \text{ V}$ | T _j = 150 °C | | 165 | | Α | | | | |
| Q_{rr} | | T _j = 150 °C | | 22 | | μC | | | | |
| E _{rr} | | T _j = 150 °C | | 8.5 | | mJ | | | | |
| R _{th(j-c)} | per diode | | | | 0.31 | K/W | | | | |
| Freewhee | ling diode | | | | | | | | | |
| $V_F = V_{EC}$ | I _F = 150 A | T _j = 25 °C | | 2.14 | 2.46 | V | | | | |
| V _{GE} = 0 V chiplevel | chiplevel | T _j = 150 °C | | 2.07 | 2.38 | V | | | | |
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| R _{th(j-c)} | per diode | | | | 0.31 | K/W | | | | |
| Module | | | | | | | | | | |
| L _{CE} | | | | 30 | | nΗ | | | | |
| R _{CC'+EE'} | measured per | T _C = 25 °C | | 0.65 | | mΩ | | | | |
| | switch | T _C = 125 °C | | 1.09 | | mΩ | | | | |
| R _{th(c-s)} | calculated without thermal coupling (λ _{grease} =0.81 W/(m*K)) | | | 0.04 | 0.05 | K/W | | | | |
| Ms | to heat sink M6 | | 3 | | 5 | Nm | | | | |
| Mt | | to terminals M5 | 2.5 | | 5 | Nm | | | | |
| | 1 | | | | | Nm | | | | |
| w | | • | | | 160 | g | | | | |

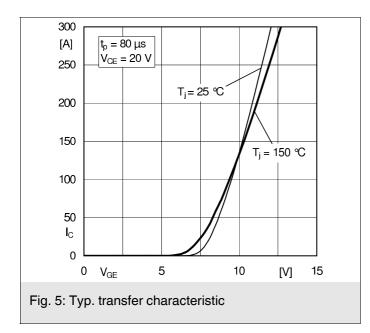


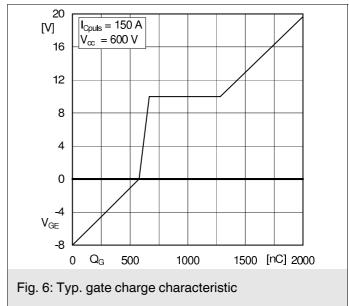


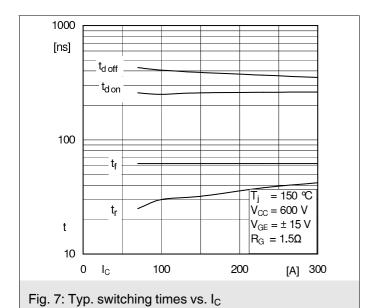


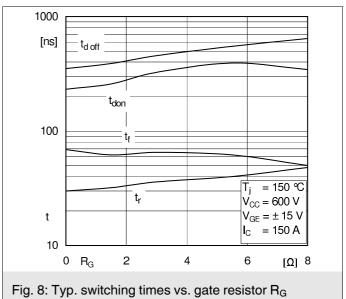


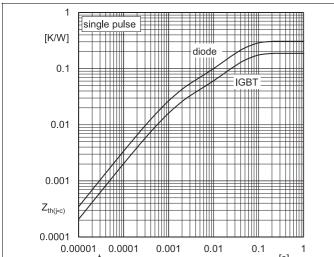












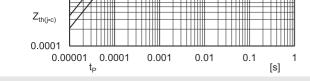
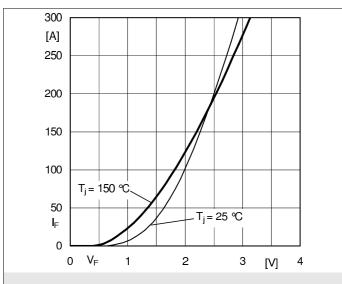
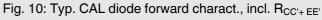


Fig. 9: Transient thermal impedance





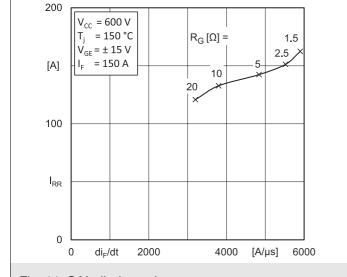


Fig. 11: CAL diode peak reverse recovery current

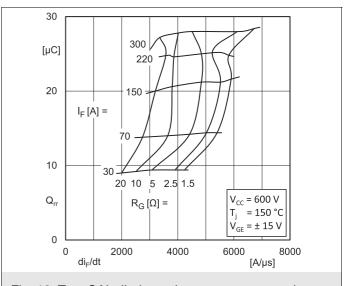
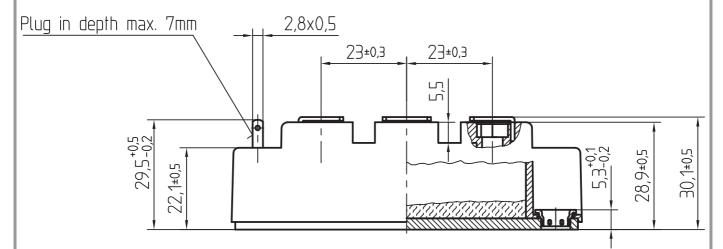
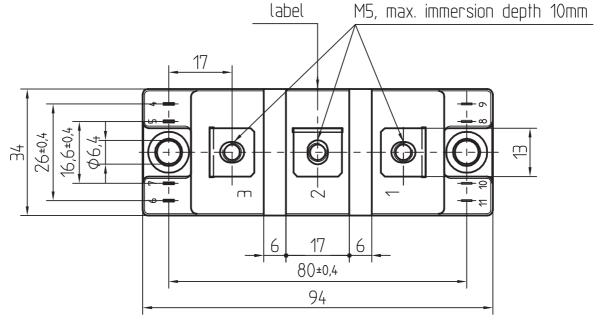


Fig. 12: Typ. CAL diode peak reverse recovery charge

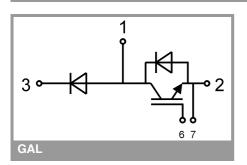






General tolerance +/- 0,5 mm

SEMITRANS 2



This is an electrostatic discharge sensitive device (ESDS), international standard IEC 60747-1, chapter IX.

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