

# SEMITRANS<sup>®</sup> 2

### Trench IGBT Modules

### SKM195GB07E3

#### Features

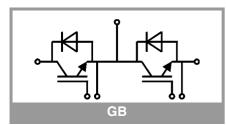
- V<sub>CE(sat)</sub> with positive temperature coefficient
- High short circuit capability, self limiting to 6 x lcnom
- Fast & soft inverse CAL diodes
- Insulated copper baseplate using DBC Technology (Direct Copper Bonding)
- With integrated gate resistor

### **Typical Applications\***

- AC inverter drives
- UPS
- · Electronic welders
- Wind power
- Public transport

### Remarks

- Case temperature limited to T<sub>c</sub> = 125°C max.
- Recommended  $T_{op} = -40 \dots +150^{\circ}C$
- Product reliability results valid for T<sub>j</sub> = 150°C
- Use of soft R<sub>G</sub> necessary



Absolute	e Maximum Ratir	ngs		
Symbol	Conditions		Values	Unit
IGBT				
V <sub>CES</sub>	T <sub>j</sub> = 25 °C		650	V
lc	T <sub>j</sub> = 175 °C	T <sub>c</sub> = 25 °C	266	Α
		T <sub>c</sub> = 80 °C	201	Α
I <sub>Cnom</sub>			200	А
I <sub>CRM</sub>	$I_{CRM} = 3 x I_{Cnom}$		600	Α
V <sub>GES</sub>			-20 20	V
t <sub>psc</sub>	$V_{CC} = 360 V$ $V_{GE} \le 15 V$ $V_{CES} \le 650 V$	T <sub>j</sub> = 150 °C	6	μs
Tj			-40 175	°C
Inverse o	diode			<b>!</b>
V <sub>RRM</sub>	T <sub>j</sub> = 25 °C		650	
I <sub>F</sub>	T <sub>j</sub> = 175 °C	T <sub>c</sub> = 25 °C	217	Α
		T <sub>c</sub> = 80 °C	157	А
<b>I</b> <sub>Fnom</sub>			200	Α
I <sub>FRM</sub>	$I_{FRM} = 2 x I_{Fnom}$		400	Α
I <sub>FSM</sub>	t <sub>p</sub> = 10 ms, sin 180°, T <sub>j</sub> = 25 °C		1470	Α
Tj			-40 175	°C
Module	•			· ·
I <sub>t(RMS)</sub>			200	А
T <sub>stg</sub>	module without	TIM	-40 125	°C
V <sub>isol</sub>	AC sinus 50 Hz,	t = 1 min	4000	V

#### Characteristics Symbol Conditions Unit min. typ. max. IGBT I<sub>C</sub> = 200 A T<sub>i</sub> = 25 °C V V<sub>CE(sat)</sub> 1.46 1.90 V<sub>GE</sub> = 15 V T<sub>i</sub> = 150 °C 2.10 V 1.70 chiplevel V<sub>CE0</sub> T<sub>i</sub> = 25 °C 0.90 1.00 V chiplevel T<sub>i</sub> = 150 °C 0.82 0.90 ٧ T<sub>i</sub> = 25 °C 2.8 4.5 mΩ V<sub>GE</sub> = 15 V $r_{CE}$ chiplevel T<sub>i</sub> = 150 °C 4.4 6.0 mΩ V 5.1 5.8 V<sub>GE(th)</sub> $V_{GE}=V_{CE}$ , $I_C = 3.2 \text{ mA}$ 6.4 $I_{CES}$ $V_{GE} = 0 V, V_{CE} = 650 V, T_j = 25 °C$ 0.3 mΑ f = 1 MHz Cies 12.3 nF V<sub>CE</sub> = 25 V Coes f = 1 MHz0.77 nF $V_{GE} = 0 V$ f = 1 MHz0.37 nF Cres V<sub>GE</sub> = - 8 V...+ 15 V $Q_{G}$ 1600 nC T<sub>i</sub> = 25 °C $R_{\text{Gint}}$ 2.0 Ω V<sub>CC</sub> = 300 V T<sub>i</sub> = 150 °C 122 ns t<sub>d(on)</sub> I<sub>C</sub> = 200 A T<sub>i</sub> = 150 °C 52 tr ns V<sub>GE</sub> = +15/-15 V T<sub>j</sub> = 150 °C Eon 6.3 m.J $R_{G \text{ on}} = 1 \Omega$ T<sub>i</sub> = 150 °C 650 $R_{G off} = 5.6 \ \Omega$ ns t<sub>d(off)</sub> $di/dt_{on} = 3810 \text{ A}/\mu \text{s} T_{i} = 150 \text{ }^{\circ}\text{C}$ tf 62 ns di/dt<sub>off</sub> = 3260 A/µs du/dt = 2090 V/µs T<sub>i</sub> = 150 °C Eoff 8.3 mJ 0.22 K/W R<sub>th(j-c)</sub> per IGBT per IGBT ( $\lambda_{grease}=0.81 \text{ W/(m*K)}$ ) K/W $R_{\text{th(c-s)}}$ 0.064 per IGBT, pre-applied phase change R<sub>th(c-s)</sub> K/W 0.054 material



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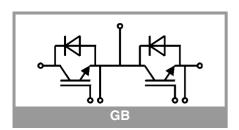
### **Typical Applications\***

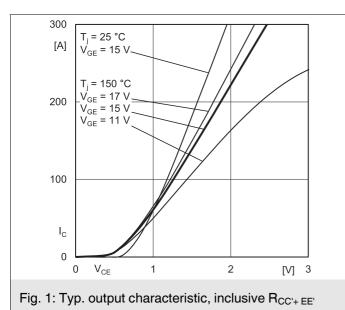
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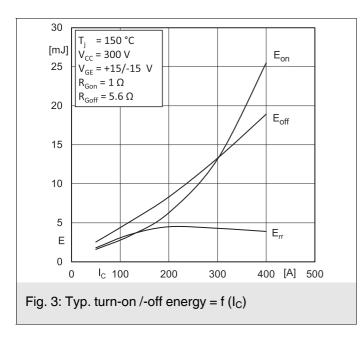
## Remarks

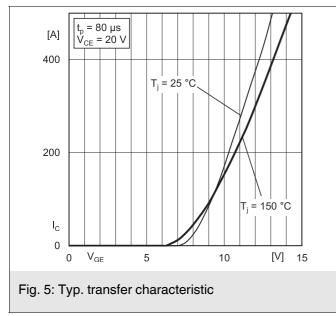
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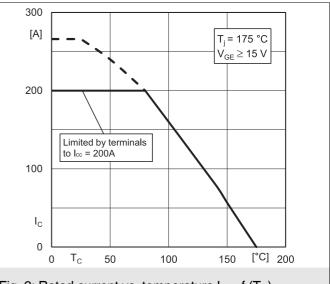
Characte	ristics					
Symbol	Conditions	min.	typ.	max.	Unit	
Inverse d	iode					
$V_F = V_{EC}$	I <sub>F</sub> = 200 A	T <sub>j</sub> = 25 °C		1.39	1.75	V
	V <sub>GE</sub> = 0 V chiplevel	T <sub>j</sub> = 150 °C		1.38	1.76	V
V <sub>F0</sub>	chiplevel	T <sub>j</sub> = 25 °C		1.04	1.24	V
		T <sub>j</sub> = 150 °C		0.85	0.99	V
۲ <sub>F</sub>	chiplevel	T <sub>j</sub> = 25 °C		1.76	2.6	mΩ
		T <sub>j</sub> = 150 °C		2.6	3.9	mΩ
I <sub>RRM</sub>	$I_{F} = 200 \text{ A} \\ di/dt_{off} = 3885 \text{ A/}\mu\text{s} \\ V_{GE} = \pm 15 \text{ V} \\ V_{CC} = 300 \text{ V} $	T <sub>j</sub> = 150 °C		200		Α
Q <sub>rr</sub>		T <sub>j</sub> = 150 °C		22		μC
E <sub>rr</sub>		T <sub>j</sub> = 150 °C		4.5		mJ
R <sub>th(j-c)</sub>	per diode			0.4	K/W	
R <sub>th(c-s)</sub>	per diode ( $\lambda_{grease}=0$		0.069		K/W	
R <sub>th(c-s)</sub>	per diode, pre-appl material		0.061		K/W	
Module						
L <sub>CE</sub>				30		nH
R <sub>CC'+EE'</sub>	measured per switch	T <sub>C</sub> = 25 °C		0.65		mΩ
		T <sub>C</sub> = 125 °C		1.09		mΩ
Rth <sub>(c-s)1</sub>	calculated without thermal coupling			0.017		K/W
Rth <sub>(c-s)2</sub>	including thermal co Ts underneath mod $(\lambda_{grease}=0.81 \text{ W/(m}^*)$		0.027		K/W	
Rth <sub>(c-s)2</sub>	including thermal control Ts underneath moderneath moderneath moderneath moderneath moderneath material structures and the second structures and the		0.023		К/М	
Ms	to heat sink M6	3		5	Nm	
Mt		to terminals M5	2.5		5	Nm
	1					Nm
w		1			160	g

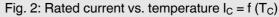


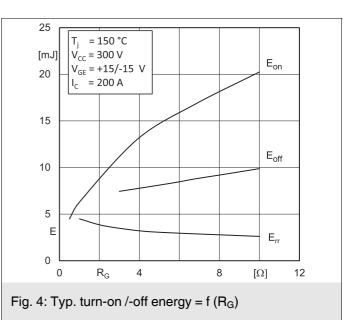


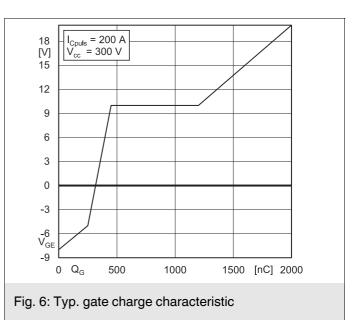


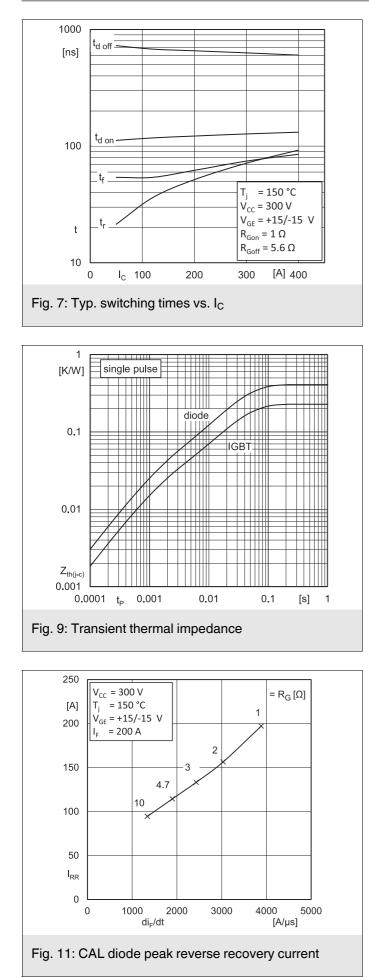


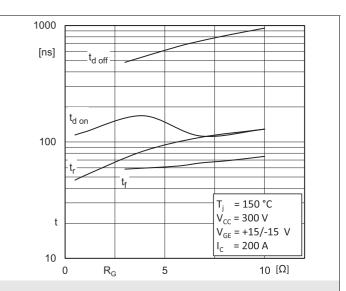


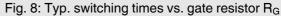


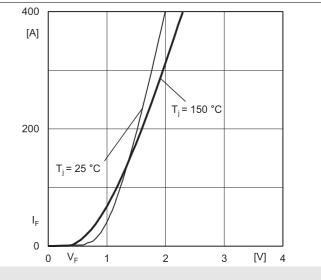


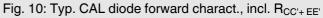












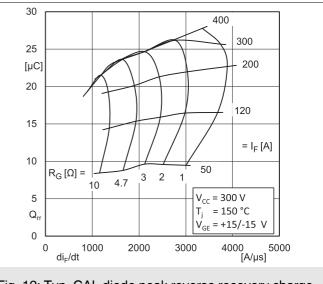
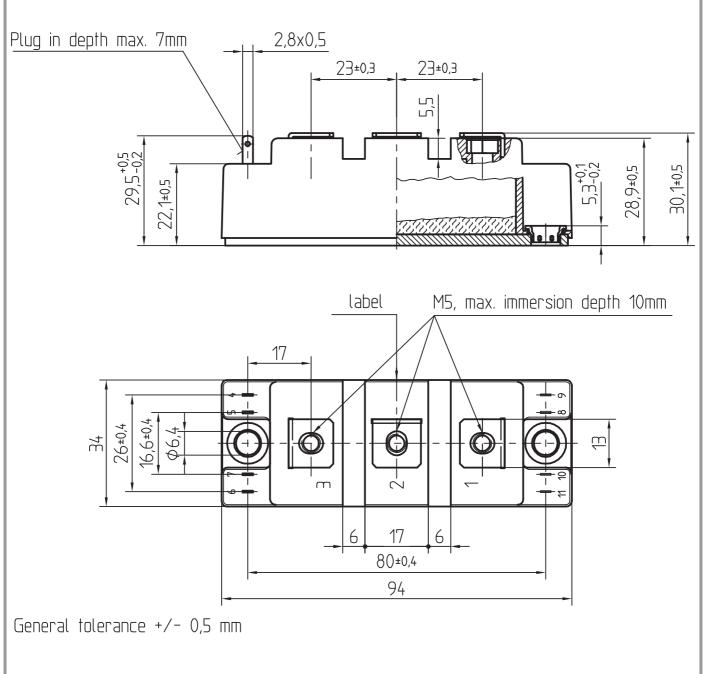
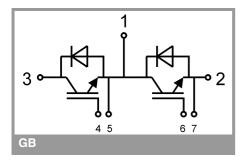


Fig. 12: Typ. CAL diode peak reverse recovery charge

Dimensions in mm





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This is an electrostatic discharge sensitive device (ESDS), international standard IEC 60747-1, chapter IX.

#### **\*IMPORTANT INFORMATION AND WARNINGS**

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