



SEMITRANS® 2

Trench IGBT Modules

SKM295GB066D

Features*

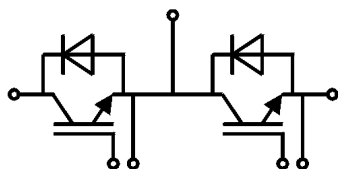
- $V_{CE(sat)}$ with positive temperature coefficient
- High short circuit capability, self limiting to $6 \times I_{Cnom}$
- Fast & soft switching inverse CAL diodes
- Large clearance (10 mm) and creepage distances (20 mm)
- Insulated copper baseplate using DBC Technology (Direct Bonded Copper)
- UL recognized, file no. E63532

Typical Applications

- AC inverter drives
- UPS
- Electronic welders

Remarks

- Case temperature limited to $T_c = 125^\circ\text{C}$ max, recommended $T_{op} = -40 \dots +150^\circ\text{C}$
- Product reliability results are valid for $T_j \leq 150^\circ\text{C}$
- Short circuit data: Use of soft R_G necessary!
- Take care of over-voltage caused by stray inductances



GB

Absolute Maximum Ratings

Symbol	Conditions		Values	Unit
IGBT				
V _{CES}	T _J = 25 °C		600	V
I _C	T _J = 175 °C	T _c = 25 °C	362	A
		T _c = 80 °C	272	A
I _{Cnom}			300	A
I _{CRM}			600	A
V _{GES}			-20 ... 20	V
t _{psc}	V _{CC} = 360 V V _{GE} ≤ 15 V V _{CES} ≤ 600 V	T _J = 150 °C	6	μs
T _J			-40 ... 175	°C
Inverse diode				
I _F	T _J = 175 °C	T _c = 25 °C	286	A
		T _c = 80 °C	209	A
I _{FRM}			400	A
I _{FSM}	t _p = 10 ms, sin 180°, T _J = 25 °C		1773	A
T _J			-40 ... 175	°C
Module				
I _{t(RMS)}			200	A
T _{stg}			-40 ... 125	°C
V _{isol}	AC sinus 50 Hz, t = 1 min		4000	V

Characteristics

Symbol	Conditions	min.	typ.	max.	Unit
IGBT					
$V_{CE(sat)}$	$I_C = 300\text{ A}$ $V_{GE} = 15\text{ V}$ chipelevel	$T_j = 25^\circ\text{C}$	1.45	1.85	V
		$T_j = 150^\circ\text{C}$	1.69	2.10	V
V_{CE0}	chipelevel	$T_j = 25^\circ\text{C}$	0.90	1.00	V
		$T_j = 150^\circ\text{C}$	0.85	0.90	V
r_{CE}	$V_{GE} = 15\text{ V}$ chipelevel	$T_j = 25^\circ\text{C}$	1.83	2.8	$\text{m}\Omega$
		$T_j = 150^\circ\text{C}$	2.8	4.0	$\text{m}\Omega$
$V_{GE(th)}$	$V_{GE} = V_{CE}, I_C = 4.8\text{ mA}$	5	5.8	6.5	V
I_{CES}	$V_{GE} = 0\text{ V}$ $V_{CE} = 600\text{ V}$	$T_j = 25^\circ\text{C}$		0.2	mA
		$T_j = 150^\circ\text{C}$	-		mA
C_{ies}	$V_{CE} = 25\text{ V}$ $V_{GE} = 0\text{ V}$	$f = 1\text{ MHz}$	18.5		nF
C_{oes}		$f = 1\text{ MHz}$	1.15		nF
C_{res}		$f = 1\text{ MHz}$	0.55		nF
Q_G	$V_{GE} = -8\text{ V} \dots +15\text{ V}$		1700		nC
R_{Gint}	$T_j = 25^\circ\text{C}$		1.0		Ω
$t_{d(on)}$	$V_{CC} = 300\text{ V}$	$T_j = 150^\circ\text{C}$	94		ns
t_r	$I_C = 300\text{ A}$	$T_j = 150^\circ\text{C}$	157		ns
E_{on}	$V_{GE} = +15/-8\text{ V}$ $R_{G on} = 5.6\text{ }\Omega$	$T_j = 150^\circ\text{C}$	20.5		mJ
$t_{d(off)}$	$R_{G off} = 14\text{ }\Omega$	$T_j = 150^\circ\text{C}$	1537		ns
t_f	$di/dt_{on} = 1770\text{ A}/\mu\text{s}$ $di/dt_{off} = 2450\text{ A}/\mu\text{s}$	$T_j = 150^\circ\text{C}$	112		ns
E_{off}	$dv/dt = 1160\text{ V}/\mu\text{s}$ $L_s = 32\text{ nH}$	$T_j = 150^\circ\text{C}$	22		mJ
$R_{th(j-c)}$	per IGBT			0.172	K/W



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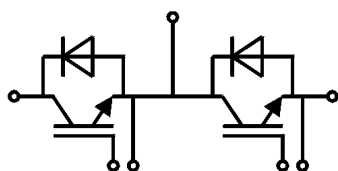
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Symbol	Conditions		min.	typ.	max.	Unit
Inverse diode						
V _F = V _{EC}	I _F = 200 A	T _j = 25 °C		1.36	1.55	V
	V _{GE} = 0 V chiplevel	T _j = 150 °C		1.35	1.54	V
V _{F0}	chiplevel	T _j = 25 °C		1.00	1.10	V
		T _j = 150 °C		0.85	0.95	V
r _F	chiplevel	T _j = 25 °C		1.82	2.3	mΩ
		T _j = 150 °C		2.5	3.0	mΩ
I _{RRM}	I _F = 300 A	T _j = 150 °C		108		A
Q _{rr}	di/dt _{off} = 1870 A/μs	T _j = 150 °C		20		μC
E _{rr}	V _{CC} = 300 V L _s = 32 nH	T _j = 150 °C		3		mJ
R _{th(j-c)}	per diode				0.29	K/W
Module						
L _{CE}				30		nH
R _{CC'+EE'}	measured per switch	T _C = 25 °C		0.65		mΩ
		T _C = 125 °C		1.09		mΩ
R _{th(c-s)}	calculated without thermal coupling (λ _{grease} =0.81 W/(m²K))			0.04	0.05	K/W
M _s	to heat sink M6		3		5	Nm
M _t		to terminals M5	2.5		5	Nm
					-	
w					160	g



GB

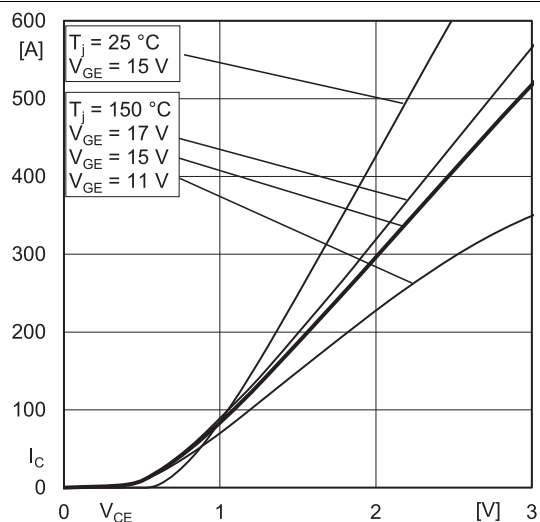


Fig. 1: Typ. output characteristic, inclusive $R_{CC'} + E_{E'}$

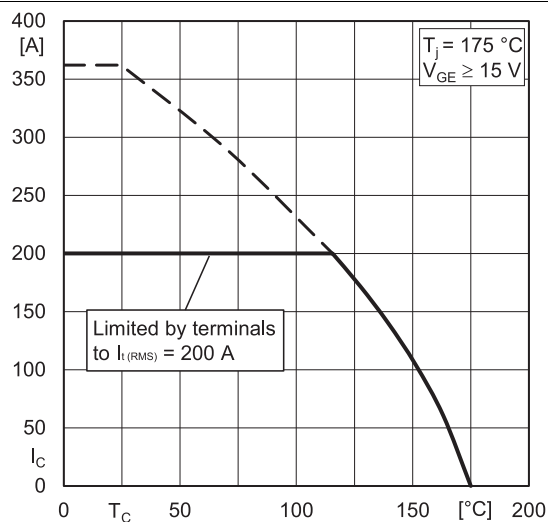


Fig. 2: Rated current vs. temperature $I_C = f(T_C)$

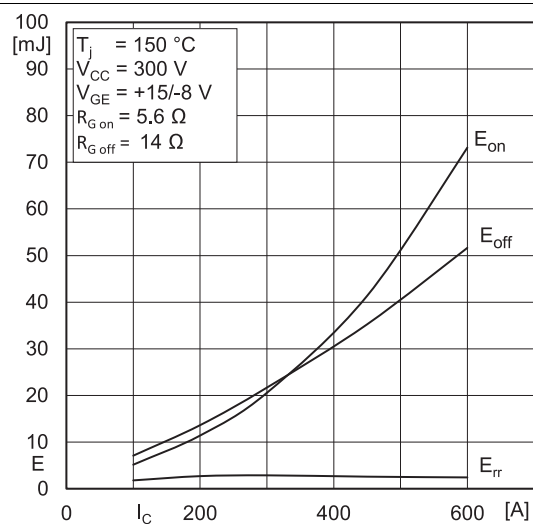


Fig. 3: Typ. turn-on /-off energy = $f(I_C)$

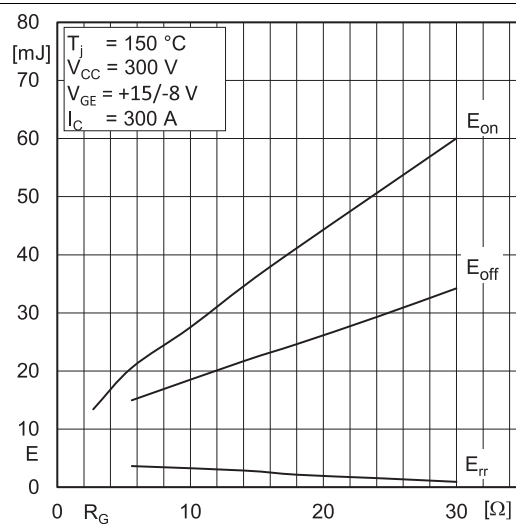


Fig. 4: Typ. turn-on /-off energy = $f(R_G)$

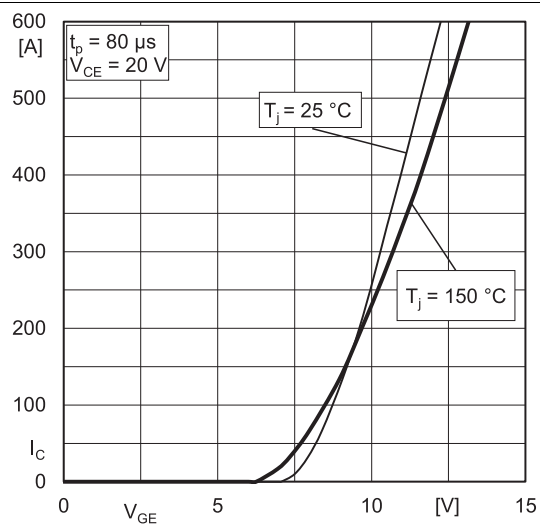


Fig. 5: Typ. transfer characteristic

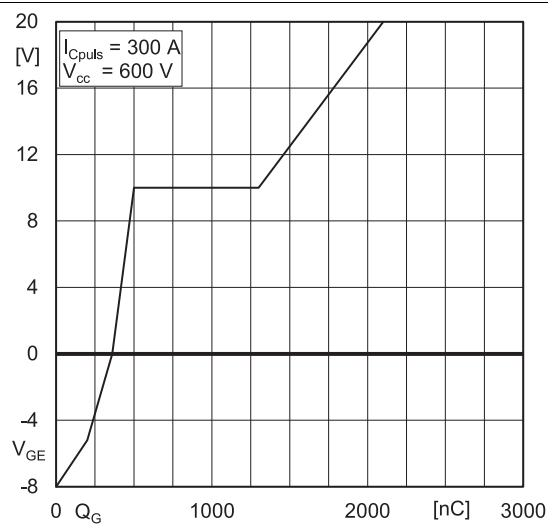


Fig. 6: Typ. gate charge characteristic

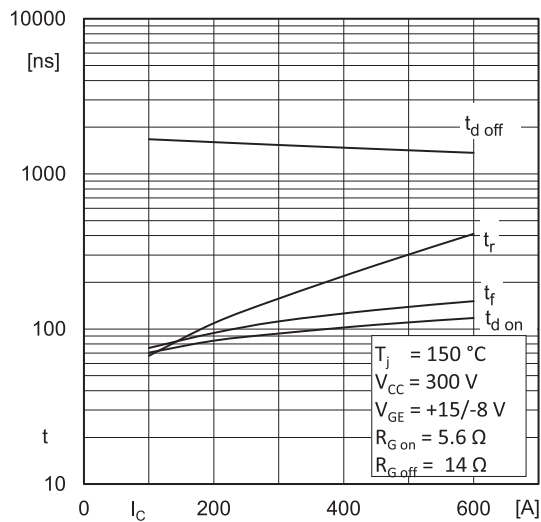


Fig. 7: Typ. switching times vs. I_C

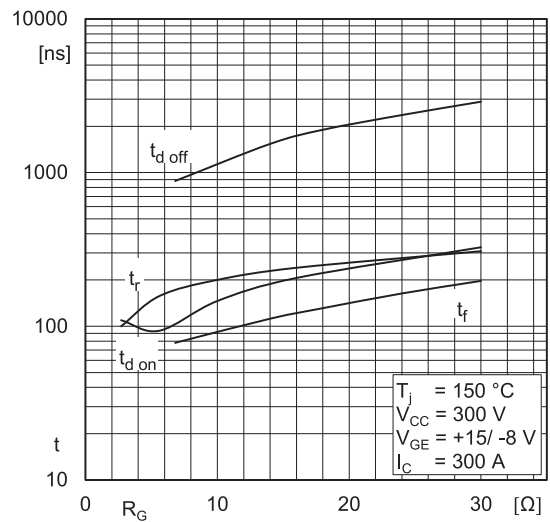


Fig. 8: Typ. switching times vs. gate resistor R_G

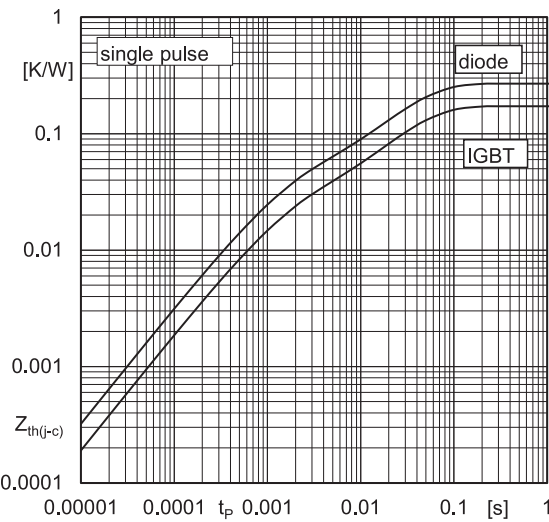


Fig. 9: Transient thermal impedance

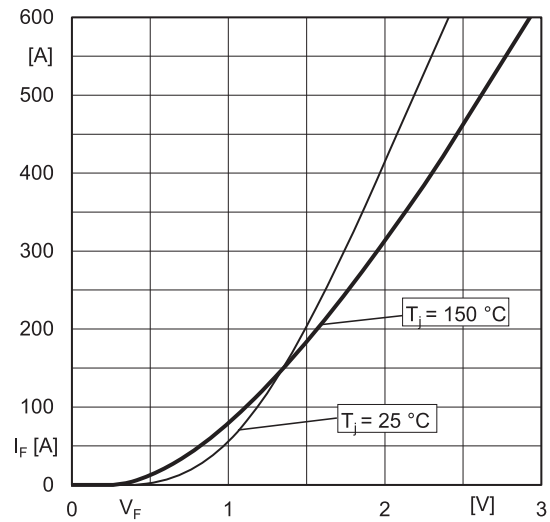


Fig. 10: Typ. CAL diode forward charact., incl. $R_{CC'+EE'}$

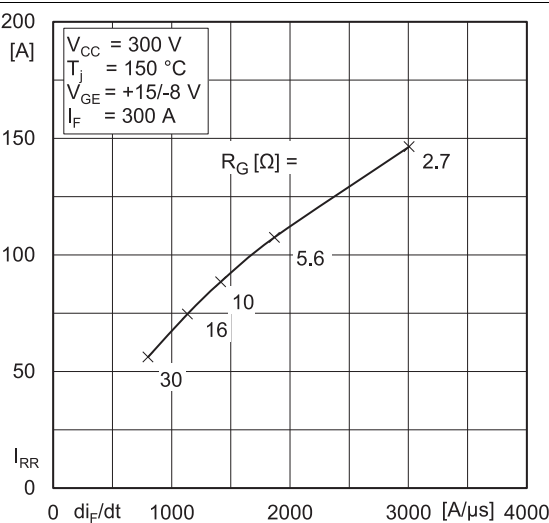


Fig. 11: Typ. CAL diode peak reverse recovery current

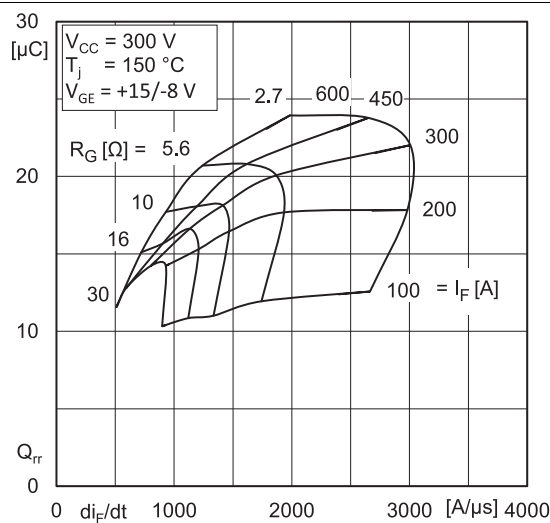
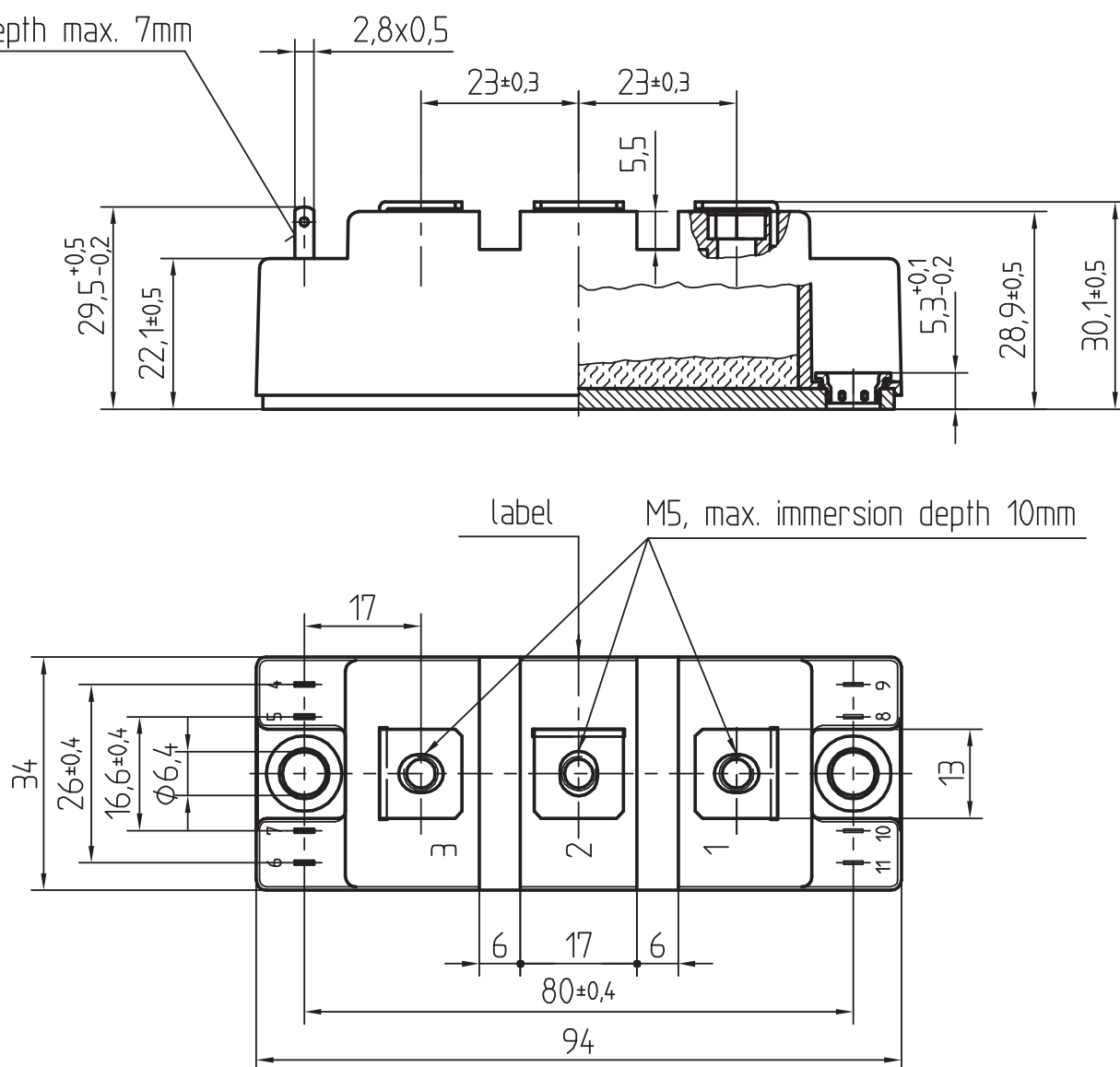


Fig. 12: Typ. CAL diode peak reverse recovery charge

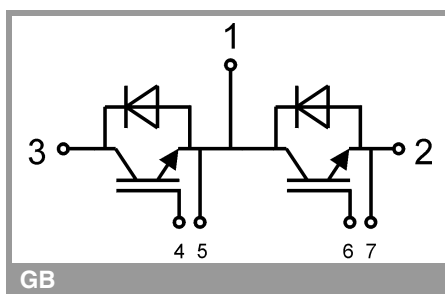
Dimensions in mm

Plug in depth max. 7mm



General tolerance $\pm 0,5$ mm

SEMITRANS 2



This is an electrostatic discharge sensitive device (ESDS), international standard IEC 60747-1, chapter IX.

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