

SEMITRANS® 3

Trench IGBT Modules

SKM300GAL07E3

Target Data

Features

- V_{CE(sat)} with positive temperature coefficient
- High short circuit capability, self limiting to 6 x Icnom
- Fast & soft inverse CAL diodes
- Insulated copper baseplate using DBC Technology (Direct Copper Bonding)
- · With integrated gate resistor

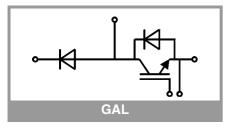
Typical Applications*

- · Electronic welders
- DC/DC converter
- · Brake chopper
- Switched reluctance motor

Remarks

- · Case temperature limited to $T_c = 125$ °C max.
- Recommended T_{op} = -40 ... +150°C
- Product reliability results valid for $T_j = 150$ °C
- Use of soft R_G necessary





Absolute	Maximum Ratin	gs		
Symbol	Conditions		Values	Unit
IGBT				ı ı
V _{CES}	T _j = 25 °C		650	V
Ic	T 175 00	T _c = 25 °C	382	Α
	T _j = 175 °C	T _c = 80 °C	297	Α
I _{Cnom}			300	Α
I _{CRM}	$I_{CRM} = 3xI_{Cnom}$		900	Α
V _{GES}			-20 20	V
t _{psc}	$V_{CC} = 360 \text{ V}$ $V_{GE} \le 15 \text{ V}$ $V_{CES} \le 650 \text{ V}$	T _j = 150 °C	6	μs
Tj			-40 175	°C
Inverse d	liode		<u> </u>	
V _{RRM}	T _j = 25 °C		650	V
I _F	T _j = 175 °C	T _c = 25 °C	335	Α
		T _c = 80 °C	244	Α
I _{Fnom}			300	Α
I _{FRM}	$I_{FRM} = 2xI_{Fnom}$		600	Α
I _{FSM}	$t_p = 10 \text{ ms, sin } 180^{\circ}, T_j = 25 ^{\circ}\text{C}$		2160	Α
Tj			-40 175	°C
Freewhee	eling diode			
V_{RRM}	T _j = 25 °C		650	V
lF	T _j = 175 °C	T _c = 25 °C	335	Α
		T _c = 80 °C	244	Α
I _{Fnom}			300	Α
I _{FRM}	$I_{FRM} = 2xI_{Fnom}$		600	Α
I _{FSM}	$t_p = 10 \text{ ms, sin } 180^{\circ}, T_j = 25 {}^{\circ}\text{C}$		2160	Α
Tj			-40 175	°C
Module				
I _{t(RMS)}			500	Α
T _{stg}	module without TIM		-40 125	°C
V _{isol}	AC sinus 50 Hz,	t = 1 min	4000	V

Characteristics							
Symbol	Conditions	min.	typ.	max.	Unit		
IGBT	•		•				
V _{CE(sat)}	$I_C = 300 \text{ A}$ $V_{GE} = 15 \text{ V}$ chiplevel	T _j = 25 °C		1.45	1.90	V	
		T _j = 150 °C		1.69	2.10	V	
V _{CE0}	chiplevel	T _j = 25 °C		0.90	1.00	V	
		T _j = 150 °C		0.82	0.90	V	
r _{CE}	V _{GE} = 15 V	T _j = 25 °C		1.83	3.0	mΩ	
	chiplevel	T _j = 150 °C		2.9	4.0	mΩ	
$V_{GE(th)}$	$V_{GE}=V_{CE}$, $I_{C}=4.8$ mA		5.1	5.8	6.4	V	
I _{CES}	V _{GE} = 0 V V _{CE} = 650 V	T _j = 25 °C			0.3	mA	
		T _j = 150 °C		-		mA	
C _{ies}	V _{CE} = 25 V V _{GE} = 0 V	f = 1 MHz		18.5		nF	
Coes		f = 1 MHz		1.16		nF	
C _{res}		f = 1 MHz		0.55		nF	
Q _G	V _{GE} = - 8 V+ 15 V			2400		nC	
R _{Gint}	T _j = 25 °C			1.0		Ω	



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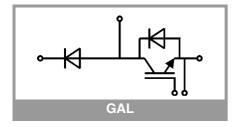
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Remarks

- Case temperature limited to T_c = 125°C max.
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- Product reliability results valid for $T_j = 150$ °C
- Use of soft R_G necessary

Characte	eristics					
Symbol	Conditions		min.	typ.	max.	Unit
t _{d(on)}	$V_{CC} = 300 \text{ V}$	T _j = 150 °C		150		ns
t _r	$I_{\rm C} = 300 {\rm A}$	T _j = 150 °C		50		ns
E _{on}	$V_{GE} = +15/-15 \text{ V}$	T _j = 150 °C		3		mJ
t _{d(off)}		T _j = 150 °C		810		ns
t _f	di/dt _{on} = 7000 A/μs	T _j = 150 °C		67		ns
E _{off}	di/dt _{off} = 4500 A/μs du/dt = 1700 V/μs	T _j = 150 °C		14		mJ
R _{th(j-c)}	per IGBT	<u> </u>			0.15	K/W
R _{th(c-s)}	per IGBT (λ _{grease} =0	.81 W/(m*K))		0.042		K/W
Inverse d	iode		1			I
$V_F = V_{EC}$	I _F = 300 A	T _i = 25 °C		1.40	1.76	V
	$V_{GE} = 0 V$	T _j = 150 °C		1.39	1.77	V
\ <u>\</u>	chiplevel	T _i = 25 °C		1.04		V
V _{F0}	chiplevel	T _i = 150 °C			1.24	V
		$T_i = 150^{\circ} C$		0.85	0.99	-
r _F	chiplevel	$T_i = 25 \text{ C}$ $T_i = 150 \text{ °C}$		1.19	1.76	mΩ
	I _F = 300 A	,		1.79	2.6	mΩ
I _{RRM}	$di/dt_{off} = 5400 \text{ A}/\mu\text{s}$	T _j = 150 °C		313		A
Q _{rr}	$V_{GE} = \pm 15 \text{ V}$	T _j = 150 °C		31.5		μC
Err	V _{CC} = 300 V	T _j = 150 °C		6.4		mJ
R _{th(j-c)}	per diode				0.25	K/W
R _{th(c-s)}	per diode (λ _{grease} =0	.81 W/(m*K))		0.044		K/W
Freewhee	eling diode					
$V_F = V_{EC}$	I _F = 300 A	T _j = 25 °C		1.40	1.76	V
	V _{GE} = 0 V chiplevel	T _j = 150 °C		1.39	1.77	V
V_{F0}	chiplevel	T _j = 25 °C		1.04	1.24	V
		T _j = 150 °C		0.85	0.99	V
r _F	chiployol	T _j = 25 °C		1.19	1.76	mΩ
	chiplevel	T _j = 150 °C		1.79	2.6	mΩ
I _{RRM}	I _F = 300 A	T _j = 150 °C		313		Α
Q _{rr}	$di/dt_{off} = 5400 \text{ A/}\mu\text{s}$	T _j = 150 °C		31.5		μС
Err	$V_{GE} = \pm 15 \text{ V}$ $V_{CC} = 300 \text{ V}$	T _j = 150 °C		6.4		mJ
R _{th(j-c)}	per diode	-			0.25	K/W
R _{th(c-s)}	per diode (λ _{grease} =0	.81 W/(m*K))		0.044		K/W
Module	, grouss -	. "	+			ļ
L _{CE}				15		nН
R _{CC'+EE'}	measured per	T _C = 25 °C		0.55		mΩ
- *OO +EE	switch	T _C = 125 °C		0.85		mΩ
Rth _{(c-s)1}	calculated without thermal coupling (λ_{orease} =0.81 W/(m*K))			0.021		K/W
Rth _{(c-s)2}	including thermal coupling, Ts underneath module (\(\lambda_{\text{grease}} = 0.81 \) W/(m*K))			0.035		K/W
Ms	to heat sink M6		3		5	Nm
Mt		to terminals M6	2.5		5	Nm
						Nm
W		1			325	g
			1			



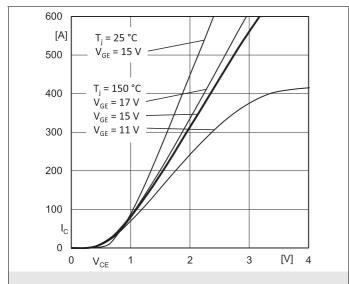


Fig. 1: Typ. output characteristic, inclusive R_{CC'+ EE'}

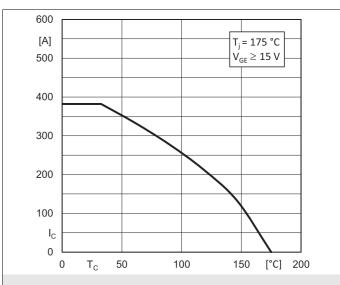


Fig. 2: Rated current vs. temperature $I_C = f(T_C)$

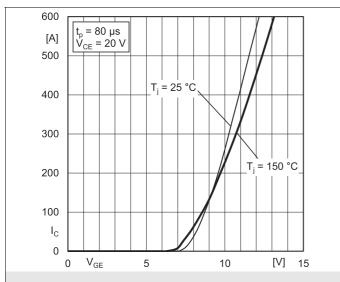


Fig. 5: Typ. transfer characteristic

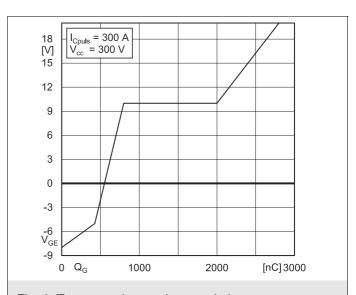


Fig. 6: Typ. gate charge characteristic

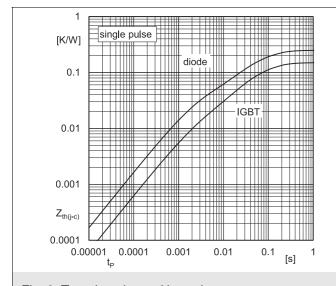


Fig. 9: Transient thermal impedance

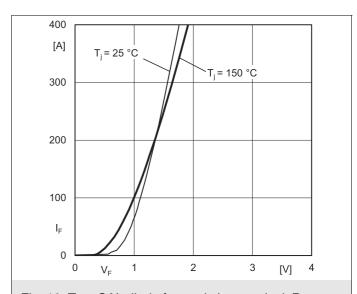
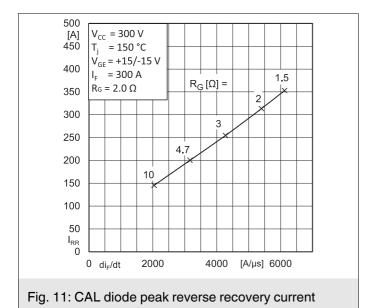


Fig. 10: Typ. CAL diode forward charact., incl. R_{CC'+ EE'}



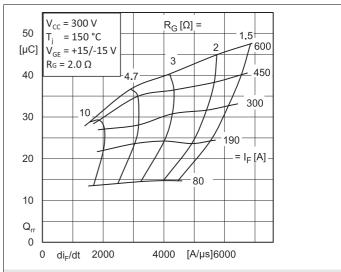
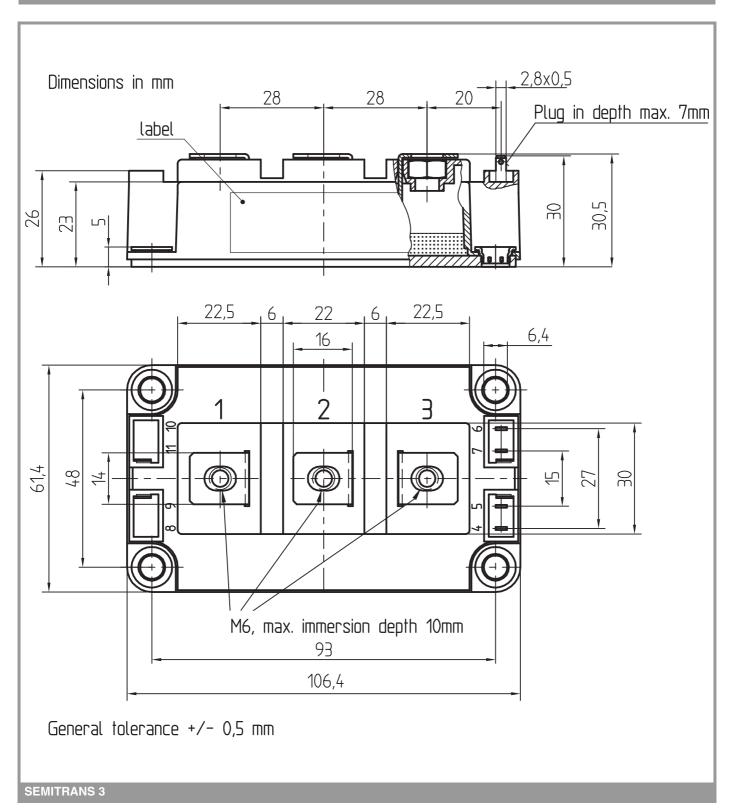
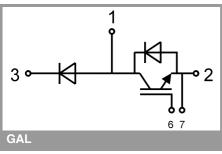


Fig. 12: Typ. CAL diode peak reverse recovery charge





This is an electrostatic discharge sensitive device (ESDS), international standard IEC 60747-1, chapter IX.

*IMPORTANT INFORMATION AND WARNINGS

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