

SEMITRANS® 3

High Speed IGBT4 Modules

SKM400GAR12F4

Features*

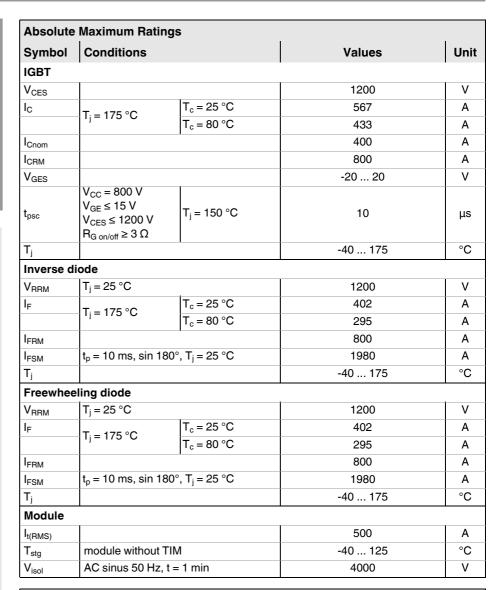
- · High speed trench and field-stop IGBT
- CAL4 ultra-fast = soft switching 4. generation CAL-diode
- Insulated copper baseplate using DBC technology (Direct Bonded Copper)
- · Increased power cycling capability
- For higher switching frequencies above 15kHz
- UL recognized, file no. E63532

Typical Applications

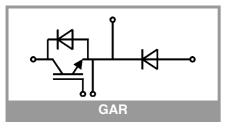
- · Electronic welders
- DC/DC converter
- Brake chopper
- · Switched reluctance motor

Remarks

- Case temperature limited to T_c = 125°C max.
- Recommended $T_{j,op} = -40 \dots +150$ °C
- Product reliability results valid for T_i = 150°C



Characteristics									
Symbol	Conditions	min.	typ.	max.	Unit				
IGBT	•		•						
• C⊏(Sat)	$I_{C} = 400 \text{ A}$ $V_{GE} = 15 \text{ V}$ chiplevel	T _j = 25 °C		2.06	2.42	V			
		T _j = 150 °C		2.59	2.97	V			
V _{CE0}	chiplevel	T _j = 25 °C		1.10	1.28	V			
		T _j = 150 °C		0.95	1.13	V			
r _{CE}	V _{GE} = 15 V chiplevel	T _j = 25 °C		2.4	2.9	mΩ			
		T _j = 150 °C		4.1	4.6	mΩ			
$V_{GE(th)}$	V _{GE} =V _{CE} , I _C = 15.2 mA		5.1	5.8	6.4	V			
I _{CES}	V _{GE} = 0 V V _{CE} = 1200 V	T _j = 25 °C			5	mA			
		T _j = 150 °C		-		mA			
C _{ies}	V _{CE} = 25 V V _{GF} = 0 V	f = 1 MHz		24.6		nF			
C _{oes}		f = 1 MHz		1.62		nF			
C _{res}	VGE - O V	f = 1 MHz		1.38		nF			
Q_G	V _{GE} = - 8 V+ 15 V			2268		nC			
R _{Gint}	T _j = 25 °C			1.6		Ω			





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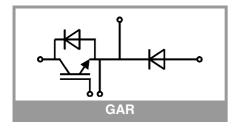
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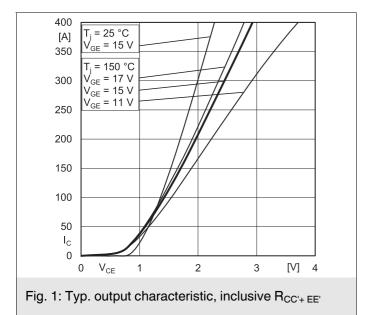
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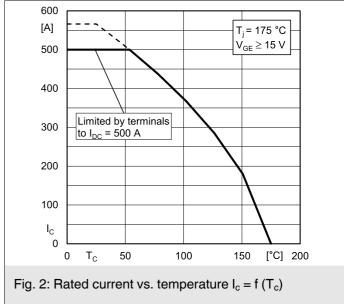
Remarks

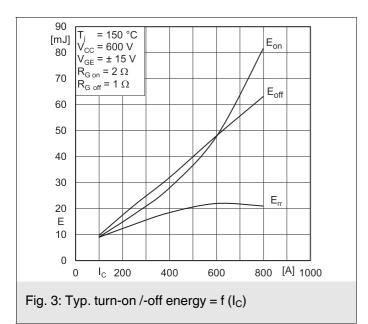
- Case temperature limited to T_c = 125°C max.
- Recommended $T_{j,op} = -40 \dots +150$ °C
- Product reliability results valid for T_i = 150°C

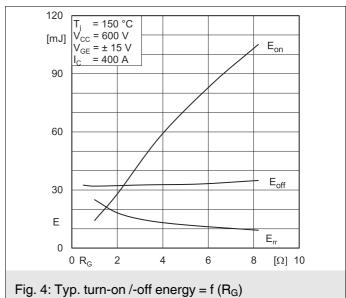
Characteristics										
Symbol	Conditions	min.	typ.	max.	Unit					
t _{d(on)}	V _{CC} = 600 V	T _j = 150 °C		110		ns				
t _r	I _C = 400 A	T _i = 150 °C		55		ns				
E _{on}	$V_{GE} = +15/-15 \text{ V}$	T _i = 150 °C	28			mJ				
	$R_{G \text{ on}} = 2 \Omega$	T _i = 150 °C	415			<u> </u>				
t _{d(off)}	$R_{G \text{ off}} = 1 \Omega$ $di/dt_{on} = 7960 \text{ A/}\mu\text{s}$	·	75			ns				
t _f	$di/dt_{off} = 4430 \text{ A/}\mu\text{s}$	1 _j = 150 C	/5			ns				
E _{off}	dv/dt = 4530 V/μs	T _j = 150 °C	32			mJ				
R _{th(j-c)}	per IGBT				0.068	K/W				
R _{th(c-s)}	per IGBT, P12 (reference)			0.041		K/W				
Inverse di	ode									
$V_F = V_{EC}$	I _F = 400 A	T _j = 25 °C		2.55	2.93	V				
	$V_{GE} = 0 V$	T _i = 150 °C		2.44	2.80	٧				
\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \	chiplevel	,								
V _{F0}	chiplevel	$T_j = 25 ^{\circ}\text{C}$		1.51	1.75	V				
		T _j = 150 °C		1.16	1.40	V				
r _F	chiplevel	T _j = 25 °C		2.6	2.9	mΩ				
	•	T _j = 150 °C		3.2	3.5	mΩ				
I _{RRM}	$I_F = 400 \text{ A}$ di/dt _{off} = 7183 A/µs	T _j = 150 °C		424		Α				
Q _{rr}	$V_{GE} = -15 \text{ V}$	T _j = 150 °C		51		μC				
Err	V _{CC} = 600 V	T _j = 150 °C		18.5		mJ				
R _{th(j-c)}	per diode				0.14	K/W				
R _{th(c-s)}	per diode, P12 (refe	per diode, P12 (reference)		0.047		K/W				
	ling diode					I				
$V_F = V_{EC}$	I _F = 400 A	T _i = 25 °C		2.55	2.93	V				
1 20	$V_{GE} = 0 V$					V				
	chiplevel	T _j = 150 °C		2.44	2.80					
V_{F0}	chiplevel	T _j = 25 °C		1.51	1.75	V				
		T _j = 150 °C		1.16	1.40	V				
r _F	chiplevel	T _j = 25 °C		2.6	2.9	mΩ				
		T _j = 150 °C		3.2	3.5	mΩ				
I _{RRM}	I _F = 400 A	T _j = 150 °C		424		Α				
Q_{rr}	$di/dt_{off} = 7183 \text{ A/}\mu\text{s}$ $V_{GE} = -15 \text{ V}$	T _j = 150 °C		51		μC				
Err	$V_{CC} = 600 \text{ V}$	T _j = 150 °C		18.5		mJ				
R _{th(j-c)}	per diode				0.14	K/W				
R _{th(c-s)}	per diode, P12 (reference)			0.047		K/W				
Module	1. , , , ,	,				<u> </u>				
L _{CE}		Ī		15		nH				
R _{CC'+EE'}	measured per	T _C = 25 °C		0.55		mΩ				
LICC.+EE.	switch	T _C = 125 °C		0.85		mΩ				
R _{th(c-s)1}	calculated without thermal coupling, P12 (reference)		(0.0219		K/W				
R _{th(c-s)2}	including thermal coupling, T _s underneath module, P12 (reference)			0.024		K/W				
Ms	to heat sink M6	, , , , , , , , , , , , , , , , , , , ,	3		5	Nm				
M _t		to terminals M6	2.5		5	Nm				
,	=					Nm				
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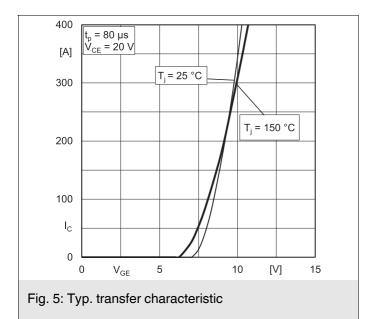


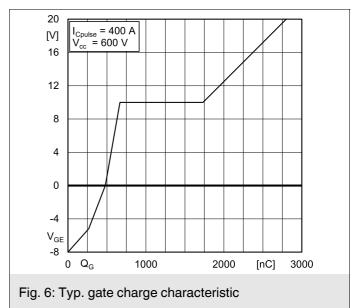


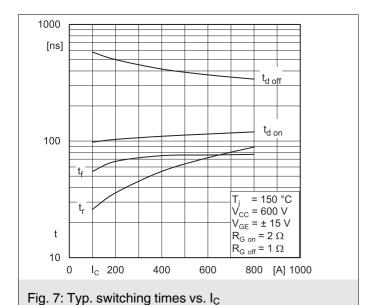


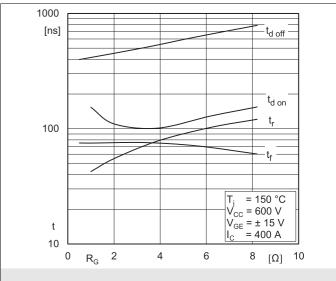


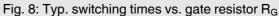












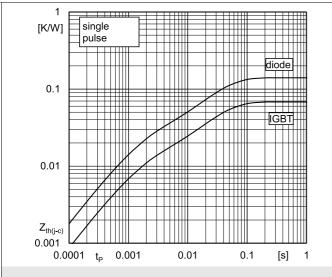


Fig. 9: Transient thermal impedance

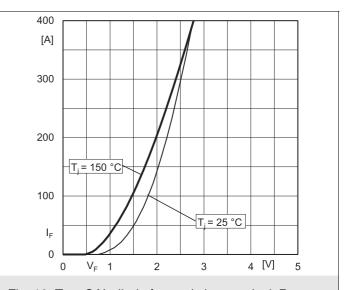


Fig. 10: Typ. CAL diode forward charact., incl. $R_{CC'+\,EE'}$

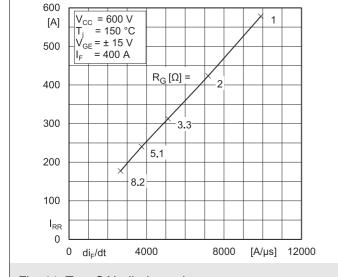


Fig. 11: Typ. CAL diode peak reverse recovery current

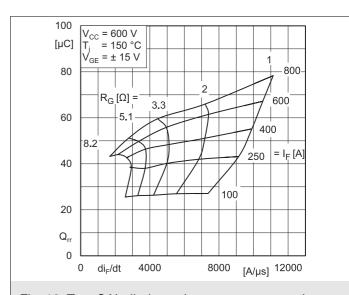


Fig. 12: Typ. CAL diode peak reverse recovery charge

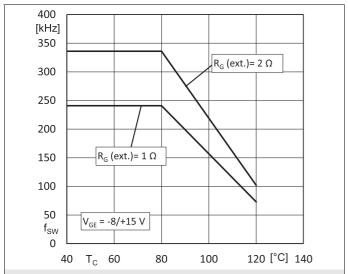
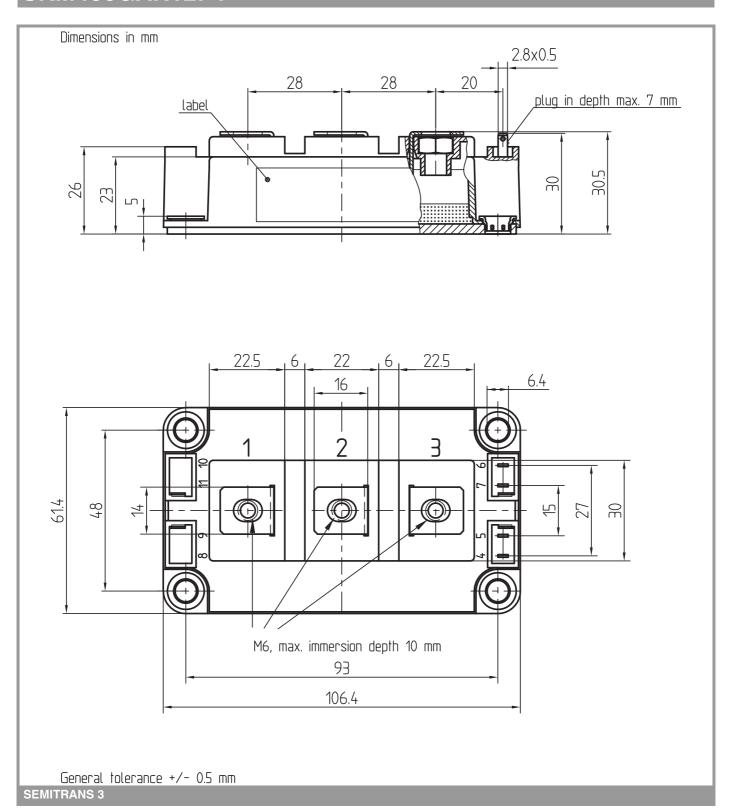
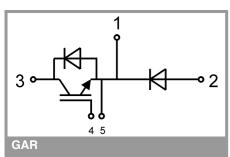


Fig. 13: Max. switching frequency vs. case temperature $f_{\text{sw}} = f(T_{\text{c}})$





IMPORTANT INFORMATION AND WARNINGS

This is an electrostatic discharge sensitive device (ESDS) according to international standard IEC 61340.

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