

IGBT4 Modules

SKM450GB12E4D1

Features*

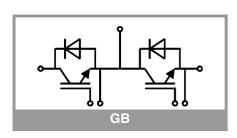
- IGBT4 = 4th generation medium fast trench IGBT (Infineon)
- CAL4 = Soft switching 4th generation CAL-diode
- Insulated copper baseplate using DBC technology (Direct Bonded Copper)
- Increased power cycling capability
- With integrated gate resistor
- For higher switching frequencies up to 12kHz
- UL recognized, file no. E63532
- SKM...**D1**: increased diode performance

Typical Applications

- AC inverter drives
- UPS

Remarks

- Case temperature limited to T_c = 125°C max.
- Recommended T_{op} = -40 ... +150°C
- Product reliability results valid for T_j = 150°C



Absolute	Maximum Ratin	gs		
Symbol	Conditions		Values	Unit
IGBT	•			
V _{CES}	T _j = 25 °C		1200	V
I _C	T _j = 175 °C	T _c = 25 °C	699	Α
		T _c = 80 °C	538	Α
I _{Cnom}			450	Α
I _{CRM}	I _{CRM} = 3 x I _{Cnom}		1350	Α
V_{GES}			-20 20	V
t _{psc}	$V_{CC} = 800 \text{ V}$ $V_{GE} \le 15 \text{ V}$ $V_{CES} \le 1200 \text{ V}$	T _j = 150 °C	10	μs
Tj			-40 175	°C
Inverse d	liode			
V_{RRM}	T _j = 25 °C		1200	V
l _F	T _i = 175 °C	T _c = 25 °C	623	Α
	- 1 j = 1/5 C	T _c = 80 °C	466	Α
I _{Fnom}			500	Α
I _{FRM}	I _{FRM} = 2xI _{Fnom}		1000	Α
I _{FSM}	t _p = 10 ms, sin 180°, T _j = 25 °C		2736	Α
Tj			-40 175	°C
Module				•
I _{t(RMS)}			500	А
T _{stg}	module without TIM		-40 125	°C
V _{isol}	AC sinus 50 Hz, t = 1 min		4000	V

Characteristics								
Symbol	Conditions	min.	typ.	max.	Unit			
IGBT								
V _{CE(sat)}	$V_{CE(sat)}$ $I_C = 450 \text{ A}$ $V_{GE} = 15 \text{ V}$ chiplevel	T _j = 25 °C		1.84	2.07	V		
		T _j = 150 °C		2.23	2.42	V		
V _{CE0}	chiplevel	T _j = 25 °C		0.80	0.90	V		
		T _j = 150 °C		0.70	0.80	V		
r _{CE}	V _{GE} = 15 V	T _j = 25 °C		2.3	2.6	mΩ		
	chiplevel	T _j = 150 °C		3.4	3.6	mΩ		
$V_{\text{GE(th)}}$	$V_{GE} = V_{CE}, I_C = 16.4$	mA	5	5.8	6.5	V		
I _{CES}	$V_{GE} = 0 \text{ V}, V_{CE} = 12$	00 V, T _j = 25 °C			5	mA		
C _{ies}	V 05.V	f = 1 MHz		27.2		nF		
Coes	V _{CE} = 25 V V _{GE} = 0 V	f = 1 MHz		1.76		nF		
C_{res}		f = 1 MHz		1.50		nF		
Q_G	V _{GE} = - 8 V+ 15 V			2500		nC		
R _{Gint}	T _j = 25 °C			1.9		Ω		
t _{d(on)}	$\begin{array}{c} V_{CC} = 600 \ V \\ I_{C} = 450 \ A \\ V_{GE} = +15/-15 \ V \\ R_{G \ on} = 1 \ \Omega \\ R_{G \ off} = 1 \ \Omega \\ di/dt_{on} = 8100 \ A/\mu s \\ di/dt_{off} = 3400 \ A/\mu s \end{array}$	T _j = 150 °C		253		ns		
t _r		T _j = 150 °C		59		ns		
E _{on}		T _j = 150 °C		28		mJ		
t _{d(off)}		T _j = 150 °C		505		ns		
t _f		T _j = 150 °C		112		ns		
E _{off}		T _j = 150 °C		58		mJ		
R _{th(j-c)}	per IGBT				0.062	K/W		
R _{th(c-s)}	per IGBT (λ _{grease} =0.81 W/(m*K))			0.028		K/W		
R _{th(c-s)}	per IGBT, pre-applied phase change material			0.017		K/W		



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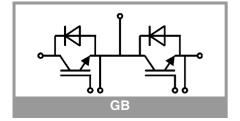
Remarks

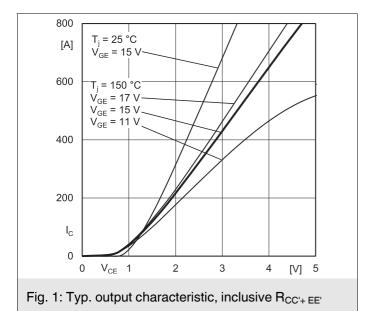
• Case temperature limited to T_c = 125°C max.

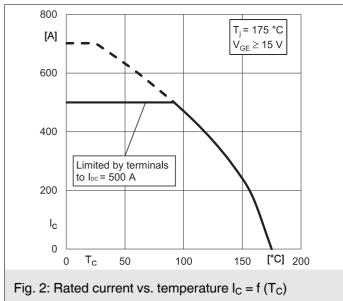
• Recommended T_{op} = -40 ... +150°C

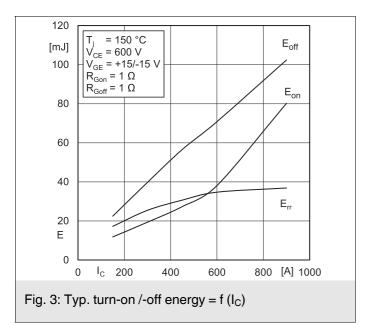
 Product reliability results valid for T_i = 150°C

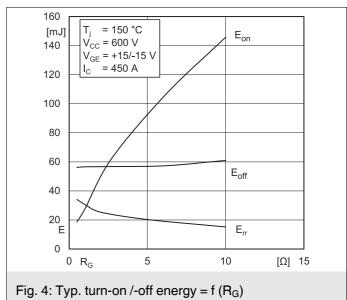
Characteristics									
Symbol	Conditions	min.	typ.	max.	Unit				
Inverse diode									
$V_F = V_{EC}$	$I_F = 450 \text{ A}$ $V_{GE} = 0 \text{ V}$ chiplevel	T _j = 25 °C		2.04	2.35	V			
		T _j = 150 °C		1.94	2.23	V			
V_{F0}	chiplevel	T _j = 25 °C		1.30	1.50	V			
	Chipievei	T _j = 150 °C		0.90	1.10	V			
r _F	chiplevel	T _j = 25 °C		1.64	1.88	$m\Omega$			
	·	T _j = 150 °C		2.3	2.5	mΩ			
I _{RRM}	$I_F = 450 \text{ A}$ di/dt _{off} = 8000 A/µs $V_{GE} = -15 \text{ V}$	T _j = 150 °C		504		Α			
Q_{rr}		T _j = 150 °C		75		μC			
E _{rr}	V _{CC} = 600 V	T _j = 150 °C		31		mJ			
R _{th(j-c)}	per diode			0.095	K/W				
R _{th(c-s)}	per diode (λ _{grease} =0.81 W/(m*K))			0.037		K/W			
R _{th(c-s)}	per diode, pre-applied phase change material			0.03		K/W			
Module									
L _{CE}				15		nΗ			
R _{CC'+EE'}	measured per	T _C = 25 °C		0.55		mΩ			
	switch	T _C = 125 °C		0.85		$m\Omega$			
$R_{th(c-s)1}$	calculated without thermal coupling			0.008		K/W			
R _{th(c-s)2}	including thermal coupling, Ts underneath module (λ _{grease} =0.81 W/(m*K))			0.013		K/W			
$R_{\text{th(c-s)2}}$	including thermal coupling, Ts underneath module, pre-applied phase change material			0.009		K/W			
M_s	to heat sink M6		3		5	Nm			
Mt		to terminals M6	2.5		5	Nm			
						Nm			
W					325	g			

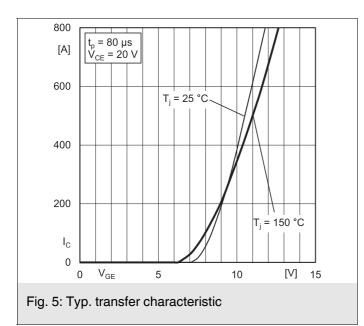


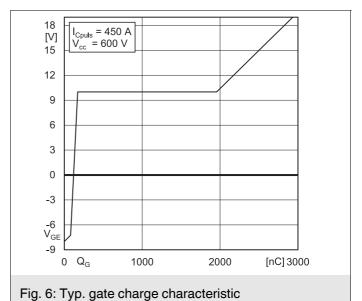












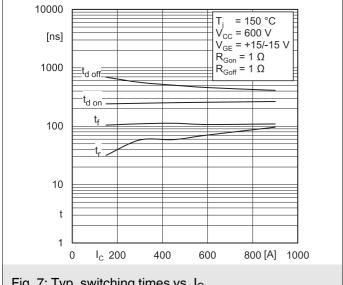


Fig. 7: Typ. switching times vs. I_C

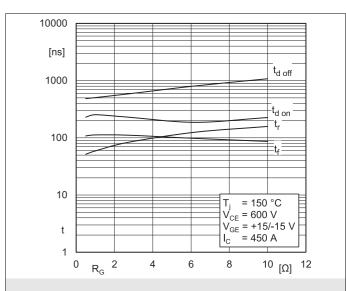


Fig. 8: Typ. switching times vs. gate resistor R_G

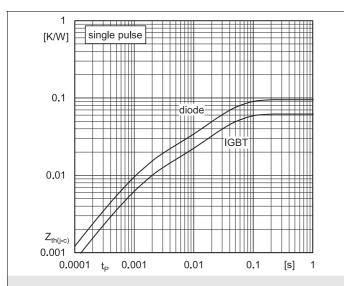


Fig. 9: Transient thermal impedance

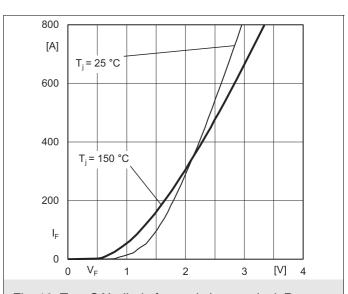


Fig. 10: Typ. CAL diode forward charact., incl. R_{CC'+ EE'}

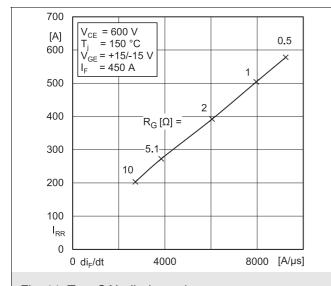


Fig. 11: Typ. CAL diode peak reverse recovery current

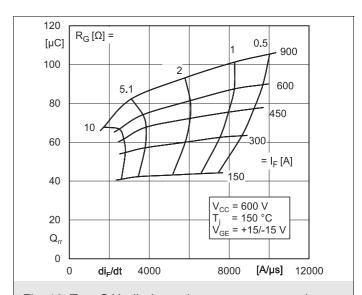
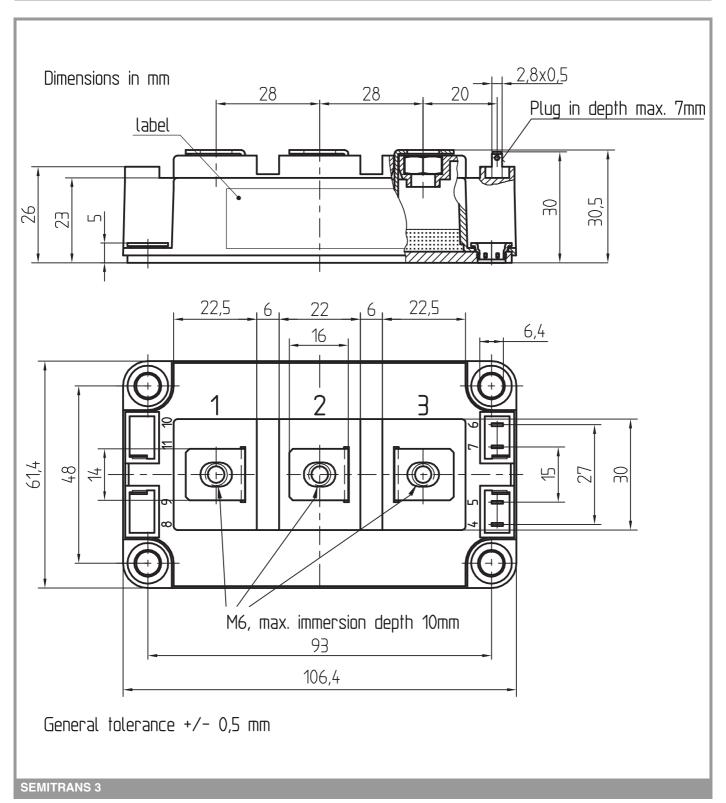
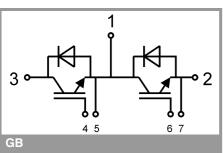


Fig. 12: Typ. CAL diode peak reverse recovery charge





This is an electrostatic discharge sensitive device (ESDS), international standard IEC 60747-1, chapter IX.

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