

# SEMITRANS® 3

### **IGBT4** Modules

### SKM300GB17E4

### Features\*

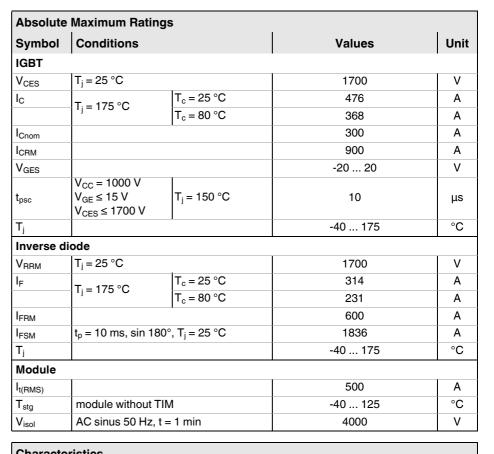
- IGBT4 = 4th generation medium fast trench IGBT (Infineon)
- CAL4 = Soft switching 4th generation CAL-Diode
- Insulated copper baseplate using DBC Technology (Direct Copper Bonding)
- With integrated Gate resistor
- For switching frequencies up to 8kHz
- UL recognized, file no. E63532

### **Typical Applications**

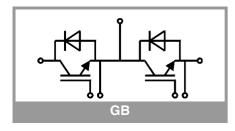
- · AC inverter drives
- UPS
- Electronic welders
- · Wind power
- · Public transport

#### **Remarks**

- Case temperature limited to T<sub>C</sub> = 125°C max.
- Recommended  $T_{j,op} = -40 \dots +150$ °C
- Product reliability results valid for T<sub>j</sub> = 150°C



| Characteristics      |   |                         |       |       |       |      |  |  |  |
|----------------------|---|-------------------------|-------|-------|-------|------|--|--|--|
| Symbol               | Conditions  |                         | min.  | typ.  | max.  | Unit |  |  |  |
| IGBT                 |   |                         | •     |       |       | •    |  |  |  |
| V <sub>CE(sat)</sub> | I <sub>C</sub> = 300 A  | T <sub>j</sub> = 25 °C  |       | 1.91  | 2.20  | V    |  |  |  |
|                      | V <sub>GE</sub> = 15 V<br>chiplevel   | T <sub>j</sub> = 150 °C |       | 2.29  | 2.60  | V    |  |  |  |
| V <sub>CE0</sub>     | chiplevel   | T <sub>j</sub> = 25 °C  |       | 0.80  | 0.90  | V    |  |  |  |
|                      |   | T <sub>j</sub> = 150 °C |       | 0.70  | 0.80  | V    |  |  |  |
| r <sub>CE</sub>      | V <sub>GE</sub> = 15 V<br>chiplevel   | T <sub>j</sub> = 25 °C  |       | 3.7   | 4.3   | mΩ   |  |  |  |
|                      |   | T <sub>j</sub> = 150 °C |       | 5.3   | 6.0   | mΩ   |  |  |  |
| V <sub>GE(th)</sub>  | $V_{GE}=V_{CE}$ , $I_{C}=12$ mA   |                         | 5.2   | 5.8   | 6.4   | V    |  |  |  |
| I <sub>CES</sub>     | $V_{GE} = 0 \text{ V}, V_{CE} = 17$   |                         |       | 4.0   | mA    |      |  |  |  |
| C <sub>ies</sub>     | V <sub>CE</sub> = 25 V<br>V <sub>GE</sub> = 0 V   | f = 1 MHz               |       | 27.2  |       | nF   |  |  |  |
| Coes                 |   | f = 1 MHz               |       | 1.06  |       | nF   |  |  |  |
| C <sub>res</sub>     |   | f = 1 MHz               |       | 0.88  |       | nF   |  |  |  |
| Q <sub>G</sub>       | V <sub>GE</sub> = - 8 V+ 15 V   |                         |       | 2400  |       | nC   |  |  |  |
| R <sub>Gint</sub>    | T <sub>j</sub> = 25 °C  |                         |       | 2.1   |       | Ω    |  |  |  |
| t <sub>d(on)</sub>   | $V_{CC} = 1200 \text{ V}$ $I_{C} = 300 \text{ A}$ $V_{GE} = +15/-15 \text{ V}$ $R_{G \text{ on}} = 2 \Omega$ $R_{G \text{ off}} = 2 \Omega$ $di/dt_{on} = 10200 \text{ A}/$ | T <sub>j</sub> = 150 °C |       | 207   |       | ns   |  |  |  |
| t <sub>r</sub>       |   | T <sub>j</sub> = 150 °C |       | 37.5  |       | ns   |  |  |  |
| E <sub>on</sub>      |   | T <sub>j</sub> = 150 °C |       | 88    |       | mJ   |  |  |  |
| t <sub>d(off)</sub>  |   | T <sub>j</sub> = 150 °C |       | 756   |       | ns   |  |  |  |
| t <sub>f</sub>       |   | T <sub>j</sub> = 150 °C |       | 154   |       | ns   |  |  |  |
| E <sub>off</sub>     | $\mu s$ $di/dt_{off} = 1500 \text{ A/}\mu s$ $dv/dt = 4500 \text{ V/}\mu s$   | T <sub>j</sub> = 150 °C |       | 121   |       | mJ   |  |  |  |
| R <sub>th(j-c)</sub> | per IGBT  |                         |       |       | 0.083 | K/W  |  |  |  |
| $R_{th(c-s)}$        | per IGBT, P12 (reference)   |                         |       | 0.040 |       | K/W  |  |  |  |
| R <sub>th(c-s)</sub> | per IGBT, HP-PCM  |                         | 0.022 |       | K/W   |      |  |  |  |





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• IGBT4 = 4th generation medium fast trench IGBT (Infineon)

• CAL4 = Soft switching 4th generation **CAL-Diode** 

 Insulated copper baseplate using DBC Technology (Direct Copper Bonding)

• With integrated Gate resistor

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· AC inverter drives

• UPS

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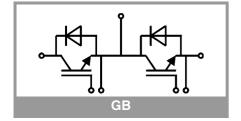
**Remarks** 

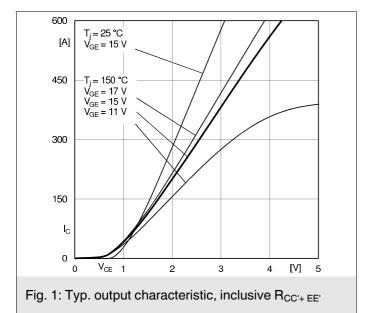
• Case temperature limited to T<sub>C</sub> = 125°C

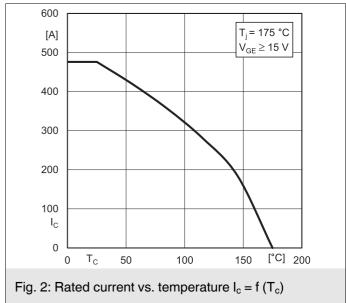
Recommended  $T_{j,op}$  = -40 ... +150°C

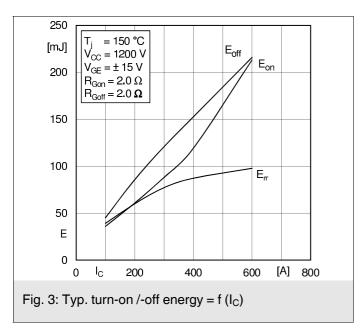
Product reliability results valid for T<sub>i</sub> = 150°C

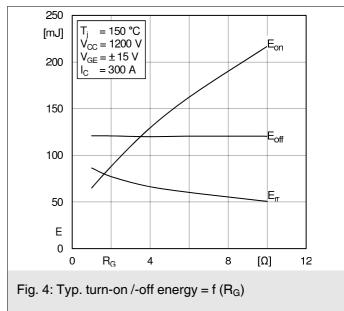
| Characteristics       |   |                         |        |       |      |     |  |  |  |  |
|-----------------------|---|-------------------------|--------|-------|------|-----|--|--|--|--|
| Symbol                | Conditions  | min.                    | typ.   | max.  | Unit |     |  |  |  |  |
| Inverse diode         |   |                         |        |       |      |     |  |  |  |  |
| $V_F = V_{EC}$        | I <sub>F</sub> = 300 A  | T <sub>j</sub> = 25 °C  |        | 2.00  | 2.40 | V   |  |  |  |  |
|                       | V <sub>GE</sub> = 0 V<br>chiplevel  | T <sub>j</sub> = 150 °C |        | 2.13  | 2.56 | V   |  |  |  |  |
| $V_{F0}$              | chiplevel   | T <sub>j</sub> = 25 °C  |        | 1.32  | 1.56 | V   |  |  |  |  |
|                       |   | T <sub>j</sub> = 150 °C |        | 1.08  | 1.22 | V   |  |  |  |  |
| r <sub>F</sub>        | chiplevel   | T <sub>j</sub> = 25 °C  |        | 2.3   | 2.8  | mΩ  |  |  |  |  |
|                       | Criipievei  | T <sub>j</sub> = 150 °C |        | 3.5   | 4.5  | mΩ  |  |  |  |  |
| I <sub>RRM</sub>      | I <sub>F</sub> = 300 A<br>di/dt <sub>off</sub> = 8600 A/μs<br>V <sub>GE</sub> = -15 V | T <sub>j</sub> = 150 °C |        | 489   |      | Α   |  |  |  |  |
| $Q_{rr}$              |   | T <sub>j</sub> = 150 °C |        | 102   |      | μC  |  |  |  |  |
| E <sub>rr</sub>       | V <sub>CC</sub> = 1200 V  | T <sub>j</sub> = 150 °C |        | 77    |      | mJ  |  |  |  |  |
| R <sub>th(j-c)</sub>  | per diode   |                         |        | 0.19  | K/W  |     |  |  |  |  |
| R <sub>th(c-s)</sub>  | per diode, P12 (refe  |                         | 0.047  |       | K/W  |     |  |  |  |  |
| R <sub>th(c-s)</sub>  | per diode, HP-PCM   |                         |        | 0.030 |      | K/W |  |  |  |  |
| Module                |   |                         |        |       |      |     |  |  |  |  |
| L <sub>CE</sub>       |   |                         | 15     |       | nH   |     |  |  |  |  |
| R <sub>CC'+EE'</sub>  | measured per switch   | T <sub>C</sub> = 25 °C  |        | 0.55  |      | mΩ  |  |  |  |  |
|                       |   | T <sub>C</sub> = 125 °C |        | 0.85  |      | mΩ  |  |  |  |  |
| R <sub>th(c-s)1</sub> | calculated without t<br>P12 (reference)   |                         | 0.0108 |       | K/W  |     |  |  |  |  |
| R <sub>th(c-s)2</sub> | including thermal co<br>T <sub>s</sub> underneath mod                                 |                         | 0.0177 |       | K/W  |     |  |  |  |  |
| R <sub>th(c-s)2</sub> | including thermal $c_s$ underneath mod  |                         | 0.0104 |       | K/W  |     |  |  |  |  |
| Ms                    | to heat sink M6   |                         | 3      |       | 5    | Nm  |  |  |  |  |
| M <sub>t</sub>        |   | to terminals M6         | 2.5    |       | 5    | Nm  |  |  |  |  |
|                       |   |                         |        | -     |      | Nm  |  |  |  |  |
| W                     |   |                         |        |       | 325  | g   |  |  |  |  |

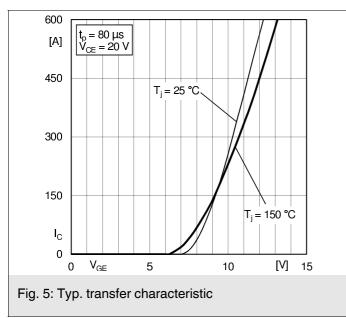


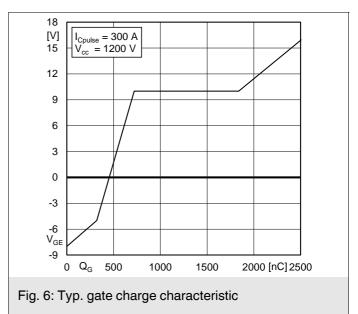


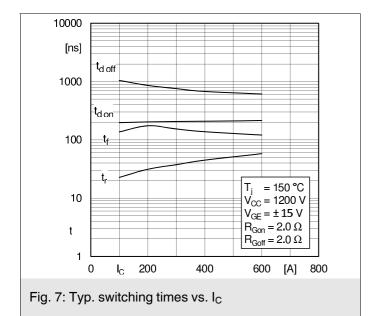


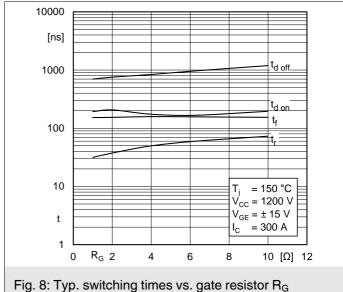


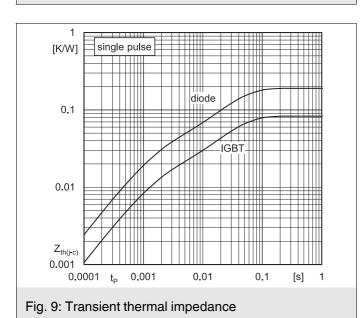


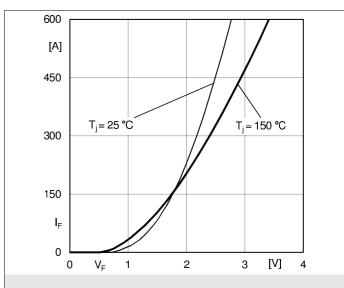


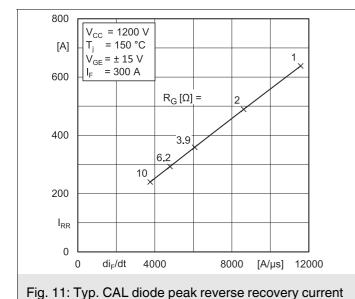


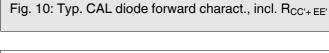












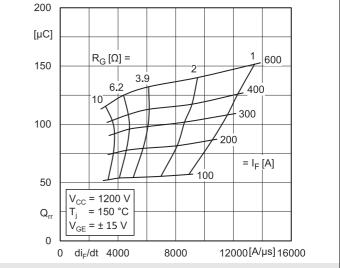
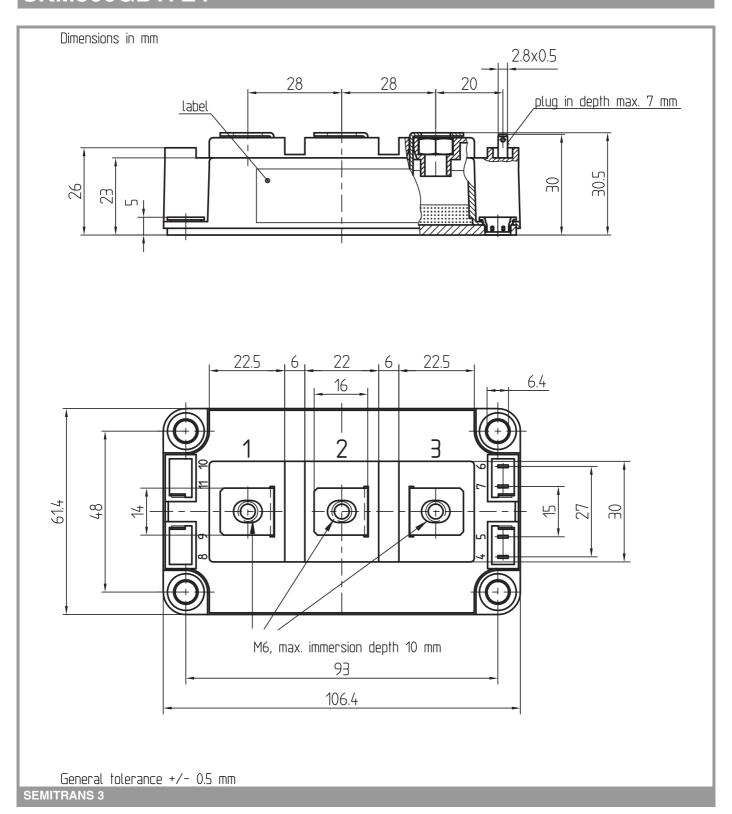
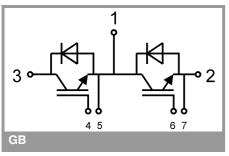


Fig. 12: Typ. CAL diode peak reverse recovery charge





#### **IMPORTANT INFORMATION AND WARNINGS**

This is an electrostatic discharge sensitive device (ESDS) according to international standard IEC 61340.

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