

V Series IGBT Module

SKM200GAL12VL2

Features*

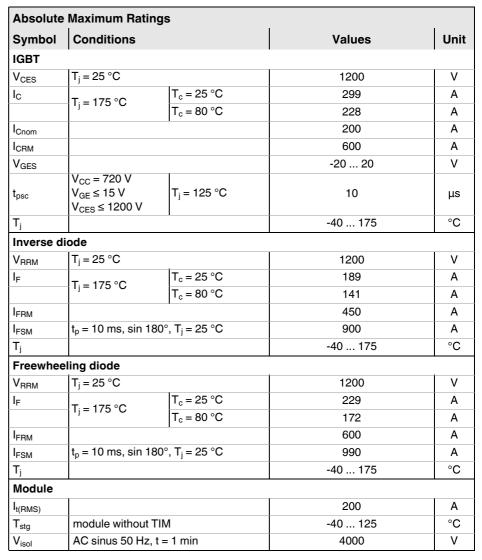
- V-IGBT = 6. Generation Trench V-IGBT (Fuji)
- CAL4 = Soft switching 4. Generation CAL-diode
- Insulated copper baseplate using DBC technology (Direct Bonded Copper)
- · Increased power cycling capability
- · With integrated gate resistor
- UL recognized, file no. E63532
- Lowest switching losses at High di/dt

Typical Applications

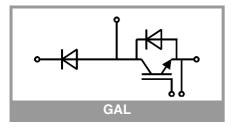
- Electronic welders
- DC/DC converter
- · Brake chopper
- · Switched reluctance motor

Remarks

- Case temperature limited to T_c = 125°C max.
- Recommended $T_{op} = -40 \dots +150$ °C
- Product reliability results valid for T_j = 150°C



| Characte | eristics | | | | | |
|-------------------|---|-------------------------|------|------|------|------|
| Symbol | Conditions | | min. | typ. | max. | Unit |
| IGBT | • | | | | | |
| VG | I _C = 200 A | T _j = 25 °C | | 1.86 | 2.30 | V |
| | V _{GE} = 15 V chiplevel | T _j = 150 °C | | 2.20 | 2.66 | V |
| V _{CE0} | chiplevel | T _j = 25 °C | | 0.94 | 1.07 | V |
| | | T _j = 150 °C | | 0.88 | 0.98 | V |
| r _{CE} | V _{GE} = 15 V | T _j = 25 °C | | 4.6 | 6.2 | mΩ |
| | chiplevel | T _j = 150 °C | | 6.6 | 8.4 | mΩ |
| $V_{GE(th)}$ | $V_{GE}=V_{CE}$, $I_{C}=8$ mA | | 5.5 | 6 | 6.5 | V |
| I _{CES} | V _{GE} = 0 V, V _{CE} = 1200 V, T _j = 25 °C | | | | 0.3 | mA |
| C _{ies} | V _{CE} = 25 V V _{GE} = 0 V | f = 1 MHz | | 12.0 | | nF |
| Coes | | f = 1 MHz | | 1.18 | | nF |
| C _{res} | | f = 1 MHz | | 1.18 | | nF |
| Q_G | V _{GE} = - 8 V+ 15 V | | | 2210 | | nC |
| R _{Gint} | T _j = 25 °C | | | 3.8 | | Ω |





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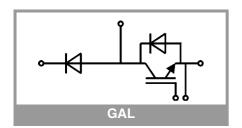
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| Symbol | Conditions | | min. | typ. | max. | Unit |
| IGBT | | | | | | |
| t _{d(on)} | V _{CC} = 600 V | T _i = 150 °C | | 305 | | ns |
| t _r | $I_{\rm C} = 200 {\rm A}$ | T _i = 150 °C | | 51 | | ns |
| E _{on} | $V_{GE} = +15/-15 \text{ V}$ $R_{G \text{ on}} = 1 \Omega$ | T _j = 150 °C | | 24 | | mJ |
| t _{d(off)} | $R_{G \text{ off}} = 1 \Omega$ | T _j = 150 °C | | 493 | | ns |
| t _f | $di/dt_{on} = 4500 A/\mu s$ | T _j = 150 °C | | 88 | | ns |
| E _{off} | di/dt _{off} = 2060 A/μs dv/dt = 5400 V/μs | T _j = 150 °C | | 22 | | mJ |
| R _{th(j-c)} | per IGBT | | ! | | 0.14 | K/W |
| R _{th(c-s)} | per IGBT (λ _{grease} =0.81 W/(m*K)) | | | 0.056 | | K/W |
| R _{th(c-s)} | per IGBT, pre-applied phase change material | | | 0.038 | | K/W |
| Inverse d | iode | | • | | | |
| $V_F = V_{EC}$ | I _F = 150 A | T _j = 25 °C | | 2.14 | 2.46 | V |
| | V _{GE} = 0 V chiplevel | T _j = 150 °C | | 2.04 | 2.38 | V |
| V _{F0} | chiplevel | T _i = 25 °C | | 1.30 | 1.50 | V |
| 10 | | T _i = 150 °C | | 0.90 | 1.10 | V |
| r _F | chiplevel | T _i = 25 °C | | 5.6 | 6.4 | mΩ |
| -1 | | T _i = 150 °C | ! | 7.6 | 8.7 | mΩ |
| I _{RRM} | I _F = 150 A | T _i = 150 °C | ! | 92 | | Α |
| Q _{rr} | $di/dt_{off} = 2250 \text{ A/}\mu\text{s}$ | T _i = 150 °C | | 25 | | μC |
| E _{rr} | V _{GE} = -15 V V _{CC} = 600 V | T _i = 150 °C | | 8.5 | | mJ |
| R _{th(j-c)} | per diode | | | | 0.31 | K/W |
| R _{th(c-s)} | per diode (λ _{grease} =0.81 W/(m*K)) | | | 0.07 | | K/W |
| R _{th(c-s)} | per diode, pre-applied phase change material | | | 0.063 | | K/W |
| Freewhee | ling diode | | • | | | • |
| $V_F = V_{EC}$ | I _F = 200 A | T _j = 25 °C | | 2.20 | 2.52 | V |
| | V _{GE} = 0 V chiplevel | T _j = 150 °C | | 2.16 | 2.47 | V |
| V _{F0} | | T _i = 25 °C | | 1.30 | 1.50 | V |
| | chiplevel | T _i = 150 °C | | 0.90 | 1.10 | V |
| r _F | chiplevel | T _i = 25 °C | | 4.5 | 5.1 | mΩ |
| | | T _j = 150 °C | | 6.3 | 6.9 | mΩ |
| I _{RRM} | I _F = 200 A | T _j = 150 °C | | 170 | | Α |
| Q _{rr} | di/dt _{off} = 3950 A/μs | T _j = 150 °C | | 33 | | μC |
| E _{rr} | $V_{GE} = -15 \text{ V}$ $V_{CC} = 600 \text{ V}$ | T _j = 150 °C | | 13 | | mJ |
| R _{th(j-c)} | per diode | l | | | 0.26 | K/W |
| R _{th(c-s)} | per diode (λ_{grease} =0.81 W/(m*K)) | | | 0.068 | | K/W |
| R _{th(c-s)} | per diode, pre-appl material | | 0.061 | | K/W | |





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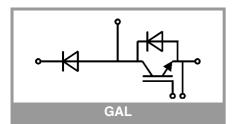
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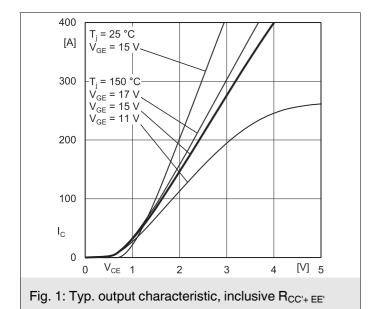
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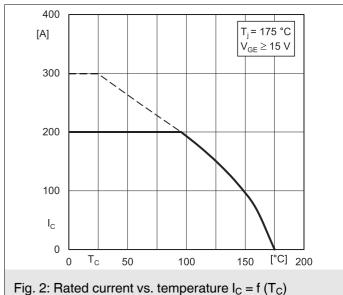
Remarks

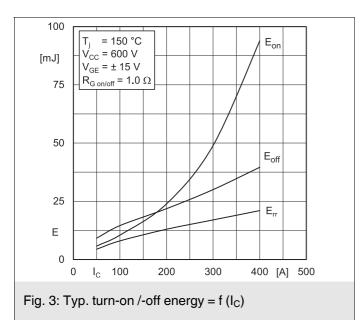
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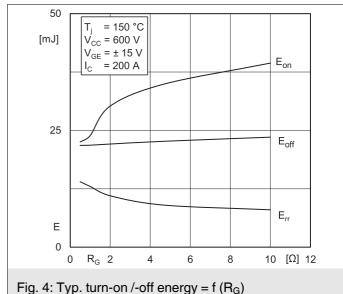
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|-----------------------|--|-------------------------|--------|-------|------|-----|
| Symbol | Conditions | min. | typ. | max. | Unit | |
| Module | | | • | | | |
| L _{CE} | | | 30 | | | nΗ |
| R _{CC'+EE'} | measured per switch | T _C = 25 °C | 0.65 | | | mΩ |
| | | T _C = 125 °C | | 1.09 | | mΩ |
| R _{th(c-s)1} | calculated without thermal coupling (\(\lambda_{\text{grease}} = 0.81 \text{ W/(m*K)}\) | | 0.0311 | | | K/W |
| R _{th(c-s)2} | including thermal coupling, T _s underneath module (\(\lambda_{\text{grease}} = 0.81 \) W/(m*K)) | | 0.034 | | | K/W |
| R _{th(c-s)2} | including thermal coupling, T _s underneath module, pre-applied phase change material | | | 0.026 | | K/W |
| Ms | to heat sink M6 | | 3 | | 5 | Nm |
| Mt | | to terminals M5 | 2.5 | | 5 | Nm |
| |] | | | | | Nm |
| W | | • | | | 160 | g |

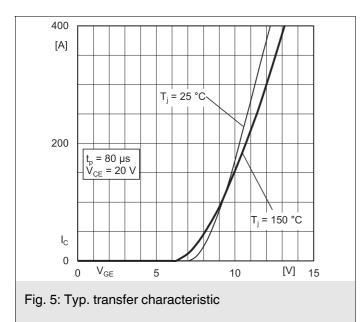


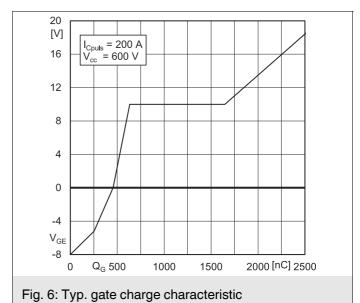


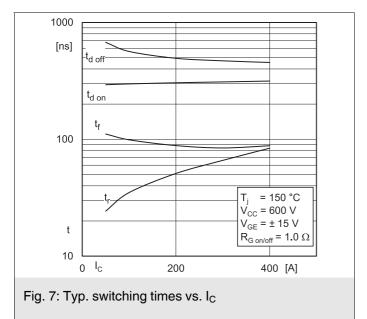


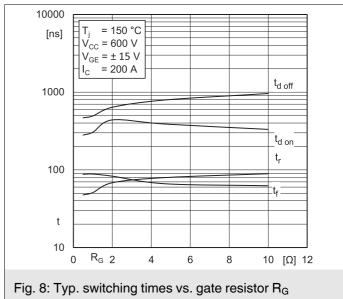


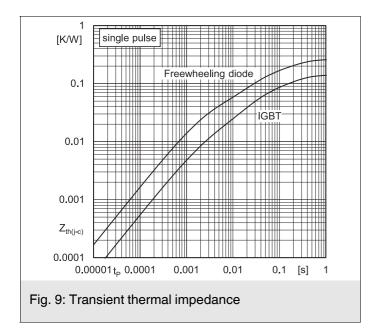


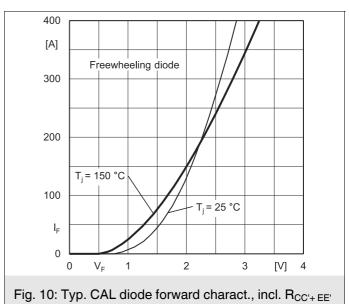




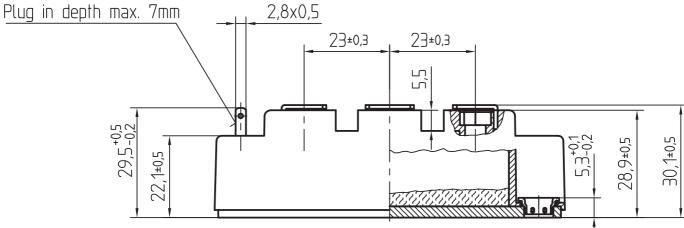


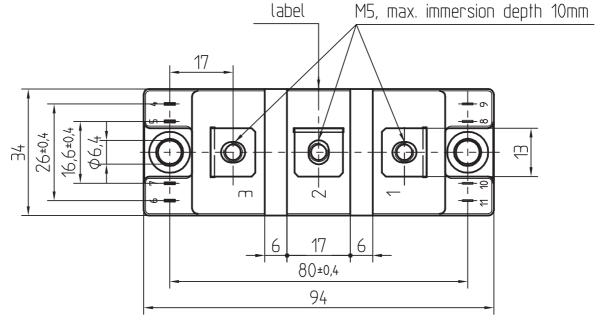






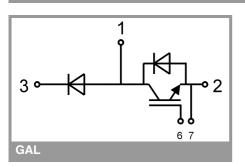






General tolerance +/- 0,5 mm

SEMITRANS 2



This is an electrostatic discharge sensitive device (ESDS), international standard IEC 60747-1, chapter IX.

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